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# SPE Runtime Management Library

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Version 2.0

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# Chapter 1

## Overview

The libspe2 functionality is split into 4 libraries:

- **libspe-base** This library provides the basic infrastructure to manage and use SPEs. The central data structure is a SPE context `spe_context`. It contains all information necessary to manage an SPE, run code on it, communicate with it, and so on. To use the libspe-base library, the header file `spebase.h` has to be included and an application needs to link against `libspebase.a` or `libspebase.so`.
- **libspe-event** This is a convenience library for the handling of events generated by an SPE. It is based on libspe-base and epoll. Since the `spe_context` introduced by libspe-base contains the file descriptors to mailboxes etc, any other event handling mechanism could also be implemented based on libspe-base.

### 1.1 Terminology

- **main thread** usually the application main thread running on a PPE
- **SPE thread** a thread that uses SPEs. Execution starts on the PPE. Execution shifts between PPE and an SPE back and fro, e.g., PPE services system calls for SPE transparently

### 1.2 Usage Scenarios

#### 1.2.1 Single-threaded sample

Note: In the new model, it is not necessary to have a main thread - the SPE thread can be the only application thread. It may run parts of its code on PPE and then start an SPE, e.g., for an accelerated function. The main thread is needed only if you want to use multiple SPEs concurrently. The following minimalistic sample illustrates the basic steps:

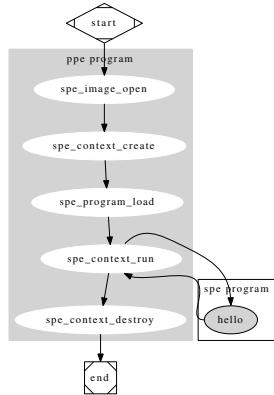


Figure 1.1: Simple program

Here is the same sample with some error checking:

### 1.2.2 Multi-threaded sample

This illustrates a threaded sample using the pthread library:

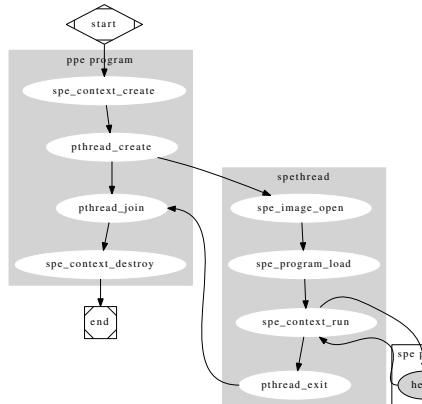


Figure 1.2: Simple pthread program

Here is the same sample with some error checking:

### 1.2.3 Problem state mapping samples

This illustrates accessing the MFC Local Store Address Register.

### 1.2.4 Event samples

This illustrates a sample using the event library. The event, which we receive is of course that the spu program has stopped, because otherwise we would not get there.

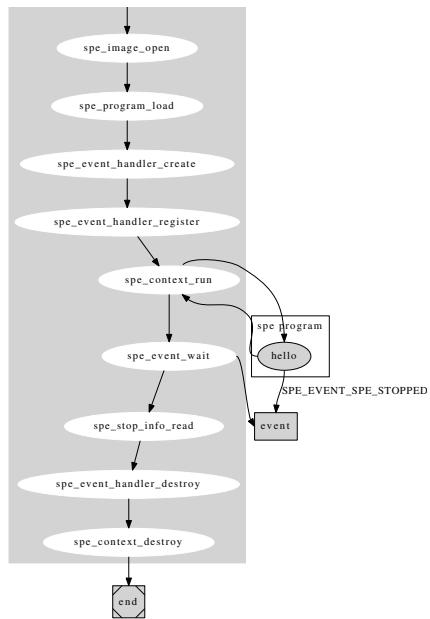


Figure 1.3: Simple event program

Events are more useful in multithreaded environments:

## Chapter 2

# Data Structure Documentation

### 2.1 addr64 Union Reference

```
#include <elf_loader.h>
```

#### Data Fields

- unsigned long long [ull](#)
- unsigned int [ui](#) [2]

#### 2.1.1 Detailed Description

Definition at line 28 of file elf\_loader.h.

#### 2.1.2 Field Documentation

##### 2.1.2.1 unsigned int ui[2]

Definition at line 31 of file elf\_loader.h.

Referenced by [\\_base\\_spe\\_context\\_run\(\)](#).

##### 2.1.2.2 unsigned long long ull

Definition at line 30 of file elf\_loader.h.

Referenced by [\\_base\\_spe\\_context\\_run\(\)](#).

The documentation for this union was generated from the following file:

- [elf\\_loader.h](#)

### 2.2 fd\_attr Struct Reference

## Data Fields

- const char \* [name](#)
- int [mode](#)

### 2.2.1 Detailed Description

Definition at line 37 of file [create.c](#).

### 2.2.2 Field Documentation

#### 2.2.2.1 int mode

Definition at line 39 of file [create.c](#).

Referenced by [\\_base\\_spe\\_open\\_if\\_closed\(\)](#).

#### 2.2.2.2 const char\* name

Definition at line 38 of file [create.c](#).

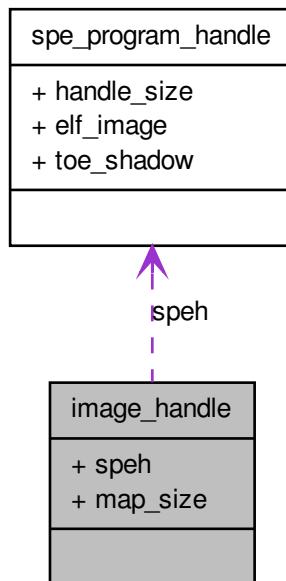
Referenced by [\\_base\\_spe\\_open\\_if\\_closed\(\)](#).

The documentation for this struct was generated from the following file:

- [create.c](#)

## 2.3 image\_handle Struct Reference

Collaboration diagram for image\_handle:



### Data Fields

- [spe\\_program\\_handle\\_t speh](#)
- [unsigned int map\\_size](#)

#### 2.3.1 Detailed Description

Definition at line 32 of file image.c.

#### 2.3.2 Field Documentation

##### 2.3.2.1 unsigned int map\_size

Definition at line 34 of file image.c.

Referenced by `_base_spe_image_close()`, and `_base_spe_image_open()`.

##### 2.3.2.2 spe\_program\_handle\_t speh

Definition at line 33 of file image.c.

Referenced by `_base_spe_image_close()`, and `_base_spe_image_open()`.

The documentation for this struct was generated from the following file:

- [image.c](#)

## 2.4 mfc\_command\_parameter\_area Struct Reference

```
#include <dma.h>
```

### Data Fields

- `uint32_t pad`
- `uint32_t lsa`
- `uint64_t ea`
- `uint16_t size`
- `uint16_t tag`
- `uint16_t class`
- `uint16_t cmd`

### 2.4.1 Detailed Description

Definition at line 27 of file dma.h.

### 2.4.2 Field Documentation

#### 2.4.2.1 `uint16_t class`

Definition at line 33 of file dma.h.

#### 2.4.2.2 `uint16_t cmd`

Definition at line 34 of file dma.h.

#### 2.4.2.3 `uint64_t ea`

Definition at line 30 of file dma.h.

#### 2.4.2.4 `uint32_t lsa`

Definition at line 29 of file dma.h.

#### 2.4.2.5 `uint32_t pad`

Definition at line 28 of file dma.h.

**2.4.2.6 uint16\_t size**

Definition at line 31 of file dma.h.

**2.4.2.7 uint16\_t tag**

Definition at line 32 of file dma.h.

The documentation for this struct was generated from the following file:

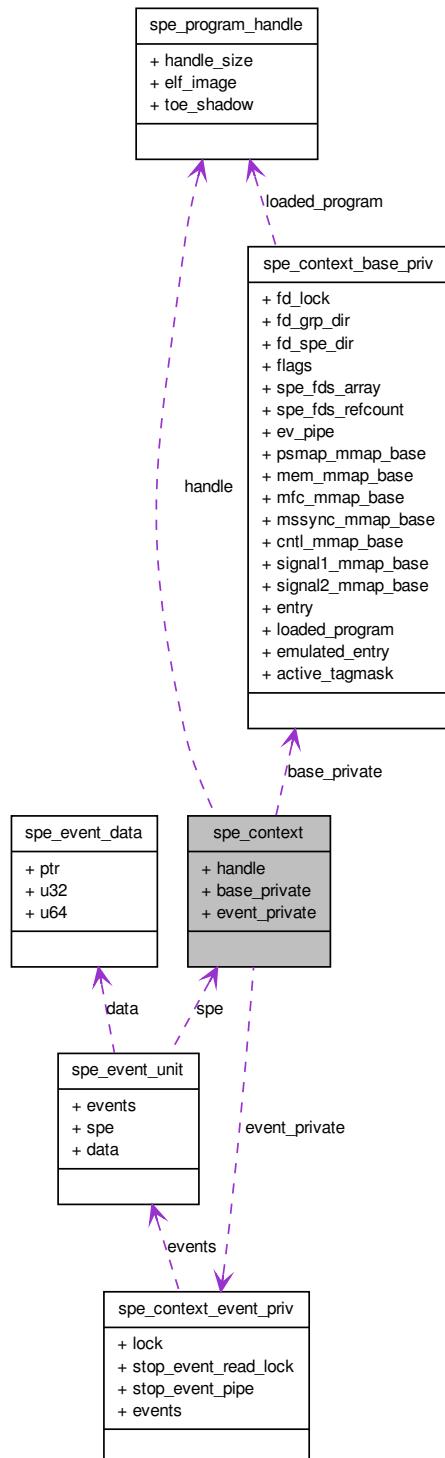
- [dma.h](#)

## 2.5 spe\_context Struct Reference

---

```
#include <libspe2-types.h>
```

Collaboration diagram for spe\_context:



## Data Fields

- `spe_program_handle_t handle`
- `struct spe_context_base_priv * base_private`
- `struct spe_context_event_priv * event_private`

### 2.5.1 Detailed Description

SPE context The SPE context is one of the base data structures for the libspe2 implementation. It holds all persistent information about a "logical SPE" used by the application. This data structure should not be accessed directly, but the application uses a pointer to an SPE context as an identifier for the "logical SPE" it is dealing with through libspe2 API calls.

Definition at line 64 of file libspe2-types.h.

### 2.5.2 Field Documentation

#### 2.5.2.1 struct spe\_context\_base\_priv\* base\_private

Definition at line 76 of file libspe2-types.h.

Referenced by `_base_spe_spe_dir_get()`, `_base_spe_stop_event_source_get()`, `_base_spe_stop_event_target_get()`, `_base_spe_close_if_open()`, `_base_spe_context_create()`, `_base_spe_context_lock()`, `_base_spe_context_run()`, `_base_spe_context_unlock()`, `_base_spe_handle_library_callback()`, `_base_spe_in_mbox_status()`, `_base_spe_in_mbox_write()`, `_base_spe_ls_area_get()`, `_base_spe_mfcio_tag_status_read()`, `_base_spe_mssync_start()`, `_base_spe_mssync_status()`, `_base_spe_open_if_closed()`, `_base_spe_out_intr_mbox_status()`, `_base_spe_out_mbox_read()`, `_base_spe_out_mbox_status()`, `_base_spe_program_load()`, `_base_spe_program_load_complete()`, `_base_spe_ps_area_get()`, `_base_spe_signal_write()`, and `_event_spe_event_handler_register()`.

#### 2.5.2.2 struct spe\_context\_event\_priv\* event\_private

Definition at line 77 of file libspe2-types.h.

#### 2.5.2.3 spe\_program\_handle\_t handle

Definition at line 72 of file libspe2-types.h.

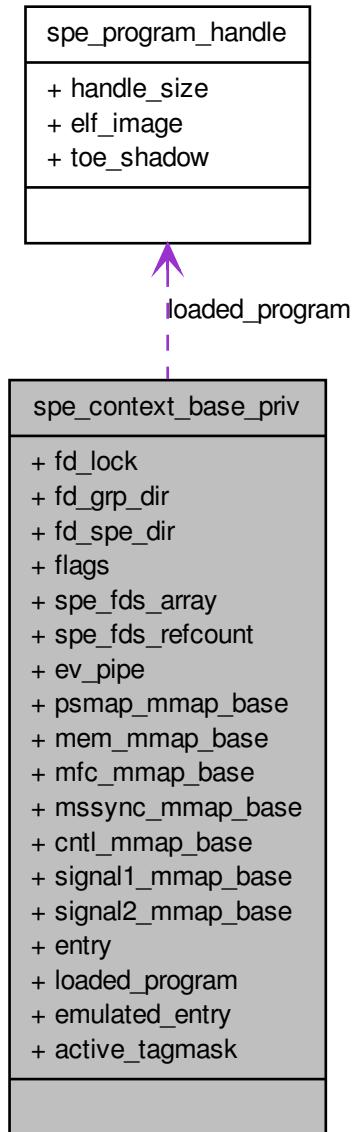
The documentation for this struct was generated from the following file:

- `libspe2-types.h`

## 2.6 spe\_context\_base\_priv Struct Reference

```
#include <spebase.h>
```

Collaboration diagram for spe\_context\_base\_priv:



## Data Fields

- pthread\_mutex\_t [fd\\_lock](#) [NUM\_MBOX\_FDS]
- int [fd\\_grp\\_dir](#)
- int [fd\\_spe\\_dir](#)
- unsigned int [flags](#)
- int [spe\\_fds\\_array](#) [NUM\_MBOX\_FDS]

- int [spe\\_fds\\_refcount](#) [NUM\_MBOX\_FDS]
- int [ev\\_pipe](#) [2]
- void \* [psmap\\_mmap\\_base](#)
- void \* [mem\\_mmap\\_base](#)
- void \* [mfc\\_mmap\\_base](#)
- void \* [mssync\\_mmap\\_base](#)
- void \* [cntl\\_mmap\\_base](#)
- void \* [signal1\\_mmap\\_base](#)
- void \* [signal2\\_mmap\\_base](#)
- int [entry](#)
- [spe\\_program\\_handle\\_t](#) \* [loaded\\_program](#)
- int [emulated\\_entry](#)
- int [active\\_tagmask](#)

## 2.6.1 Detailed Description

Definition at line 61 of file spebase.h.

## 2.6.2 Field Documentation

### 2.6.2.1 int active\_tagmask

Definition at line 108 of file spebase.h.

Referenced by [\\_base\\_spe\\_mfcio\\_tag\\_status\\_read\(\)](#).

### 2.6.2.2 void\* cntl\_mmap\_base

Definition at line 88 of file spebase.h.

Referenced by [\\_base\\_spe\\_context\\_create\(\)](#), [\\_base\\_spe\\_in\\_mbox\\_status\(\)](#), [\\_base\\_spe\\_out\\_intr\\_mbox\\_status\(\)](#), [\\_base\\_spe\\_out\\_mbox\\_status\(\)](#), and [\\_base\\_spe\\_ps\\_area\\_get\(\)](#).

### 2.6.2.3 int emulated\_entry

Definition at line 103 of file spebase.h.

Referenced by [\\_base\\_spe\\_context\\_run\(\)](#), and [\\_base\\_spe\\_program\\_load\(\)](#).

### 2.6.2.4 int entry

Definition at line 93 of file spebase.h.

Referenced by [\\_base\\_spe\\_context\\_run\(\)](#), and [\\_base\\_spe\\_program\\_load\(\)](#).

### 2.6.2.5 int ev\_pipe[2]

Definition at line 81 of file spebase.h.

Referenced by [\\_\\_base\\_spe\\_stop\\_event\\_source\\_get\(\)](#), and [\\_\\_base\\_spe\\_stop\\_event\\_target\\_get\(\)](#).

**2.6.2.6 int fd\_grp\_dir**

Definition at line 68 of file spebase.h.

**2.6.2.7 pthread\_mutex\_t fd\_lock[**NUM\_MBOX\_FDS**]**

Definition at line 65 of file spebase.h.

Referenced by `_base_spe_context_create()`, `_base_spe_context_lock()`, and `_base_spe_context_unlock()`.

**2.6.2.8 int fd\_spe\_dir**

Definition at line 71 of file spebase.h.

Referenced by `_base_spe_spe_dir_get()`, `_base_spe_context_create()`, `_base_spe_context_run()`, `_base_spe_open_if_closed()`, and `_base_spe_program_load_complete()`.

**2.6.2.9 unsigned int flags**

Definition at line 74 of file spebase.h.

Referenced by `_base_spe_context_create()`, `_base_spe_context_run()`, `_base_spe_handle_library_callback()`, `_base_spe_in_mbox_status()`, `_base_spe_in_mbox_write()`, `_base_spe_mfcio_tag_status_read()`, `_base_spe_mssync_start()`, `_base_spe_mssync_status()`, `_base_spe_out_intr_mbox_status()`, `_base_spe_out_mbox_read()`, `_base_spe_out_mbox_status()`, `_base_spe_program_load()`, `_base_spe_signal_write()`, and `_event_spe_event_handler_register()`.

**2.6.2.10 spe\_program\_handle\_t\* loaded\_program**

Definition at line 99 of file spebase.h.

Referenced by `_base_spe_context_create()`, `_base_spe_program_load()`, and `_base_spe_program_load_complete()`.

**2.6.2.11 void\* mem\_mmap\_base**

Definition at line 85 of file spebase.h.

Referenced by `_base_spe_context_create()`, `_base_spe_context_run()`, `_base_spe_handle_library_callback()`, `_base_spe_ls_area_get()`, and `_base_spe_program_load()`.

**2.6.2.12 void\* mfc\_mmap\_base**

Definition at line 86 of file spebase.h.

Referenced by `_base_spe_context_create()`, and `_base_spe_ps_area_get()`.

**2.6.2.13 void\* mssync\_mmap\_base**

Definition at line 87 of file spebase.h.

Referenced by `_base_spe_context_create()`, `_base_spe_mssync_start()`, `_base_spe_mssync_status()`, and `_base_spe_ps_area_get()`.

**2.6.2.14 void\* psmmap\_mmap\_base**

Definition at line 84 of file spebase.h.

Referenced by \_base\_spe\_context\_create().

**2.6.2.15 void\* signal1\_mmap\_base**

Definition at line 89 of file spebase.h.

Referenced by \_base\_spe\_context\_create(), \_base\_spe\_ps\_area\_get(), and \_base\_spe\_signal\_write().

**2.6.2.16 void\* signal2\_mmap\_base**

Definition at line 90 of file spebase.h.

Referenced by \_base\_spe\_context\_create(), \_base\_spe\_ps\_area\_get(), and \_base\_spe\_signal\_write().

**2.6.2.17 int spe\_fds\_array[NUM\_MBOX\_FDS]**

Definition at line 77 of file spebase.h.

Referenced by \_base\_spe\_close\_if\_open(), \_base\_spe\_context\_create(), and \_base\_spe\_open\_if\_closed().

**2.6.2.18 int spe\_fds\_refcount[NUM\_MBOX\_FDS]**

Definition at line 78 of file spebase.h.

Referenced by \_base\_spe\_close\_if\_open(), and \_base\_spe\_open\_if\_closed().

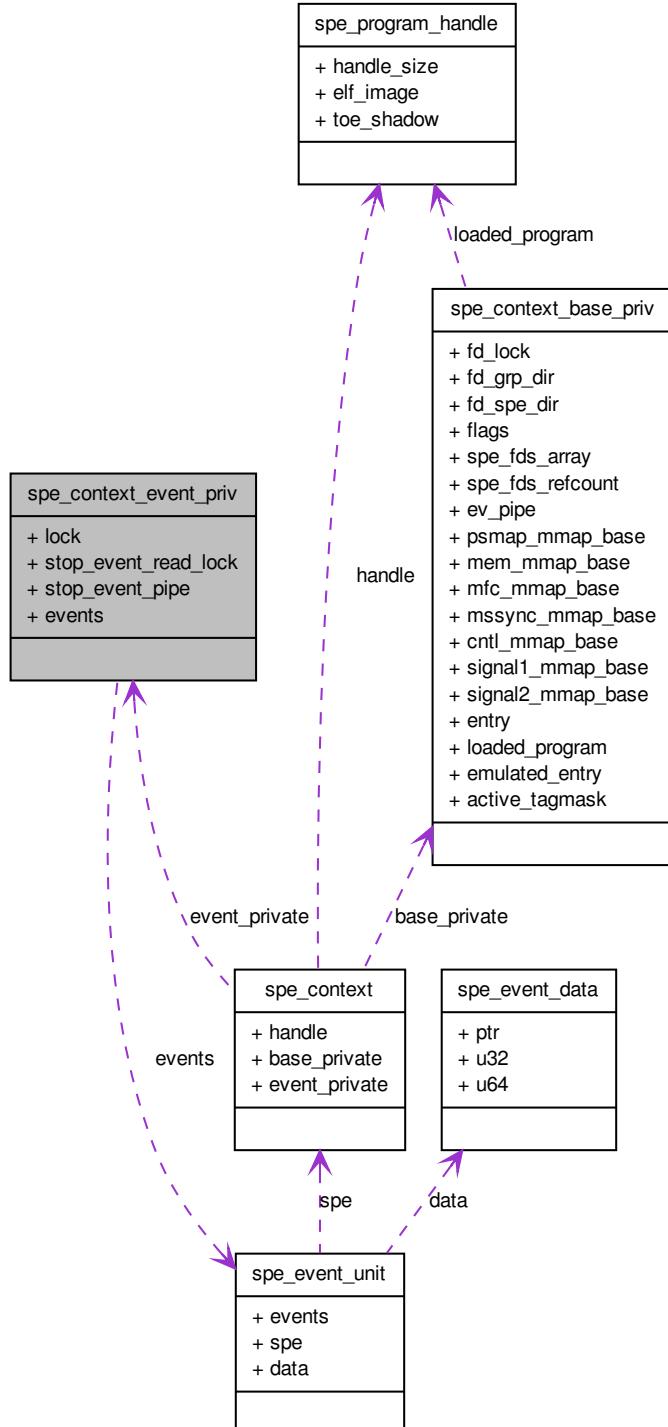
The documentation for this struct was generated from the following file:

- [spebase.h](#)

## 2.7 spe\_context\_event\_priv Struct Reference

```
#include <speevent.h>
```

Collaboration diagram for spe\_context\_event\_priv:



## Data Fields

- `pthread_mutex_t lock`
- `pthread_mutex_t stop_event_read_lock`
- `int stop_event_pipe [2]`
- `spe_event_unit_t events [__NUM_SPE_EVENT_TYPES]`

### 2.7.1 Detailed Description

Definition at line 35 of file speevent.h.

### 2.7.2 Field Documentation

#### 2.7.2.1 `spe_event_unit_t events[__NUM_SPE_EVENT_TYPES]`

Definition at line 40 of file speevent.h.

Referenced by `_event_spe_context_initialize()`, `_event_spe_event_handler_deregister()`, and `_event_spe_event_handler_register()`.

#### 2.7.2.2 `pthread_mutex_t lock`

Definition at line 37 of file speevent.h.

Referenced by `_event_spe_context_finalize()`, and `_event_spe_context_initialize()`.

#### 2.7.2.3 `int stop_event_pipe[2]`

Definition at line 39 of file speevent.h.

Referenced by `_event_spe_context_finalize()`, `_event_spe_context_initialize()`, `_event_spe_context_run()`, `_event_spe_event_handler_deregister()`, `_event_spe_event_handler_register()`, and `_event_spe_stop_info_read()`.

#### 2.7.2.4 `pthread_mutex_t stop_event_read_lock`

Definition at line 38 of file speevent.h.

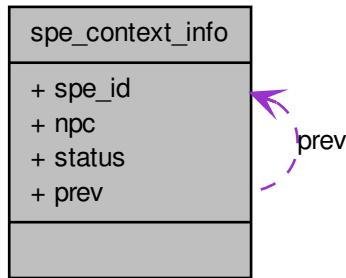
Referenced by `_event_spe_context_finalize()`, `_event_spe_context_initialize()`, and `_event_spe_stop_info_read()`.

The documentation for this struct was generated from the following file:

- `speevent.h`

## 2.8 spe\_context\_info Struct Reference

Collaboration diagram for spe\_context\_info:



### Data Fields

- int [spe\\_id](#)
- unsigned int [npc](#)
- unsigned int [status](#)
- struct [spe\\_context\\_info](#) \* [prev](#)

#### 2.8.1 Detailed Description

Definition at line 40 of file run.c.

#### 2.8.2 Field Documentation

##### 2.8.2.1 unsigned int npc

Definition at line 42 of file run.c.

Referenced by [\\_base\\_spe\\_context\\_run\(\)](#).

##### 2.8.2.2 struct spe\_context\_info\* prev

Definition at line 44 of file run.c.

Referenced by [\\_base\\_spe\\_context\\_run\(\)](#).

##### 2.8.2.3 int spe\_id

Definition at line 41 of file run.c.

Referenced by [\\_base\\_spe\\_context\\_run\(\)](#).

### 2.8.2.4 unsigned int status

Definition at line 43 of file run.c.

Referenced by \_base\_spe\_context\_run().

The documentation for this struct was generated from the following file:

- [run.c](#)

## 2.9 spe\_event\_data Union Reference

```
#include <libspe2-types.h>
```

### Data Fields

- [void \\* ptr](#)
- [unsigned int u32](#)
- [unsigned long long u64](#)

### 2.9.1 Detailed Description

spe\_event\_data\_t User data to be associated with an event

Definition at line 143 of file libspe2-types.h.

### 2.9.2 Field Documentation

#### 2.9.2.1 void\* ptr

Definition at line 145 of file libspe2-types.h.

Referenced by \_event\_spe\_event\_handler\_register().

#### 2.9.2.2 unsigned int u32

Definition at line 146 of file libspe2-types.h.

#### 2.9.2.3 unsigned long long u64

Definition at line 147 of file libspe2-types.h.

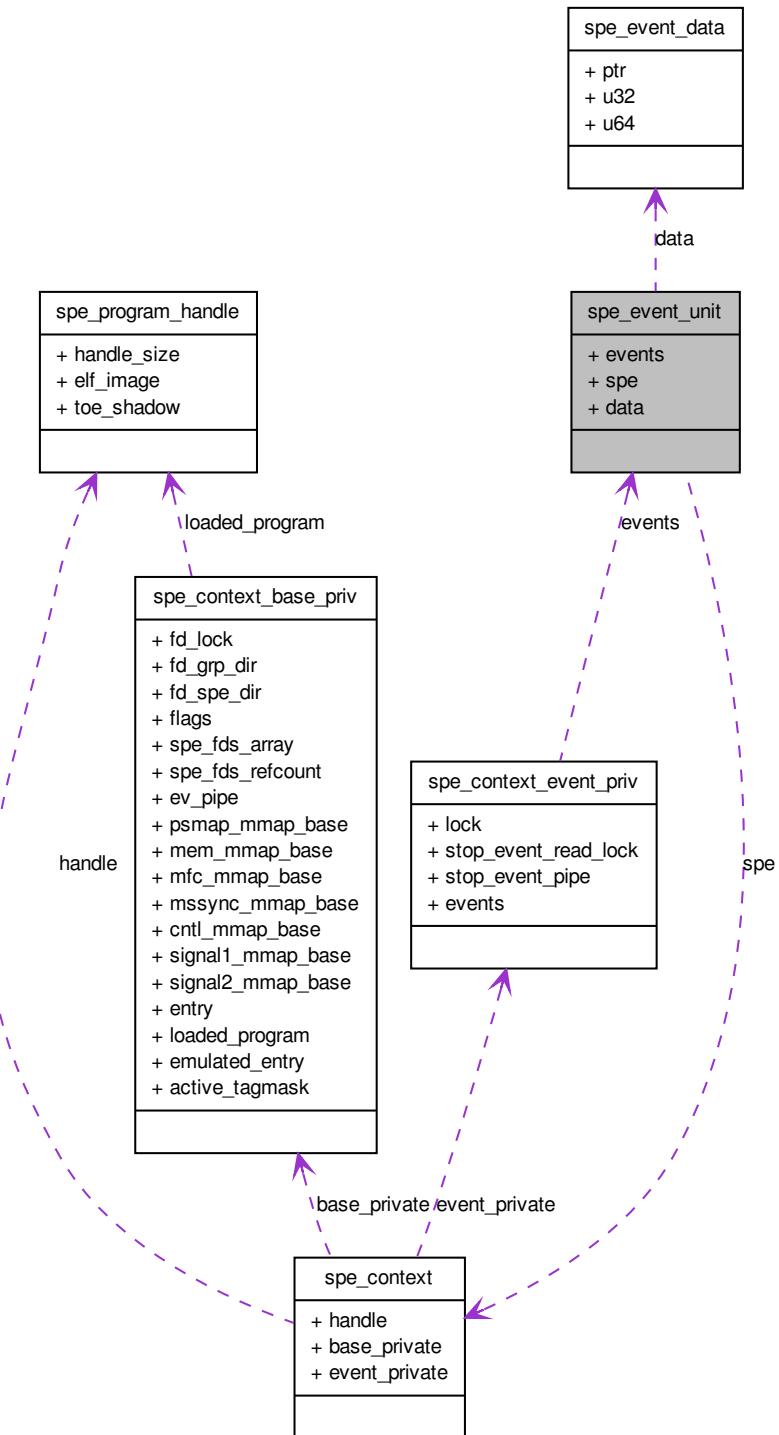
The documentation for this union was generated from the following file:

- [libspe2-types.h](#)

## 2.10 spe\_event\_unit Struct Reference

```
#include <libspe2-types.h>
```

Collaboration diagram for spe\_event\_unit:



## Data Fields

- `unsigned int events`
- `spe_context_ptr_t spe`
- `spe_event_data_t data`

### 2.10.1 Detailed Description

`spe_event_t`

Definition at line 152 of file `libspe2-types.h`.

### 2.10.2 Field Documentation

#### 2.10.2.1 `spe_event_data_t data`

Definition at line 156 of file `libspe2-types.h`.

Referenced by `_event_spe_event_handler_register()`.

#### 2.10.2.2 `unsigned int events`

Definition at line 154 of file `libspe2-types.h`.

Referenced by `_event_spe_event_handler_deregister()`, and `_event_spe_event_handler_register()`.

#### 2.10.2.3 `spe_context_ptr_t spe`

Definition at line 155 of file `libspe2-types.h`.

Referenced by `_event_spe_context_initialize()`, `_event_spe_event_handler_deregister()`, `_event_spe_event_handler_register()`, and `_event_spe_event_wait()`.

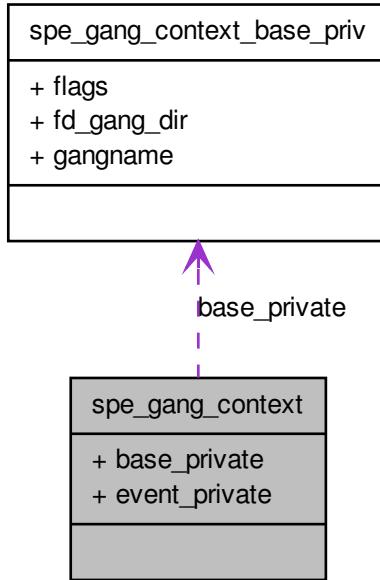
The documentation for this struct was generated from the following file:

- `libspe2-types.h`

## 2.11 `spe_gang_context` Struct Reference

```
#include <libspe2-types.h>
```

Collaboration diagram for spe\_gang\_context:



## Data Fields

- struct [spe\\_gang\\_context\\_base\\_priv](#) \* [base\\_private](#)
- struct [spe\\_gang\\_context\\_event\\_priv](#) \* [event\\_private](#)

### 2.11.1 Detailed Description

**SPE gang context** The SPE gang context is one of the base data structures for the libspe2 implementation. It holds all persistent information about a group of SPE contexts that should be treated as a gang, i.e., be execute together with certain properties. This data structure should not be accessed directly, but the application uses a pointer to an SPE gang context as an identifier for the SPE gang it is dealing with through libspe2 API calls.

Definition at line 94 of file libspe2-types.h.

### 2.11.2 Field Documentation

#### 2.11.2.1 struct spe\_gang\_context\_base\_priv\* base\_private

Definition at line 99 of file libspe2-types.h.

Referenced by [\\_base\\_spe\\_context\\_create\(\)](#), and [\\_base\\_spe\\_gang\\_context\\_create\(\)](#).

### 2.11.2.2 struct spe\_gang\_context\_event\_priv\* event\_private

Definition at line 100 of file libspe2-types.h.

The documentation for this struct was generated from the following file:

- [libspe2-types.h](#)

## 2.12 spe\_gang\_context\_base\_priv Struct Reference

```
#include <spebase.h>
```

### Data Fields

- unsigned int flags
- int fd\_gang\_dir
- char gangname [256]

### 2.12.1 Detailed Description

[spe\\_context](#): This holds the persistant information of a SPU instance it is created by [spe\\_create\\_context\(\)](#)

Definition at line 150 of file spebase.h.

### 2.12.2 Field Documentation

#### 2.12.2.1 int fd\_gang\_dir

Definition at line 156 of file spebase.h.

#### 2.12.2.2 unsigned int flags

Definition at line 153 of file spebase.h.

#### 2.12.2.3 char gangname[256]

Definition at line 158 of file spebase.h.

Referenced by [\\_base\\_spe\\_context\\_create\(\)](#), and [\\_base\\_spe\\_gang\\_context\\_create\(\)](#).

The documentation for this struct was generated from the following file:

- [spebase.h](#)

## 2.13 spe\_Id\_info Struct Reference

```
#include <elf_loader.h>
```

## Data Fields

- unsigned int [entry](#)

### 2.13.1 Detailed Description

Definition at line 34 of file [elf\\_loader.h](#).

### 2.13.2 Field Documentation

#### 2.13.2.1 unsigned int entry

Definition at line 36 of file [elf\\_loader.h](#).

Referenced by [\\_base\\_spe\\_load\\_spe\\_elf\(\)](#), and [\\_base\\_spe\\_program\\_load\(\)](#).

The documentation for this struct was generated from the following file:

- [elf\\_loader.h](#)

## 2.14 spe\_mfc\_command\_area Struct Reference

```
#include <cbea_map.h>
```

## Data Fields

- unsigned char [reserved\\_0\\_3](#) [4]
- unsigned int [MFC\\_LSA](#)
- unsigned int [MFC\\_EAH](#)
- unsigned int [MFC\\_EAL](#)
- unsigned int [MFC\\_Size\\_Tag](#)
- union {
  - unsigned int [MFC\\_ClassID\\_CMD](#)
  - unsigned int [MFC\\_CMDStatus](#)};
- unsigned char [reserved\\_18\\_103](#) [236]
- unsigned int [MFC\\_QStatus](#)
- unsigned char [reserved\\_108\\_203](#) [252]
- unsigned int [Prxy\\_QueryType](#)
- unsigned char [reserved\\_208\\_21B](#) [20]
- unsigned int [Prxy\\_QueryMask](#)
- unsigned char [reserved\\_220\\_22B](#) [12]
- unsigned int [Prxy\\_TagStatus](#)

### 2.14.1 Detailed Description

Definition at line 34 of file [cbea\\_map.h](#).

## 2.14.2 Field Documentation

**2.14.2.1 union { ... }**

**2.14.2.2 unsigned int MFC\_ClassID\_CMD**

Definition at line 41 of file cbea\_map.h.

**2.14.2.3 unsigned int MFC\_CMDStatus**

Definition at line 42 of file cbea\_map.h.

**2.14.2.4 unsigned int MFC\_EAH**

Definition at line 37 of file cbea\_map.h.

**2.14.2.5 unsigned int MFC\_EAL**

Definition at line 38 of file cbea\_map.h.

**2.14.2.6 unsigned int MFC\_LSA**

Definition at line 36 of file cbea\_map.h.

**2.14.2.7 unsigned int MFC\_QStatus**

Definition at line 45 of file cbea\_map.h.

**2.14.2.8 unsigned int MFC\_Size\_Tag**

Definition at line 39 of file cbea\_map.h.

**2.14.2.9 unsigned int Prxy\_QueryMask**

Definition at line 49 of file cbea\_map.h.

**2.14.2.10 unsigned int Prxy\_QueryType**

Definition at line 47 of file cbea\_map.h.

**2.14.2.11 unsigned int Prxy\_TagStatus**

Definition at line 51 of file cbea\_map.h.

**2.14.2.12 unsigned char reserved\_0\_3[4]**

Definition at line 35 of file cbea\_map.h.

**2.14.2.13 unsigned char reserved\_108\_203[252]**

Definition at line 46 of file cbea\_map.h.

**2.14.2.14 unsigned char reserved\_18\_103[236]**

Definition at line 44 of file cbea\_map.h.

**2.14.2.15 unsigned char reserved\_208\_21B[20]**

Definition at line 48 of file cbea\_map.h.

**2.14.2.16 unsigned char reserved\_220\_22B[12]**

Definition at line 50 of file cbea\_map.h.

The documentation for this struct was generated from the following file:

- [cbea\\_map.h](#)

## 2.15 spe\_mssync\_area Struct Reference

```
#include <cbea_map.h>
```

### Data Fields

- [unsigned int MFC\\_MSSync](#)

### 2.15.1 Detailed Description

Definition at line 30 of file cbea\_map.h.

### 2.15.2 Field Documentation

#### 2.15.2.1 unsigned int MFC\_MSSync

Definition at line 31 of file cbea\_map.h.

Referenced by `_base_spe_mssync_start()`, and `_base_spe_mssync_status()`.

The documentation for this struct was generated from the following file:

- [cbea\\_map.h](#)

## 2.16 spe\_program\_handle Struct Reference

```
#include <libspe2-types.h>
```

## Data Fields

- unsigned int handle\_size
- void \* [elf\\_image](#)
- void \* [toe\\_shadow](#)

### 2.16.1 Detailed Description

SPE program handle Structure [spe\\_program\\_handle](#) per CESOF specification libspe2 applications usually only keep a pointer to the program handle and do not use the structure directly.

Definition at line 43 of file libspe2-types.h.

### 2.16.2 Field Documentation

#### 2.16.2.1 void\* elf\_image

Definition at line 50 of file libspe2-types.h.

Referenced by [\\_base\\_spe\\_image\\_close\(\)](#), [\\_base\\_spe\\_image\\_open\(\)](#), [\\_base\\_spe\\_load\\_spe\\_elf\(\)](#), [\\_base\\_spe\\_parse\\_isolated\\_elf\(\)](#), [\\_base\\_spe\\_program\\_load\\_complete\(\)](#), [\\_base\\_spe\\_toe\\_ear\(\)](#), and [\\_base\\_spe\\_verify\\_spe\\_elf\\_image\(\)](#).

#### 2.16.2.2 unsigned int handle\_size

Definition at line 49 of file libspe2-types.h.

Referenced by [\\_base\\_spe\\_image\\_open\(\)](#).

#### 2.16.2.3 void\* toe\_shadow

Definition at line 51 of file libspe2-types.h.

Referenced by [\\_base\\_spe\\_image\\_close\(\)](#), [\\_base\\_spe\\_image\\_open\(\)](#), and [\\_base\\_spe\\_toe\\_ear\(\)](#).

The documentation for this struct was generated from the following file:

- [libspe2-types.h](#)

## 2.17 spe\_reg128 Struct Reference

```
#include <handler_utils.h>
```

## Data Fields

- unsigned int slot [4]

### 2.17.1 Detailed Description

Definition at line 23 of file handler\_utils.h.

## 2.17.2 Field Documentation

### 2.17.2.1 unsigned int slot[4]

Definition at line 24 of file handler\_utils.h.

The documentation for this struct was generated from the following file:

- [handler\\_utils.h](#)

## 2.18 spe\_sig\_notify\_1\_area Struct Reference

```
#include <cbea_map.h>
```

### Data Fields

- unsigned char [reserved\\_0\\_B](#) [12]
- unsigned int [SPU\\_Sig\\_Notify\\_1](#)

### 2.18.1 Detailed Description

Definition at line 69 of file cbea\_map.h.

## 2.18.2 Field Documentation

### 2.18.2.1 unsigned char reserved\_0\_B[12]

Definition at line 70 of file cbea\_map.h.

### 2.18.2.2 unsigned int SPU\_Sig\_Notify\_1

Definition at line 71 of file cbea\_map.h.

Referenced by [\\_base\\_spe\\_signal\\_write\(\)](#).

The documentation for this struct was generated from the following file:

- [cbea\\_map.h](#)

## 2.19 spe\_sig\_notify\_2\_area Struct Reference

```
#include <cbea_map.h>
```

### Data Fields

- unsigned char [reserved\\_0\\_B](#) [12]
- unsigned int [SPU\\_Sig\\_Notify\\_2](#)

### 2.19.1 Detailed Description

Definition at line 74 of file cbea\_map.h.

### 2.19.2 Field Documentation

#### 2.19.2.1 `unsigned char reserved_0_B[12]`

Definition at line 75 of file cbea\_map.h.

#### 2.19.2.2 `unsigned int SPU_Sig_Notify_2`

Definition at line 76 of file cbea\_map.h.

Referenced by `_base_spe_signal_write()`.

The documentation for this struct was generated from the following file:

- [cbea\\_map.h](#)

## 2.20 `spe_spu_control_area` Struct Reference

```
#include <cbea_map.h>
```

### Data Fields

- `unsigned char reserved_0_3 [4]`
- `unsigned int SPU_Out_Mbox`
- `unsigned char reserved_8_B [4]`
- `unsigned int SPU_In_Mbox`
- `unsigned char reserved_10_13 [4]`
- `unsigned int SPU_Mbox_Stat`
- `unsigned char reserved_18_1B [4]`
- `unsigned int SPU_RunCntl`
- `unsigned char reserved_20_23 [4]`
- `unsigned int SPU_Status`
- `unsigned char reserved_28_33 [12]`
- `unsigned int SPU_NPC`

### 2.20.1 Detailed Description

Definition at line 54 of file cbea\_map.h.

### 2.20.2 Field Documentation

#### 2.20.2.1 `unsigned char reserved_0_3[4]`

Definition at line 55 of file cbea\_map.h.

**2.20.2.2 unsigned char reserved\_10\_13[4]**

Definition at line 59 of file cbea\_map.h.

**2.20.2.3 unsigned char reserved\_18\_1B[4]**

Definition at line 61 of file cbea\_map.h.

**2.20.2.4 unsigned char reserved\_20\_23[4]**

Definition at line 63 of file cbea\_map.h.

**2.20.2.5 unsigned char reserved\_28\_33[12]**

Definition at line 65 of file cbea\_map.h.

**2.20.2.6 unsigned char reserved\_8\_B[4]**

Definition at line 57 of file cbea\_map.h.

**2.20.2.7 unsigned int SPU\_In\_Mbox**

Definition at line 58 of file cbea\_map.h.

**2.20.2.8 unsigned int SPU\_Mbox\_Stat**

Definition at line 60 of file cbea\_map.h.

Referenced by \_base\_spe\_in\_mbox\_status(), \_base\_spe\_out\_intr\_mbox\_status(), and \_base\_spe\_out\_mbox\_status().

**2.20.2.9 unsigned int SPU\_NPC**

Definition at line 66 of file cbea\_map.h.

**2.20.2.10 unsigned int SPU\_Out\_Mbox**

Definition at line 56 of file cbea\_map.h.

**2.20.2.11 unsigned int SPU\_RunCntl**

Definition at line 62 of file cbea\_map.h.

**2.20.2.12 unsigned int SPU\_Status**

Definition at line 64 of file cbea\_map.h.

The documentation for this struct was generated from the following file:

- [cbea\\_map.h](#)

## 2.21 spe\_stop\_info Struct Reference

```
#include <libspe2-types.h>
```

### Data Fields

- `unsigned int stop_reason`
- `union {`
  - `int spe_exit_code`
  - `int spe_signal_code`
  - `int spe_runtime_error`
  - `int spe_runtime_exception`
  - `int spe_runtime_fatal`
  - `int spe_callback_error`
  - `int spe_isolation_error`
  - `void * __reserved_ptr`
  - `unsigned long long __reserved_u64``}` `result`
- `int spu_status`

### 2.21.1 Detailed Description

`spe_stop_info_t`

Definition at line 118 of file libspe2-types.h.

### 2.21.2 Field Documentation

#### 2.21.2.1 `void* __reserved_ptr`

Definition at line 129 of file libspe2-types.h.

#### 2.21.2.2 `unsigned long long __reserved_u64`

Definition at line 130 of file libspe2-types.h.

#### 2.21.2.3 `union { ... } result`

Referenced by `_base_spe_context_run()`.

#### 2.21.2.4 `int spe_callback_error`

Definition at line 126 of file libspe2-types.h.

Referenced by `_base_spe_context_run()`.

**2.21.2.5 int spe\_exit\_code**

Definition at line 121 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**2.21.2.6 int spe\_isolation\_error**

Definition at line 127 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**2.21.2.7 int spe\_runtime\_error**

Definition at line 123 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**2.21.2.8 int spe\_runtime\_exception**

Definition at line 124 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**2.21.2.9 int spe\_runtime\_fatal**

Definition at line 125 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**2.21.2.10 int spe\_signal\_code**

Definition at line 122 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**2.21.2.11 int spu\_status**

Definition at line 132 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**2.21.2.12 unsigned int stop\_reason**

Definition at line 119 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

The documentation for this struct was generated from the following file:

- [libspe2-types.h](#)



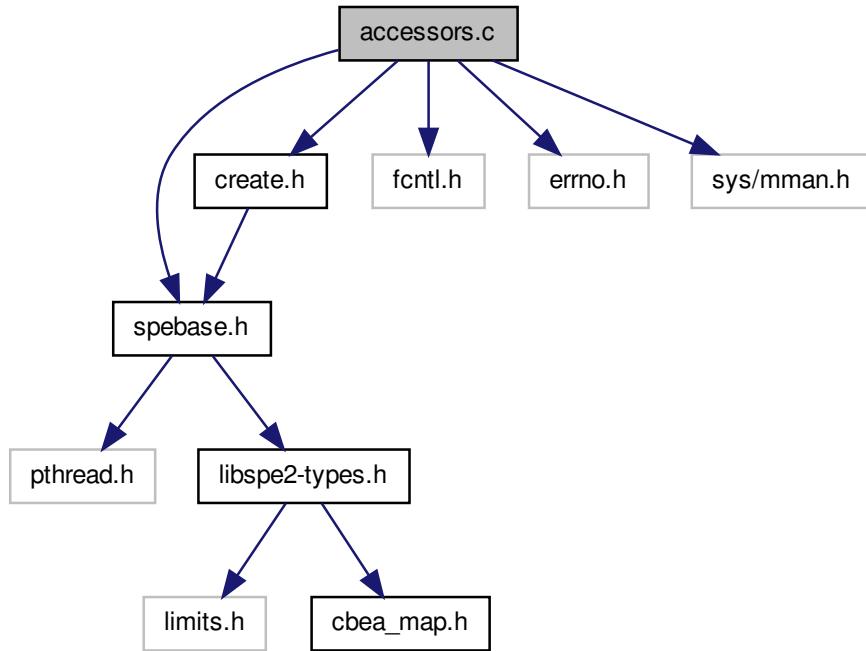
## Chapter 3

# File Documentation

### 3.1 accessors.c File Reference

```
#include "spebase.h"
#include "create.h"
#include <fcntl.h>
#include <errno.h>
#include <sys/mman.h>
```

Include dependency graph for accessors.c:



## Functions

- `void * __base_spe_ps_area_get (spe_context_ptr_t spe, enum ps_area area)`
- `void * __base_spe_ls_area_get (spe_context_ptr_t spe)`
- `__attribute__ ((noinline))`
- `int __base_spe_event_source_acquire (spe_context_ptr_t spe, enum fd_name fdesc)`
- `void __base_spe_event_source_release (struct spe_context *spe, enum fd_name fdesc)`
- `int __base_spe_spe_dir_get (spe_context_ptr_t spe)`
- `int __base_spe_stop_event_source_get (spe_context_ptr_t spe)`
- `int __base_spe_stop_event_target_get (spe_context_ptr_t spe)`
- `int __base_spe_ls_size_get (spe_context_ptr_t spe)`

### 3.1.1 Function Documentation

#### 3.1.1.1 \_\_attribute\_\_ ( (noinline) )

Definition at line 69 of file `accessors.c`.

```
{
    return;
}
```

### 3.1.1.2 int \_\_base\_spe\_event\_source\_acquire ( spe\_context\_ptr\_t spe, enum fd\_name fdesc )

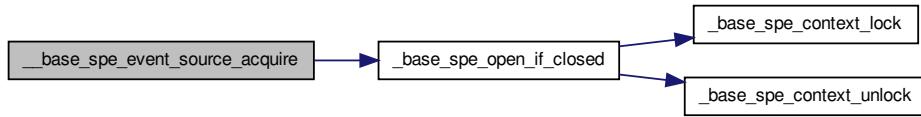
Definition at line 74 of file accessors.c.

References \_base\_spe\_open\_if\_closed().

Referenced by \_event\_spe\_event\_handler\_deregister(), and \_event\_spe\_event\_handler\_register().

```
{
    return _base_spe_open_if_closed(spe, fdesc, 0);
}
```

Here is the call graph for this function:



### 3.1.1.3 void \_\_base\_spe\_event\_source\_release ( struct spe\_context \* spectx, enum fd\_name fdesc )

\_\_base\_spe\_event\_source\_release releases the file descriptor to the specified event source

#### Parameters

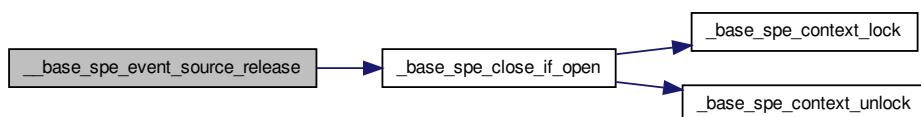
<i>spectx</i>	Specifies the SPE context
<i>fdesc</i>	Specifies the event source

Definition at line 79 of file accessors.c.

References \_base\_spe\_close\_if\_open().

```
{
    __base_spe_close_if_open(spe, fdesc);
}
```

Here is the call graph for this function:



### 3.1.1.4 int \_\_base\_spe\_spe\_dir\_get( spe\_context\_ptr\_t spe )

Definition at line 84 of file accessors.c.

References spe\_context::base\_private, and spe\_context\_base\_priv::fd\_spe\_dir.

```
{
    return spe->base_private->fd_spe_dir;
}
```

### 3.1.1.5 int \_\_base\_spe\_stop\_event\_source\_get( spe\_context\_ptr\_t spe )

speevent users read from this end

Definition at line 92 of file accessors.c.

References spe\_context::base\_private, and spe\_context\_base\_priv::ev\_pipe.

```
{
    return spe->base_private->ev_pipe[1];
}
```

### 3.1.1.6 int \_\_base\_spe\_stop\_event\_target\_get( spe\_context\_ptr\_t spe )

speevent writes to this end

Definition at line 100 of file accessors.c.

References spe\_context::base\_private, and spe\_context\_base\_priv::ev\_pipe.

```
{
    return spe->base_private->ev_pipe[0];
}
```

### 3.1.1.7 void\* \_\_base\_spe\_ls\_area\_get( spe\_context\_ptr\_t spe )

Definition at line 64 of file accessors.c.

References spe\_context::base\_private, and spe\_context\_base\_priv::mem\_mmap\_base.

```
{
    return spe->base_private->mem_mmap_base;
}
```

### 3.1.1.8 int \_\_base\_spe\_ls\_size\_get( spe\_context\_ptr\_t spe )

`__base_spe_ls_size_get` returns the size of the local store area

#### Parameters

<i>spectx</i>	Specifies the SPE context
---------------	---------------------------

Definition at line 105 of file accessors.c.

References LS\_SIZE.

```
{
    return LS_SIZE;
}
```

### 3.1.1.9 void\* \_base\_spe\_ps\_area\_get( spe\_context\_ptr\_t spe, enum ps\_area area )

Definition at line 30 of file accessors.c.

References spe\_context::base\_private, spe\_context\_base\_priv::cntl\_mmap\_base, spe\_context\_base\_priv::mfc\_mmap\_base, spe\_context\_base\_priv::mssync\_mmap\_base, spe\_context\_base\_priv::signal1\_mmap\_base, spe\_context\_base\_priv::signal2\_mmap\_base, SPE\_CONTROL\_AREA, SPE\_MFC\_COMMAND\_AREA, SPE\_MSSYNC\_AREA, SPE\_SIG\_NOTIFY\_1\_AREA, and SPE\_SIG\_NOTIFY\_2\_AREA.

```
{
    void *ptr;

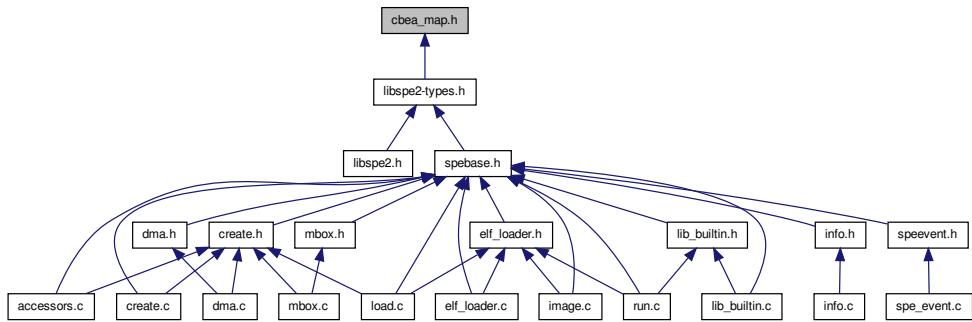
    switch (area) {
        case SPE_MSSYNC_AREA:
            ptr = spe->base_private->mssync_mmap_base;
            break;
        case SPE_MFC_COMMAND_AREA:
            ptr = spe->base_private->mfc_mmap_base;
            break;
        case SPE_CONTROL_AREA:
            ptr = spe->base_private->cntl_mmap_base;
            break;
        case SPE_SIG_NOTIFY_1_AREA:
            ptr = spe->base_private->signal1_mmap_base;
            break;
        case SPE_SIG_NOTIFY_2_AREA:
            ptr = spe->base_private->signal2_mmap_base;
            break;
        default:
            errno = EINVAL;
            return NULL;
            break;
    }

    if (ptr == MAP_FAILED) {
        errno = EACCES;
        return NULL;
    }

    return ptr;
}
```

## 3.2 cbea\_map.h File Reference

This graph shows which files directly or indirectly include this file:



## Data Structures

- struct [spe\\_mssync\\_area](#)
- struct [spe\\_mfc\\_command\\_area](#)
- struct [spe\\_spu\\_control\\_area](#)
- struct [spe\\_sig\\_notify\\_1\\_area](#)
- struct [spe\\_sig\\_notify\\_2\\_area](#)

## Typedefs

- typedef struct [spe\\_mssync\\_area](#) [spe\\_mssync\\_area\\_t](#)
- typedef struct [spe\\_mfc\\_command\\_area](#) [spe\\_mfc\\_command\\_area\\_t](#)
- typedef struct [spe\\_spu\\_control\\_area](#) [spe\\_spu\\_control\\_area\\_t](#)
- typedef struct [spe\\_sig\\_notify\\_1\\_area](#) [spe\\_sig\\_notify\\_1\\_area\\_t](#)
- typedef struct [spe\\_sig\\_notify\\_2\\_area](#) [spe\\_sig\\_notify\\_2\\_area\\_t](#)

### 3.2.1 Typedef Documentation

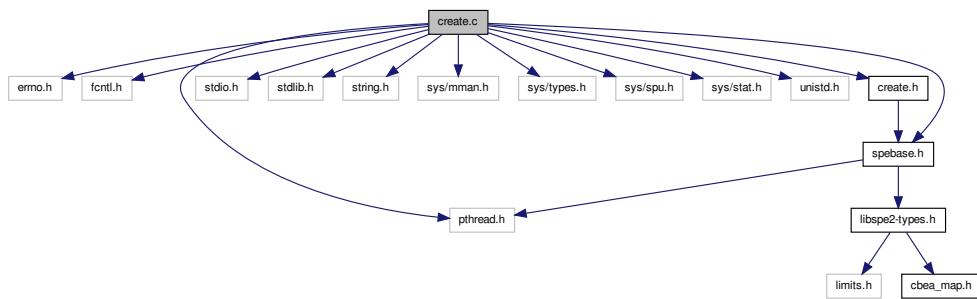
- 3.2.1.1 [typedef struct spe\\_mfc\\_command\\_area spe\\_mfc\\_command\\_area\\_t](#)
- 3.2.1.2 [typedef struct spe\\_mssync\\_area spe\\_mssync\\_area\\_t](#)
- 3.2.1.3 [typedef struct spe\\_sig\\_notify\\_1\\_area spe\\_sig\\_notify\\_1\\_area\\_t](#)
- 3.2.1.4 [typedef struct spe\\_sig\\_notify\\_2\\_area spe\\_sig\\_notify\\_2\\_area\\_t](#)
- 3.2.1.5 [typedef struct spe\\_spu\\_control\\_area spe\\_spu\\_control\\_area\\_t](#)

## 3.3 create.c File Reference

```
#include <errno.h>
```

```
#include <fcntl.h>
#include <pthread.h>
#include <stdio.h>
#include <stdlib.h>
#include <string.h>
#include <sys/mman.h>
#include <sys/types.h>
#include <sys/spu.h>
#include <sys/stat.h>
#include <unistd.h>
#include "create.h"
#include "spebase.h"
```

Include dependency graph for create.c:



## Data Structures

- struct [fd\\_attr](#)

## Functions

- void [\\_base\\_spe\\_context\\_lock](#) (spe\_context\_ptr\_t spe, enum [fd\\_name](#) fdesc)
- void [\\_base\\_spe\\_context\\_unlock](#) (spe\_context\_ptr\_t spe, enum [fd\\_name](#) fdesc)
- int [\\_base\\_spe\\_open\\_if\\_closed](#) (struct [spe\\_context](#) \*spe, enum [fd\\_name](#) fdesc, int locked)
- void [\\_base\\_spe\\_close\\_if\\_open](#) (struct [spe\\_context](#) \*spe, enum [fd\\_name](#) fdesc)
- [spe\\_context\\_ptr\\_t \\_base\\_spe\\_context\\_create](#) (unsigned int flags, [spe\\_gang\\_context\\_ptr\\_t](#) gctx, [spe\\_context\\_ptr\\_t](#) aff\_spe)
- [spe\\_gang\\_context\\_ptr\\_t \\_base\\_spe\\_gang\\_context\\_create](#) (unsigned int flags)
- int [\\_base\\_spe\\_context\\_destroy](#) ([spe\\_context\\_ptr\\_t](#) spe)
- int [\\_base\\_spe\\_gang\\_context\\_destroy](#) ([spe\\_gang\\_context\\_ptr\\_t](#) gctx)

### 3.3.1 Function Documentation

#### 3.3.1.1 void \_base\_spe\_close\_if\_open ( struct spe\_context \* spe, enum fd\_name fdesc )

Definition at line 125 of file create.c.

References \_base\_spe\_context\_lock(), \_base\_spe\_context\_unlock(), spe\_context::base\_private, spe\_context\_base\_priv::spe\_fds\_array, and spe\_context\_base\_priv::spe\_fds\_refcount.

Referenced by \_\_base\_spe\_event\_source\_release(), and \_base\_spe\_signal\_write().

```
{
    _base_spe_context_lock(spe, fdesc);

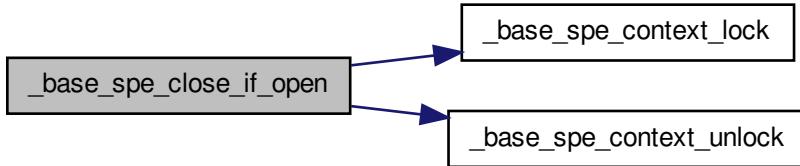
    if (spe->base_private->spe_fds_array[(int)fdesc] != -1 &&
        spe->base_private->spe_fds_refcount[(int)fdesc] == 1) {

        spe->base_private->spe_fds_refcount[(int)fdesc]--;
        close(spe->base_private->spe_fds_array[(int)fdesc]);

        spe->base_private->spe_fds_array[(int)fdesc] = -1;
    } else if (spe->base_private->spe_fds_refcount[(int)fdesc] > 0) {
        spe->base_private->spe_fds_refcount[(int)fdesc]--;
    }

    _base_spe_context_unlock(spe, fdesc);
}
```

Here is the call graph for this function:



#### 3.3.1.2 spe\_context\_ptr\_t \_base\_spe\_context\_create ( unsigned int flags, spe\_gang\_context\_ptr\_t gctx, spe\_context\_ptr\_t aff\_spe )

\_base\_spe\_context\_create creates a single SPE context, i.e., the corresponding directory is created in SPUFS either as a subdirectory of a gang or individually (maybe this is best considered a gang of one)

##### Parameters

<i>flags</i>	
<i>gctx</i>	specify NULL if not belonging to a gang
<i>aff_spe</i>	specify NULL to skip affinity information

Definition at line 183 of file create.c.

References \_base\_spe\_emulated\_loader\_present(), spe\_gang\_context::base\_private, spe\_context::base\_private, spe\_context\_base\_priv::cntl\_mmap\_base, CNTL\_OFFSET, CNTL\_SIZE, DEBUG\_PRINTF, spe\_context\_base\_priv::fd\_lock, spe\_context\_base\_priv::fd\_spe\_dir, spe\_context\_base\_priv::flags, spe\_gang\_context\_base\_priv::gangname, spe\_context\_base\_priv::loaded\_program, LS\_SIZE, spe\_context\_base\_priv::mem\_mmap\_base, spe\_context\_base\_priv::mfc\_mmap\_base, MFC\_OFFSET, MFC\_SIZE, MSS\_SIZE, spe\_context\_base\_priv::mssync\_mmap\_base, MSSYNC\_OFFSET, NUM\_MBOX\_FDS, spe\_context\_base\_priv::psmap\_mmap\_base, PSMAP\_SIZE, spe\_context\_base\_priv::signal1\_mmap\_base, SIGNAL1\_OFFSET, spe\_context\_base\_priv::signal2\_mmap\_base, SIGNAL2\_OFFSET, SIGNAL\_SIZE, SPE\_AFFINITY\_MEMORY, SPE\_CFG\_SIGNIFY1\_OR, SPE\_CFG\_SIGNIFY2\_OR, SPE\_EVENTS\_ENABLE, spe\_context\_base\_priv::spe\_fds\_array, SPE\_ISOLATE, SPE\_ISOLATE\_EMULATE, and SPE\_MAP\_PS.

```
{
    char pathname[256];
    int i, aff_spe_fd = 0;
    unsigned int spu_createflags = 0;
    struct spe_context *spe = NULL;
    struct spe_context_base_priv *priv;

    /* We need a loader present to run in emulated isolated mode */
    if (flags & SPE_ISOLATE_EMULATE
        && !_base_spe_emulated_loader_present()) {
        errno = EINVAL;
        return NULL;
    }

    /* Put some sane defaults into the SPE context */
    spe = malloc(sizeof(*spe));
    if (!spe) {
        DEBUG_PRINTF("ERROR: Could not allocate spe context.\n");
        return NULL;
    }
    memset(spe, 0, sizeof(*spe));

    spe->base_private = malloc(sizeof(*spe->base_private));
    if (!spe->base_private) {
        DEBUG_PRINTF("ERROR: Could not allocate "
                    "spe->base_private context.\n");
        free(spe);
        return NULL;
    }

    /* just a convenience variable */
    priv = spe->base_private;

    priv->fd_spe_dir = -1;
    priv->mem_mmap_base = MAP_FAILED;
    priv->psmap_mmap_base = MAP_FAILED;
    priv->mssync_mmap_base = MAP_FAILED;
    priv->mfc_mmap_base = MAP_FAILED;
    priv->cntl_mmap_base = MAP_FAILED;
    priv->signal1_mmap_base = MAP_FAILED;
    priv->signal2_mmap_base = MAP_FAILED;
    priv->loaded_program = NULL;

    for (i = 0; i < NUM_MBOX_FDS; i++) {
        priv->spe_fds_array[i] = -1;
        pthread_mutex_init(&priv->fd_lock[i], NULL);
    }

    /* initialise spu_createflags */
    if (flags & SPE_ISOLATE) {
        flags |= SPE_MAP_PS;
        spu_createflags |= SPU_CREATE_ISOLATE | SPU_CREATE_NOSCHED;
    }
}
```

```

}

if (flags & SPE_EVENTS_ENABLE)
    spu_createflags |= SPU_CREATE_EVENTS_ENABLED;

if (aff_spe)
    spu_createflags |= SPU_CREATE_AFFINITY_SPU;

if (flags & SPE_AFFINITY_MEMORY)
    spu_createflags |= SPU_CREATE_AFFINITY_MEM;

/* Make the SPUFS directory for the SPE */
if (gctx == NULL)
    sprintf(pathname, "/spu/spethread-%i-%lu",
            getpid(), (unsigned long)spe);
else
    sprintf(pathname, "/spu/%s/spethread-%i-%lu",
            gctx->base_private->gangname, getpid(),
            (unsigned long)spe);

if (aff_spe)
    aff_spe_fd = aff_spe->base_private->fd_spe_dir;

priv->fd_spe_dir = spu_create(pathname, spu_createflags,
                               S_IRUSR | S_IWUSR | S_IXUSR, aff_spe_fd);

if (priv->fd_spe_dir < 0) {
    int errno_saved = errno; /* save errno to prevent being overwritte */
    DEBUG_PRINTF("ERROR: Could not create SPE %s\n", pathname);
    perror("spu_create()");
    free_spe_context(spe);
    /* we mask most errors, but leave ENODEV, etc */
    switch (errno_saved) {
    case ENOTSUP:
    case EEXIST:
    case EINVAL:
    case EBUSY:
    case EPERM:
    case ENODEV:
        errno = errno_saved; /* restore errno */
        break;
    default:
        errno = EFAULT;
        break;
    }
    return NULL;
}

priv->flags = flags;

/* Map the required areas into process memory */
priv->mem_mmap_base = mapfileat(priv->fd_spe_dir, "mem", LS_SIZE);
if (priv->mem_mmap_base == MAP_FAILED) {
    DEBUG_PRINTF("ERROR: Could not map SPE memory.\n");
    free_spe_context(spe);
    errno = ENOMEM;
    return NULL;
}

if (flags & SPE_MAP_PS) {
    /* It's possible to map the entire problem state area with
     * one mmap - try this first */
    priv->psmap_mmap_base = mapfileat(priv->fd_spe_dir,
                                       "psmap", PSMAP_SIZE);

    if (priv->psmap_mmap_base != MAP_FAILED) {

```

```

priv->mssync_mmap_base =
    priv->psmap_mmap_base + MSSYNC_OFFSET;
priv->mfc_mmap_base =
    priv->psmap_mmap_base + MFC_OFFSET;
priv->cntl_mmap_base =
    priv->psmap_mmap_base + CNTL_OFFSET;
priv->signal1_mmap_base =
    priv->psmap_mmap_base + SIGNAL1_OFFSET;
priv->signal2_mmap_base =
    priv->psmap_mmap_base + SIGNAL2_OFFSET;

} else {
    /* map each region separately */
    priv->mfc_mmap_base =
        mapfileat(priv->fd_spe_dir, "mfc", MFC_SIZE);
    priv->mssync_mmap_base =
        mapfileat(priv->fd_spe_dir, "mss", MSS_SIZE);
    priv->cntl_mmap_base =
        mapfileat(priv->fd_spe_dir, "cntl", CNTL_SIZE);
    priv->signal1_mmap_base =
        mapfileat(priv->fd_spe_dir, "signal1",
                  SIGNAL_SIZE);
    priv->signal2_mmap_base =
        mapfileat(priv->fd_spe_dir, "signal2",
                  SIGNAL_SIZE);

    if (priv->mfc_mmap_base == MAP_FAILED ||
        priv->cntl_mmap_base == MAP_FAILED ||
        priv->signal1_mmap_base == MAP_FAILED ||
        priv->signal2_mmap_base == MAP_FAILED) {
        DEBUG_PRINTF("ERROR: Could not map SPE "
                     "PS memory.\n");
        free_spe_context(spe);
        errno = ENOMEM;
        return NULL;
    }
}

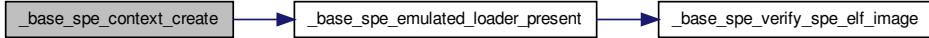
if (flags & SPE_CFG_SIGNIFY1_OR) {
    if (setsignify(priv->fd_spe_dir, "signal1_type")) {
        DEBUG_PRINTF("ERROR: Could not open SPE "
                     "signal1_type file.\n");
        free_spe_context(spe);
        errno = EFAULT;
        return NULL;
    }
}

if (flags & SPE_CFG_SIGNIFY2_OR) {
    if (setsignify(priv->fd_spe_dir, "signal2_type")) {
        DEBUG_PRINTF("ERROR: Could not open SPE "
                     "signal2_type file.\n");
        free_spe_context(spe);
        errno = EFAULT;
        return NULL;
    }
}

return spe;
}

```

Here is the call graph for this function:



### 3.3.1.3 int \_base\_spe\_context\_destroy ( spe\_context\_ptr\_t spectx )

`_base_spe_context_destroy` cleans up what is left when an SPE executable has exited. Closes open file handles and unmaps memory areas.

#### Parameters

<code>spectx</code>	Specifies the SPE context
---------------------	---------------------------

Definition at line 418 of file create.c.

References `__spe_context_update_event()`.

```

{
    int ret = free_spe_context(spe);
    __spe_context_update_event();
    return ret;
}

```

Here is the call graph for this function:



### 3.3.1.4 void \_base\_spe\_context\_lock ( spe\_context\_ptr\_t spe, enum fd\_name fd )

`_base_spe_context_lock` locks members of the SPE context

#### Parameters

<code>spectx</code>	Specifies the SPE context
<code>fd</code>	Specifies the file

Definition at line 91 of file create.c.

References spe\_context::base\_private, and spe\_context\_base\_priv::fd\_lock.

Referenced by \_base\_spe\_close\_if\_open(), and \_base\_spe\_open\_if\_closed().

```
{
    pthread_mutex_lock(&spe->base_private->fd_lock[fdesc]);
}
```

### 3.3.1.5 void \_base\_spe\_context\_unlock ( spe\_context\_ptr\_t spe, enum fd\_name fd )

\_base\_spe\_context\_unlock unlocks members of the SPE context

#### Parameters

<i>spectx</i>	Specifies the SPE context
<i>fd</i>	Specifies the file

Definition at line 96 of file create.c.

References spe\_context::base\_private, and spe\_context\_base\_priv::fd\_lock.

Referenced by \_base\_spe\_close\_if\_open(), and \_base\_spe\_open\_if\_closed().

```
{
    pthread_mutex_unlock(&spe->base_private->fd_lock[fdesc]);
}
```

### 3.3.1.6 spe\_gang\_context\_ptr\_t base\_spe\_gang\_context\_create ( unsigned int flags )

creates the directory in SPUFS that will contain all SPEs that are considered a gang Note: I would like to generalize this to a "group" or "set" Additional attributes maintained at the group level should be used to define scheduling constraints such "temporal" (e.g., scheduled all at the same time, i.e., a gang) "topology" (e.g., "closeness" of SPEs for optimal communication)

Definition at line 376 of file create.c.

References spe\_gang\_context::base\_private, DEBUG\_PRINTF, and spe\_gang\_context\_base\_priv::gangname.

```
{
    char pathname[256];
    struct spe_gang_context_base_priv *pgctx = NULL;
    struct spe_gang_context *gctx = NULL;

    gctx = malloc(sizeof(*gctx));
    if (!gctx) {
        DEBUG_PRINTF("ERROR: Could not allocate spe context.\n");
        return NULL;
    }
    memset(gctx, 0, sizeof(*gctx));

    pgctx = malloc(sizeof(*pgctx));
    if (!pgctx) {
        DEBUG_PRINTF("ERROR: Could not allocate spe context.\n");
        free(gctx);
        return NULL;
    }
}
```

```

    memset(pgctx, 0, sizeof(*pgctx));

    gctx->base_private = pgctx;

    sprintf(gctx->base_private->gangname, "gang-%i-%lu", getpid(),
            (unsigned long)gctx);
    sprintf(pathname, "/spu/%s", gctx->base_private->gangname);

    gctx->base_private->fd_gang_dir = spu_create(pathname, SPU_CREATE GANG,
                                                    S_IRUSR | S_IWUSR | S_IXUSR);

    if (gctx->base_private->fd_gang_dir < 0) {
        DEBUG_PRINTF("ERROR: Could not create Gang %s\n", pathname);
        free_spe_gang_context(gctx);
        errno = EFAULT;
        return NULL;
    }

    gctx->base_private->flags = flags;

    return gctx;
}

```

### 3.3.1.7 int \_base\_spe\_gang\_context\_destroy ( spe\_gang\_context\_ptr\_t gctx )

\_base\_spe\_gang\_context\_destroy destroys a gang context and frees associated resources

#### Parameters

<i>gctx</i>	Specifies the SPE gang context
-------------	--------------------------------

Definition at line 427 of file create.c.

```
{
    return free_spe_gang_context(gctx);
}
```

### 3.3.1.8 int \_base\_spe\_open\_if\_closed ( struct spe\_context \* spe, enum fd\_name fdesc, int locked )

Definition at line 101 of file create.c.

References \_base\_spe\_context\_lock(), \_base\_spe\_context\_unlock(), spe\_context::base\_private, spe\_context\_base\_priv::fd\_spe\_dir, fd\_attr::mode, fd\_attr::name, spe\_context\_base\_priv::spe\_fds\_array, and spe\_context\_base\_priv::spe\_fds\_refcount.

Referenced by \_\_base\_spe\_event\_source\_acquire(), \_base\_spe\_in\_mbox\_status(), \_base\_spe\_in\_mbox\_write(), \_base\_spe\_mssync\_start(), \_base\_spe\_mssync\_status(), \_base\_spe\_out\_intr\_mbox\_read(), \_base\_spe\_out\_intr\_mbox\_status(), \_base\_spe\_out\_mbox\_read(), \_base\_spe\_out\_mbox\_status(), and \_base\_spe\_signal\_write().

```
{
    if (!locked)
        _base_spe_context_lock(spe, fdesc);

    /* already open? */
    if (spe->base_private->spe_fds_array[fdesc] != -1) {
        spe->base_private->spe_fds_refcount[fdesc]++;
    } else {
```

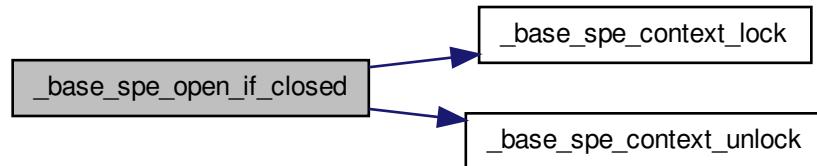
```
spe->base_private->spe_fds_array[fdesc] =
    openat(spe->base_private->fd_spe_dir,
           spe_fd_attr[fdesc].name,
           spe_fd_attr[fdesc].mode);

if (spe->base_private->spe_fds_array[(int)fdesc] > 0)
    spe->base_private->spe_fds_refcount[(int)fdesc]++;
}

if (!locked)
    _base_spe_context_unlock(spe, fdesc);

return spe->base_private->spe_fds_array[(int)fdesc];
}
```

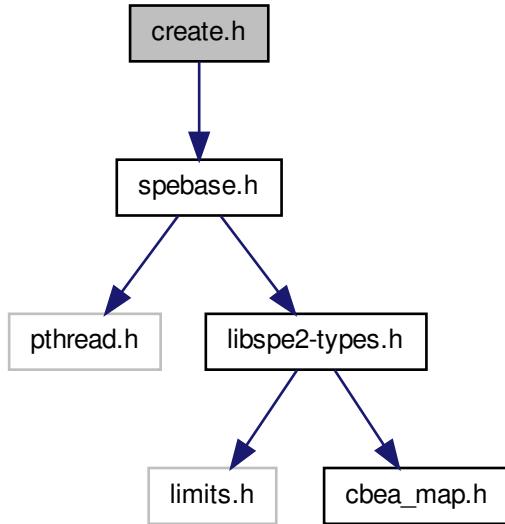
Here is the call graph for this function:



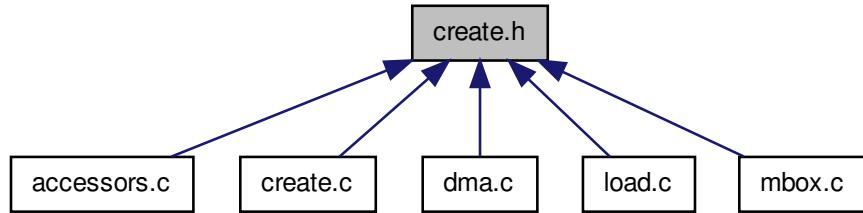
## 3.4 create.h File Reference

```
#include "spebase.h"
```

Include dependency graph for create.h:



This graph shows which files directly or indirectly include this file:



## Functions

- `int _base_spe_open_if_closed (struct spe_context *spe, enum fd_name fdesc, int locked)`
- `void _base_spe_close_if_open (struct spe_context *spe, enum fd_name fdesc)`

### 3.4.1 Function Documentation

### 3.4.1.1 void \_base\_spe\_close\_if\_open ( struct spe\_context \* spe, enum fd\_name fdesc )

Definition at line 125 of file create.c.

References \_base\_spe\_context\_lock(), \_base\_spe\_context\_unlock(), spe\_context::base\_private, spe\_context\_base\_priv::spe\_fds\_array, and spe\_context\_base\_priv::spe\_fds\_refcount.

Referenced by \_\_base\_spe\_event\_source\_release(), and \_\_base\_spe\_signal\_write().

```
{
    _base_spe_context_lock(spe, fdesc);

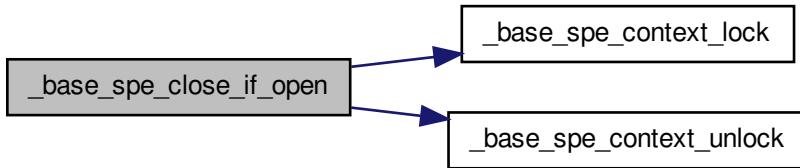
    if (spe->base_private->spe_fds_array[(int)fdesc] != -1 &&
        spe->base_private->spe_fds_refcount[(int)fdesc] == 1) {

        spe->base_private->spe_fds_refcount[(int)fdesc]--;
        close(spe->base_private->spe_fds_array[(int)fdesc]);

        spe->base_private->spe_fds_array[(int)fdesc] = -1;
    } else if (spe->base_private->spe_fds_refcount[(int)fdesc] > 0) {
        spe->base_private->spe_fds_refcount[(int)fdesc]--;
    }

    _base_spe_context_unlock(spe, fdesc);
}
```

Here is the call graph for this function:



### 3.4.1.2 int \_base\_spe\_open\_if\_closed ( struct spe\_context \* spe, enum fd\_name fdesc, int locked )

Definition at line 101 of file create.c.

References \_base\_spe\_context\_lock(), \_base\_spe\_context\_unlock(), spe\_context::base\_private, spe\_context\_base\_priv::fd\_spe\_dir, fd\_attr::mode, fd\_attr::name, spe\_context\_base\_priv::spe\_fds\_array, and spe\_context\_base\_priv::spe\_fds\_refcount.

Referenced by \_\_base\_spe\_event\_source\_acquire(), \_\_base\_spe\_in\_mbox\_status(), \_\_base\_spe\_in\_mbox\_write(), \_\_base\_spe\_mssync\_start(), \_\_base\_spe\_mssync\_status(), \_\_base\_spe\_out\_intr\_mbox\_read(), \_\_base\_spe\_out\_intr\_mbox\_status(), \_\_base\_spe\_out\_mbox\_read(), \_\_base\_spe\_out\_mbox\_status(), and \_\_base\_spe\_signal\_write().

```
{
    if (!locked)
```

```

        _base_spe_context_lock(spe, fdesc);

/* already open? */
if (spe->base_private->spe_fds_array[fdesc] != -1) {
    spe->base_private->spe_fds_refcount[fdesc]++;
} else {
    spe->base_private->spe_fds_array[fdesc] =
        openat(spe->base_private->fd_spe_dir,
                spe_fd_attr[fdesc].name,
                spe_fd_attr[fdesc].mode);

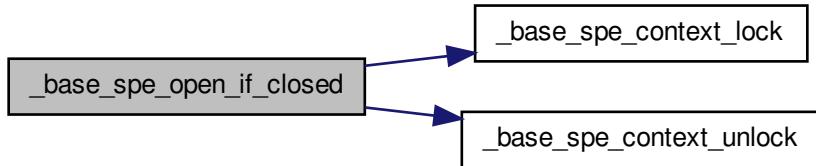
    if (spe->base_private->spe_fds_array[(int)fdesc] > 0)
        spe->base_private->spe_fds_refcount[(int)fdesc]++;
}

if (!locked)
    _base_spe_context_unlock(spe, fdesc);

return spe->base_private->spe_fds_array[(int)fdesc];
}

```

Here is the call graph for this function:



## 3.5 design.txt File Reference

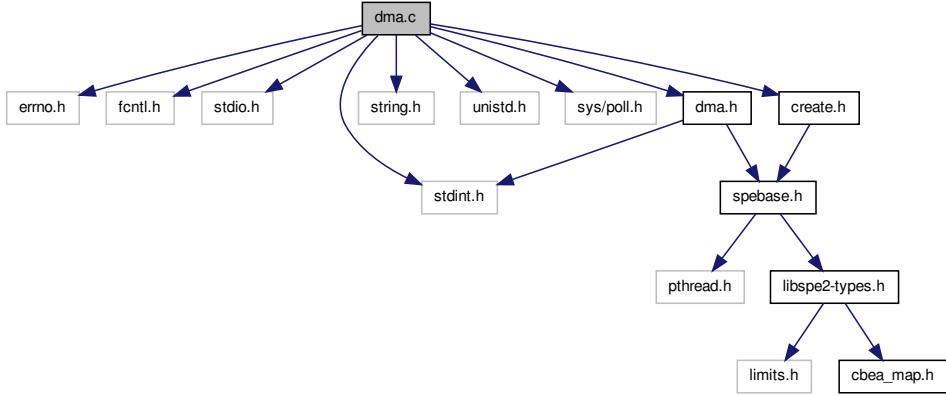
## 3.6 dma.c File Reference

```

#include <errno.h>
#include <fcntl.h>
#include <stdio.h>
#include <stdint.h>
#include <string.h>
#include <unistd.h>
#include <sys/poll.h>
#include "create.h"
#include "dma.h"

```

Include dependency graph for dma.c:



## Functions

- int `_base_spe_mfcio_put` (`spe_context_ptr_t` spectx, unsigned int ls, void \*ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)
- int `_base_spe_mfcio_putb` (`spe_context_ptr_t` spectx, unsigned int ls, void \*ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)
- int `_base_spe_mfcio_putf` (`spe_context_ptr_t` spectx, unsigned int ls, void \*ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)
- int `_base_spe_mfcio_get` (`spe_context_ptr_t` spectx, unsigned int ls, void \*ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)
- int `_base_spe_mfcio_getb` (`spe_context_ptr_t` spectx, unsigned int ls, void \*ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)
- int `_base_spe_mfcio_getf` (`spe_context_ptr_t` spectx, unsigned int ls, void \*ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)
- int `_base_spe_mfcio_tag_status_read` (`spe_context_ptr_t` spectx, unsigned int mask, unsigned int behavior, unsigned int \*tag\_status)
- int `_base_spe_mssync_start` (`spe_context_ptr_t` spectx)
- int `_base_spe_mssync_status` (`spe_context_ptr_t` spectx)

### 3.6.1 Function Documentation

#### 3.6.1.1 int `_base_spe_mfcio_get` ( `spe_context_ptr_t spectx`, `unsigned int ls`, `void * ea`, `unsigned int size`, `unsigned int tag`, `unsigned int tid`, `unsigned int rid` )

The `_base_spe_mfcio_get` function places a get DMA command on the proxy command queue of the SPE thread specified by speid. The get command transfers size bytes of data starting at the effective address specified by ea to the local store address specified by ls. The DMA is identified by the tag id specified by tag and performed according to the transfer class and replacement class specified by tid and rid respectively.

#### Parameters

<code>spectx</code>	Specifies the SPE context
---------------------	---------------------------

<i>ls</i>	Specifies the starting local store destination address.
<i>ea</i>	Specifies the starting effective address source address.
<i>size</i>	Specifies the size, in bytes, to be transferred.
<i>tag</i>	Specifies the tag id used to identify the DMA command.
<i>tid</i>	Specifies the transfer class identifier of the DMA command.
<i>rid</i>	Specifies the replacement class identifier of the DMA command.

**Returns**

On success, return 0. On failure, -1 is returned.

Definition at line 160 of file dma.c.

References MFC\_CMD\_GET.

```
{
    return spe_do_mfc_get(spectx, ls, ea, size, tag, tid, rid, MFC_CMD_GET);
}
```

### 3.6.1.2 int \_base\_spe\_mfcio\_getb ( spe\_context\_ptr\_t *spectx*, unsigned int *ls*, void \* *ea*, unsigned int *size*, unsigned int *tag*, unsigned int *tid*, unsigned int *rid* )

The \_base\_spe\_mfcio\_getb function is identical to \_base\_spe\_mfcio\_get except that it places a getb (get with barrier) DMA command on the proxy command queue. The barrier form ensures that this command and all sequence commands with the same tag identifier as this command are locally ordered with respect to all previously issued commands with the same tag group and command queue.

**Parameters**

<i>spectx</i>	Specifies the SPE context
<i>ls</i>	Specifies the starting local store destination address.
<i>ea</i>	Specifies the starting effective address source address.
<i>size</i>	Specifies the size, in bytes, to be transferred.
<i>tag</i>	Specifies the tag id used to identify the DMA command.
<i>tid</i>	Specifies the transfer class identifier of the DMA command.
<i>rid</i>	Specifies the replacement class identifier of the DMA command.

**Returns**

On success, return 0. On failure, -1 is returned.

Definition at line 171 of file dma.c.

References MFC\_CMD\_GETB.

```
{
    return spe_do_mfc_get(spectx, ls, ea, size, tag, rid, rid, MFC_CMD_GETB);
}
```

### 3.6.1.3 int \_base\_spe\_mfcio\_getf ( spe\_context\_ptr\_t *spectx*, unsigned int *ls*, void \* *ea*, unsigned int *size*, unsigned int *tag*, unsigned int *tid*, unsigned int *rid* )

The \_base\_spe\_mfcio\_getf function is identical to \_base\_spe\_mfcio\_get except that it places a getf (get with fence) DMA command on the proxy command queue. The fence form ensure that this command is locally ordered with respect to all previously issued commands with the same tag group and command queue.

#### Parameters

<i>spectx</i>	Specifies the SPE context
<i>ls</i>	Specifies the starting local store destination address.
<i>ea</i>	Specifies the starting effective address source address.
<i>size</i>	Specifies the size, in bytes, to be transferred.
<i>tag</i>	Specifies the tag id used to identify the DMA command.
<i>tid</i>	Specifies the transfer class identifier of the DMA command.
<i>rid</i>	Specifies the replacement class identifier of the DMA command.

#### Returns

On success, return 0. On failure, -1 is returned.

Definition at line 182 of file dma.c.

References MFC\_CMD\_GETF.

```
{
    return spe_do_mfc_get(spectx, ls, ea, size, tag, tid, rid, MFC_CMD_GETF);
}
```

### 3.6.1.4 int \_base\_spe\_mfcio\_put ( spe\_context\_ptr\_t *spectx*, unsigned int *ls*, void \* *ea*, unsigned int *size*, unsigned int *tag*, unsigned int *tid*, unsigned int *rid* )

The \_base\_spe\_mfcio\_put function places a put DMA command on the proxy command queue of the SPE thread specified by speid. The put command transfers size bytes of data starting at the local store address specified by ls to the effective address specified by ea. The DMA is identified by the tag id specified by tag and performed according transfer class and replacement class specified by tid and rid respectively.

#### Parameters

<i>spectx</i>	Specifies the SPE context
<i>ls</i>	Specifies the starting local store destination address.
<i>ea</i>	Specifies the starting effective address source address.
<i>size</i>	Specifies the size, in bytes, to be transferred.
<i>tag</i>	Specifies the tag id used to identify the DMA command.
<i>tid</i>	Specifies the transfer class identifier of the DMA command.
<i>rid</i>	Specifies the replacement class identifier of the DMA command.

#### Returns

On success, return 0. On failure, -1 is returned.

Definition at line 126 of file dma.c.

References MFC\_CMD\_PUT.

```
{
    return spe_do_mfc_put(spectx, ls, ea, size, tag, tid, rid, MFC_CMD_PUT);
}
```

### 3.6.1.5 int \_base\_spe\_mfcio\_putb ( spe\_context\_ptr\_t spectx, unsigned int ls, void \* ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid )

The \_base\_spe\_mfcio\_putb function is identical to \_base\_spe\_mfcio\_put except that it places a putb (put with barrier) DMA command on the proxy command queue. The barrier form ensures that this command and all sequence commands with the same tag identifier as this command are locally ordered with respect to all previously issued commands with the same tag group and command queue.

#### Parameters

<i>spectx</i>	Specifies the SPE context
<i>ls</i>	Specifies the starting local store destination address.
<i>ea</i>	Specifies the starting effective address source address.
<i>size</i>	Specifies the size, in bytes, to be transferred.
<i>tag</i>	Specifies the tag id used to identify the DMA command.
<i>tid</i>	Specifies the transfer class identifier of the DMA command.
<i>rid</i>	Specifies the replacement class identifier of the DMA command.

#### Returns

On success, return 0. On failure, -1 is returned.

Definition at line 137 of file dma.c.

References MFC\_CMD\_PUTB.

```
{
    return spe_do_mfc_put(spectx, ls, ea, size, tag, tid, rid, MFC_CMD_PUTB);
}
```

### 3.6.1.6 int \_base\_spe\_mfcio\_putf ( spe\_context\_ptr\_t spectx, unsigned int ls, void \* ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid )

The \_base\_spe\_mfcio\_putf function is identical to \_base\_spe\_mfcio\_put except that it places a putf (put with fence) DMA command on the proxy command queue. The fence form ensures that this command is locally ordered with respect to all previously issued commands with the same tag group and command queue.

#### Parameters

<i>spectx</i>	Specifies the SPE context
<i>ls</i>	Specifies the starting local store destination address.
<i>ea</i>	Specifies the starting effective address source address.
<i>size</i>	Specifies the size, in bytes, to be transferred.
<i>tag</i>	Specifies the tag id used to identify the DMA command.
<i>tid</i>	Specifies the transfer class identifier of the DMA command.
<i>rid</i>	Specifies the replacement class identifier of the DMA command.

**Returns**

On success, return 0. On failure, -1 is returned.

Definition at line 148 of file dma.c.

References MFC\_CMD\_PUTF.

```
{
    return spe_do_mfc_put(spectx, ls, ea, size, tag, tid, rid, MFC_CMD_PUTF);
}
```

### **3.6.1.7 int \_base\_spe\_mfcio\_tag\_status\_read ( spe\_context\_ptr\_t spectx, unsigned int mask, unsigned int behavior, unsigned int \* tag\_status )**

\_base\_spe\_mfcio\_tag\_status\_read

No Idea

Definition at line 307 of file dma.c.

References spe\_context\_base\_priv::active\_tagmask, spe\_context::base\_private, spe\_context\_base\_priv::flags, SPE\_MAP\_PS, SPE\_TAG\_ALL, SPE\_TAG\_ANY, and SPE\_TAG\_IMMEDIATE.

```
{
    if ( mask != 0 ) {
        if (!(spectx->base_private->flags & SPE_MAP_PS))
            mask = 0;
    } else {
        if ((spectx->base_private->flags & SPE_MAP_PS))
            mask = spectx->base_private->active_tagmask;
    }

    if (!tag_status) {
        errno = EINVAL;
        return -1;
    }

    switch (behavior) {
    case SPE_TAG_ALL:
        return spe_mfcio_tag_status_read_all(spectx, mask, tag_status);
    case SPE_TAG_ANY:
        return spe_mfcio_tag_status_read_any(spectx, mask, tag_status);
    case SPE_TAG_IMMEDIATE:
        return spe_mfcio_tag_status_read_immediate(spectx, mask, tag_status);
    default:
        errno = EINVAL;
        return -1;
    }
}
```

### **3.6.1.8 int \_base\_spe\_mssync\_start ( spe\_context\_ptr\_t spectx )**

\_base\_spe\_mssync\_start starts Multisource Synchronisation

**Parameters**

<i>spectx</i>	Specifies the SPE context
---------------	---------------------------

Definition at line 335 of file dma.c.

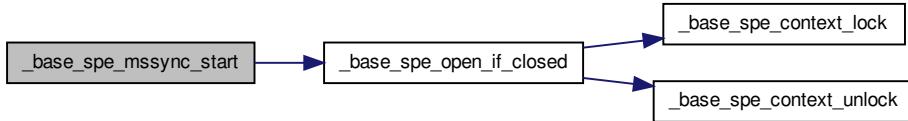
References `_base_spe_open_if_closed()`, `spe_context::base_private`, `FD_MSS`, `spe_context_base_priv::flags`, `spe_mssync_area::MFC_MSSync`, `spe_context_base_priv::mssync_mmap_base`, and `SPE_MAP_PS`.

```
{
    int ret, fd;
    unsigned int data = 1; /* Any value can be written here */

    volatile struct spe_mssync_area *mss_area =
        spectx->base_private->mssync_mmap_base;

    if (spectx->base_private->flags & SPE_MAP_PS) {
        mss_area->MFC_MSSync = data;
        return 0;
    } else {
        fd = _base_spe_open_if_closed(spectx, FD_MSS, 0);
        if (fd != -1) {
            ret = write(fd, &data, sizeof (data));
            if ((ret < 0) && (errno != EIO)) {
                perror("spe_mssync_start: internal error");
            }
            return ret < 0 ? -1 : 0;
        } else
            return -1;
    }
}
```

Here is the call graph for this function:



### 3.6.1.9 int \_base\_spe\_mssync\_status ( spe\_context\_ptr\_t *spectx* )

`_base_spe_mssync_status` retrieves status of Multisource Synchronisation

#### Parameters

<code>spectx</code>	Specifies the SPE context
---------------------	---------------------------

Definition at line 359 of file dma.c.

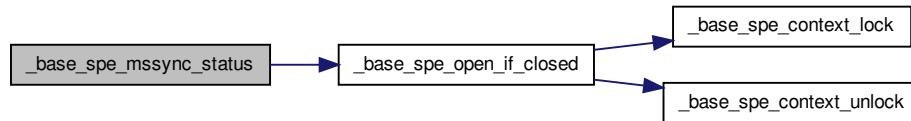
References `_base_spe_open_if_closed()`, `spe_context::base_private`, `FD_MSS`, `spe_context_base_priv::flags`, `spe_mssync_area::MFC_MSSync`, `spe_context_base_priv::mssync_mmap_base`, and `SPE_MAP_PS`.

```
{
    int ret, fd;
    unsigned int data;
```

```
volatile struct spe_mssync_area *mss_area =
    spectx->base_private->mssync_mmap_base;

if (spectx->base_private->flags & SPE_MAP_PS) {
    return mss_area->MFC_MSSync;
} else {
    fd = _base_spe_open_if_closed(spectx, FD_MSS, 0);
    if (fd != -1) {
        ret = read(fd, &data, sizeof (data));
        if ((ret < 0) && (errno != EIO)) {
            perror("spe_mssync_start: internal error");
        }
        return ret < 0 ? -1 : data;
    } else
        return -1;
}
```

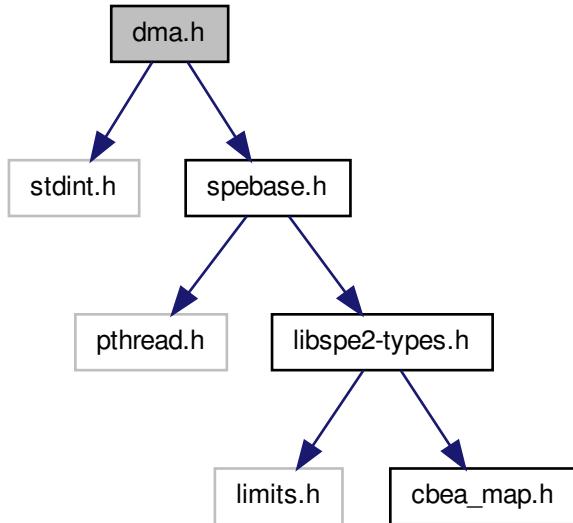
Here is the call graph for this function:



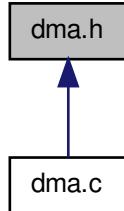
## 3.7 dma.h File Reference

```
#include <stdint.h>
#include "spebase.h"
```

Include dependency graph for dma.h:



This graph shows which files directly or indirectly include this file:



## Data Structures

- struct [mfc\\_command\\_parameter\\_area](#)

## Enumerations

- enum [mfc\\_cmd](#) {

```
MFC_CMD_PUT = 0x20, MFC_CMD_PUTB = 0x21, MFC_CMD_PUTF = 0x22, MFC_CMD_-
GET = 0x40,
MFC_CMD_GETB = 0x41, MFC_CMD_GETF = 0x42 }
```

### 3.7.1 Enumeration Type Documentation

#### 3.7.1.1 enum mfc\_cmd

**Enumerator:**

```
MFC_CMD_PUT
MFC_CMD_PUTB
MFC_CMD_PUTF
MFC_CMD_GET
MFC_CMD_GETB
MFC_CMD_GETF
```

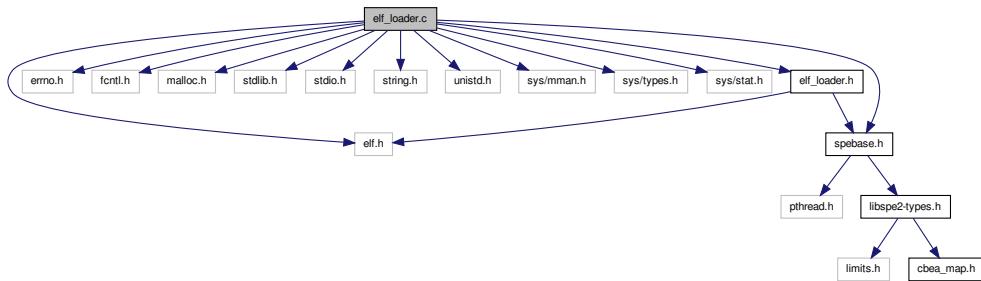
Definition at line 37 of file dma.h.

```
{
MFC_CMD_PUT    = 0x20,
MFC_CMD_PUTB   = 0x21,
MFC_CMD_PUTF   = 0x22,
MFC_CMD_GET    = 0x40,
MFC_CMD_GETB   = 0x41,
MFC_CMD_GETF   = 0x42,
};
```

## 3.8 elf\_loader.c File Reference

```
#include <elf.h>
#include <errno.h>
#include <fcntl.h>
#include <malloc.h>
#include <stdlib.h>
#include <stdio.h>
#include <string.h>
#include <unistd.h>
#include <sys/mman.h>
#include <sys/types.h>
#include <sys/stat.h>
#include "elf_loader.h"
#include "spebase.h"
```

Include dependency graph for elf\_loader.c:



## Defines

- #define **\_PRINTF**(fmt, args...) { fprintf(stderr,fmt , ## args); }
- #define **DEBUG\_PRINTF**(fmt, args...)
- #define **TAG**

## Functions

- int \_base\_spe\_verify\_spe\_elf\_image (spe\_program\_handle\_t \*handle)
- int \_base\_spe\_parse\_isolated\_elf (spe\_program\_handle\_t \*handle, uint64\_t \*addr, uint32\_t \*size)
- int \_base\_spe\_load\_spe\_elf (spe\_program\_handle\_t \*handle, void \*ld\_buffer, struct spe\_ld\_info \*ld\_info)
- int \_base\_spe\_toe\_ear (spe\_program\_handle\_t \*speh)

### 3.8.1 Define Documentation

#### 3.8.1.1 #define **\_PRINTF( fmt, args... )** { fprintf(stderr,fmt , ## args); }

Definition at line 40 of file elf\_loader.c.

#### 3.8.1.2 #define **DEBUG\_PRINTF( fmt, args... )**

Definition at line 45 of file elf\_loader.c.

Referenced by \_base\_spe\_context\_create(), \_base\_spe\_context\_run(), \_base\_spe\_count\_physical\_cpus(), \_base\_spe\_count\_physical\_spes(), \_base\_spe\_gang\_context\_create(), \_base\_spe\_handle\_library\_callback(), \_base\_spe\_load\_spe\_elf(), \_base\_spe\_out mbox\_read(), \_base\_spe\_parse\_isolated\_elf(), \_base\_spe\_program\_load(), and \_base\_spe\_program\_load\_complete().

#### 3.8.1.3 #define **TAG**

Definition at line 46 of file elf\_loader.c.

### 3.8.2 Function Documentation

#### 3.8.2.1 int \_base\_spe\_load\_spe\_elf ( spe\_program\_handle\_t \* handle, void \* ld\_buffer, struct spe\_ld\_info \* ld\_info )

Definition at line 201 of file elf\_loader.c.

References DEBUG\_PRINTF, spe\_program\_handle::elf\_image, and spe\_ld\_info::entry.

Referenced by \_base\_spe\_program\_load().

```
{
    Elf32_Ehdr *ehdr;
    Elf32_Phdr *phdr;
    Elf32_Phdr *ph, *prev_ph;

    Elf32_Shdr *shdr;
    Elf32_Shdr *sh;

    Elf32_Off toe_addr = 0;
    long toe_size = 0;

    char* str_table = 0;

    int num_load_seg = 0;
    void *elf_start;
    int ret;

    DEBUG_PRINTF ("load_spe_elf(%p, %p)\n", handle, ld_buffer);

    elf_start = handle->elf_image;

    DEBUG_PRINTF ("load_spe_elf(%p, %p)\n", handle->elf_image, ld_buffer);
    ehdr = (Elf32_Ehdr *) (handle->elf_image);

    /* Check for a Valid SPE ELF Image (again) */
    if ((ret=check_spe_elf(ehdr)))
        return ret;

    /* Start processing headers */
    phdr = (Elf32_Phdr *) ((char *) ehdr + ehdr->e_phoff);
    shdr = (Elf32_Shdr *) ((char *) ehdr + ehdr->e_shoff);
    str_table = (char*)ehdr + shdr[ehdr->e_shstrndx].sh_offset;

    /* traverse the sections to locate the toe segment */
    /* by specification, the toe sections are grouped together in a segment */
    /
    for (sh = shdr; sh < &shdr[ehdr->e_shnum]; ++sh)
    {
        DEBUG_PRINTF("section name: %s ( start: 0x%04x, size: 0x%04x)\n",
        str_table+sh->sh_name, sh->sh_offset, sh->sh_size );
        if (strcmp(".toe", str_table+sh->sh_name) == 0) {
            DEBUG_PRINTF("section offset: %d\n", sh->sh_offset);
            toe_size += sh->sh_size;
            if ((toe_addr == 0) || (toe_addr > sh->sh_addr))
                toe_addr = sh->sh_addr;
        }
        /* Disabled : Actually not needed, only good for testing
        if (strcmp(".bss", str_table+sh->sh_name) == 0) {
            DEBUG_PRINTF("zeroing .bss section:\n");
            DEBUG_PRINTF("section offset: 0x%04x\n", sh->sh_offset);
            DEBUG_PRINTF("section size: 0x%04x\n", sh->sh_size);
            memset(ld_buffer + sh->sh_offset, 0, sh->sh_size);
        } */
    }

#endif DEBUG
```

```

        if (strcmp(".note.spu_name", str_table+sh->sh_name) == 0)
            display_debug_output(elf_start, sh);
#endif /*DEBUG*/
    }

/*
 * Load all PT_LOAD segments onto the SPE local store buffer.
 */
DEBUG_PRINTF("Segments: 0x%x\n", ehdr->e_phnum);
for (ph = phdr, prev_ph = NULL; ph < &phdr[ehdr->e_phnum]; ++ph) {
    switch (ph->p_type) {
        case PT_LOAD:
            if (!overlay(ph, prev_ph)) {
                if (ph->p_filesz < ph->p_memsz) {
                    DEBUG_PRINTF("padding loaded image with z
eros:\n");
                    DEBUG_PRINTF("start: 0x%04x\n", ph->p_vaddr
dr + ph->p_filesz);
                    DEBUG_PRINTF("length: 0x%04x\n", ph->p_me
msz - ph->p_filesz);
                    memset(ld_buffer + ph->p_vaddr + ph->p_fi
lesz, 0, ph->p_memsz - ph->p_filesz);
                }
                copy_to_ld_buffer(handle, ld_buffer, ph,
toe_addr, toe_size);
                num_load_seg++;
            }
            break;
        case PT_NOTE:
            DEBUG_PRINTF("SPE_LOAD found PT_NOTE\n");
            break;
    }
}
if (num_load_seg == 0)
{
    DEBUG_PRINTF ("no segments to load");
    errno = EINVAL;
    return -errno;
}

/* Remember where the code wants to be started */
ld_info->entry = ehdr->e_entry;
DEBUG_PRINTF ("entry = 0x%x\n", ehdr->e_entry);

return 0;
}

```

### 3.8.2.2 int \_base\_spe\_parse\_isolated\_elf( spe\_program\_handle\_t \* handle, uint64\_t \* addr, uint32\_t \* size )

Definition at line 111 of file elf\_loader.c.

References DEBUG\_PRINTF, and spe\_program\_handle::elf\_image.

```
{
Elf32_Ehdr *ehdr = (Elf32_Ehdr *)handle->elf_image;
Elf32_Phdr *phdr;

if (!ehdr) {
    DEBUG_PRINTF("No ELF image has been loaded\n");
    errno = EINVAL;
    return -errno;
}
```

```

    if (ehdr->e_phentsize != sizeof(*phdr)) {
        DEBUG_PRINTF("Invalid program header format (phdr size=%d)\n",
                     ehdr->e_phentsize);
        errno = EINVAL;
        return -errno;
    }

    if (ehdr->e_phnum != 1) {
        DEBUG_PRINTF("Invalid program header count (%d), expected 1\n",
                     ehdr->e_phnum);
        errno = EINVAL;
        return -errno;
    }

    phdr = (Elf32_Phdr *) (handle->elf_image + ehdr->e_phoff);

    if (phdr->p_type != PT_LOAD || phdr->p_memsz == 0) {
        DEBUG_PRINTF("SPE program segment is not loadable (type=%x)\n",
                     phdr->p_type);
        errno = EINVAL;
        return -errno;
    }

    if (addr)
        *addr = (uint64_t) (unsigned long)
            (handle->elf_image + phdr->p_offset);

    if (size)
        *size = phdr->p_memsz;

    return 0;
}

```

### 3.8.2.3 int \_base\_spe\_toe\_ear ( spe\_program\_handle\_t \* speh )

Definition at line 354 of file elf\_loader.c.

References spe\_program\_handle::elf\_image, and spe\_program\_handle::toe\_shadow.

Referenced by \_base\_spe\_image\_open().

```

{
    Elf32_Ehdr *ehdr;
    Elf32_Shdr *shdr, *sh;
    char *str_table;
    char **ch;
    int ret;
    long toe_size;

    ehdr = (Elf32_Ehdr*) (speh->elf_image);
    shdr = (Elf32_Shdr*) ((char*) ehdr + ehdr->e_shoff);
    str_table = (char*) ehdr + shdr[ehdr->e_shstrndx].sh_offset;

    toe_size = 0;
    for (sh = shdr; sh < &shdr[ehdr->e_shnum]; ++sh)
        if (strcmp(".toe", str_table + sh->sh_name) == 0)
            toe_size += sh->sh_size;

    ret = 0;
    if (toe_size > 0) {
        for (sh = shdr; sh < &shdr[ehdr->e_shnum]; ++sh)
            if (sh->sh_type == SHT_SYMTAB || sh->sh_type ==
                SHT_DYNSYM)
                ret = toe_check_syms(ehdr, sh);
    }
}

```

```

        if (!ret && toe_size != 16) {
            /* Paranoia */
            fprintf(stderr, "Unexpected toe size of %ld\n",
                    toe_size);
            errno = EINVAL;
            ret = 1;
        }
    }
    if (!ret && toe_size) {
        /*
         * Allocate toe_shadow, and fill it with elf_image.
         */
        speh->toe_shadow = malloc(toe_size);
        if (speh->toe_shadow) {
            ch = (char**) speh->toe_shadow;
            if (sizeof(char*) == 8) {
                ch[0] = (char*) speh->elf_image;
                ch[1] = 0;
            } else {
                ch[0] = 0;
                ch[1] = (char*) speh->elf_image;
                ch[2] = 0;
                ch[3] = 0;
            }
        } else {
            errno = ENOMEM;
            ret = 1;
        }
    }
    return ret;
}

```

### 3.8.2.4 int \_base\_spe\_verify\_spe\_elf\_image( spe\_program\_handle\_t \* handle )

verifies integrity of an SPE image

Definition at line 99 of file elf\_loader.c.

References spe\_program\_handle::elf\_image.

Referenced by \_base\_spe\_emulated\_loader\_present(), and \_base\_spe\_image\_open().

```

{
    Elf32_Ehdr *ehdr;
    void *elf_start;

    elf_start = handle->elf_image;
    ehdr = (Elf32_Ehdr *) (handle->elf_image);

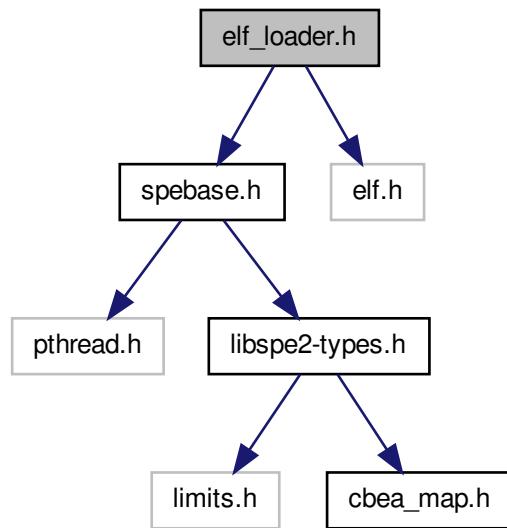
    return check_spe_elf(ehdr);
}

```

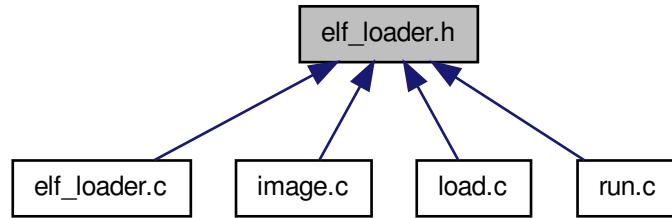
## 3.9 elf\_loader.h File Reference

```
#include "spebase.h"
#include <elf.h>
```

Include dependency graph for elf\_loader.h:



This graph shows which files directly or indirectly include this file:



## Data Structures

- union `addr64`
- struct `spe_ld_info`

## Defines

- #define `LS_SIZE` 0x40000

- #define **SPE\_LDR\_PROG\_start** (LS\_SIZE - 512)
- #define **SPE\_LDR\_PARAMS\_start** (LS\_SIZE - 128)

## Functions

- int \_base\_spe\_verify\_spe\_elf\_image (spe\_program\_handle\_t \*handle)
- int \_base\_spe\_load\_spe\_elf (spe\_program\_handle\_t \*handle, void \*ld\_buffer, struct spe\_ld\_info \*ld\_info)
- int \_base\_spe\_parse\_isolated\_elf (spe\_program\_handle\_t \*handle, uint64\_t \*addr, uint32\_t \*size)
- int \_base\_spe\_toe\_ear (spe\_program\_handle\_t \*speh)

### 3.9.1 Define Documentation

#### 3.9.1.1 #define LS\_SIZE 0x40000

Definition at line 23 of file elf\_loader.h.

Referenced by \_base\_spe\_context\_create(), \_base\_spe\_context\_run(), and \_base\_spe\_ls\_size\_get().

#### 3.9.1.2 #define SPE\_LDR\_PARAMS\_start (LS\_SIZE - 128)

Definition at line 26 of file elf\_loader.h.

#### 3.9.1.3 #define SPE\_LDR\_PROG\_start (LS\_SIZE - 512)

Definition at line 25 of file elf\_loader.h.

### 3.9.2 Function Documentation

#### 3.9.2.1 int \_base\_spe\_load\_spe\_elf ( spe\_program\_handle\_t \* handle, void \* ld\_buffer, struct spe\_ld\_info \* ld\_info )

Definition at line 201 of file elf\_loader.c.

References DEBUG\_PRINTF, spe\_program\_handle::elf\_image, and spe\_ld\_info::entry.

Referenced by \_base\_spe\_program\_load().

```
{
    Elf32_Ehdr *ehdr;
    Elf32_Phdr *phdr;
    Elf32_Phdr *ph, *prev_ph;

    Elf32_Shdr *shdr;
    Elf32_Shdr *sh;

    Elf32_Off toe_addr = 0;
    long toe_size = 0;

    char* str_table = 0;

    int num_load_seg = 0;
    void *elf_start;
    int ret;
```

```

    DEBUG_PRINTF ("load_spe_elf(%p, %p)\n", handle, ld_buffer);

    elf_start = handle->elf_image;

    DEBUG_PRINTF ("load_spe_elf(%p, %p)\n", handle->elf_image, ld_buffer);
    ehdr = (Elf32_Ehdr *) (handle->elf_image);

    /* Check for a Valid SPE ELF Image (again) */
    if ((ret=check_spe_elf(ehdr)))
        return ret;

    /* Start processing headers */
    phdr = (Elf32_Phdr *) ((char *) ehdr + ehdr->e_phoff);
    shdr = (Elf32_Shdr *) ((char *) ehdr + ehdr->e_shoff);
    str_table = (char*)ehdr + shdr[ehdr->e_shstrndx].sh_offset;

    /* traverse the sections to locate the toe segment */
    /* by specification, the toe sections are grouped together in a segment */
    /
    for (sh = shdr; sh < &shdr[ehdr->e_shnum]; ++sh)
    {
        DEBUG_PRINTF("section name: %s ( start: 0x%04x, size: 0x%04x)\n",
        str_table+sh->sh_name, sh->sh_offset, sh->sh_size );
        if (strcmp(".toe", str_table+sh->sh_name) == 0) {
            DEBUG_PRINTF("section offset: %d\n", sh->sh_offset);
            toe_size += sh->sh_size;
            if ((toe_addr == 0) || (toe_addr > sh->sh_addr))
                toe_addr = sh->sh_addr;
        }
        /* Disabled : Actually not needed, only good for testing
        if (strcmp(".bss", str_table+sh->sh_name) == 0) {
            DEBUG_PRINTF("zeroing .bss section:\n");
            DEBUG_PRINTF("section offset: 0x%04x\n", sh->sh_offset);
            DEBUG_PRINTF("section size: 0x%04x\n", sh->sh_size);
            memset(ld_buffer + sh->sh_offset, 0, sh->sh_size);
        }
    }

#endif DEBUG
    if (strcmp(".note.spu_name", str_table+sh->sh_name) == 0)
        display_debug_output(elf_start, sh);
#endif /*DEBUG*/
}

/*
 * Load all PT_LOAD segments onto the SPE local store buffer.
 */
DEBUG_PRINTF("Segments: 0x%x\n", ehdr->e_phnum);
for (ph = phdr, prev_ph = NULL; ph < &phdr[ehdr->e_phnum]; ++ph) {
    switch (ph->p_type) {
        case PT_LOAD:
            if (!overlay(ph, prev_ph)) {
                if (ph->p_filesz < ph->p_memsz) {
                    DEBUG_PRINTF("padding loaded image with zero\n");
                    DEBUG_PRINTF("start: 0x%04x\n", ph->p_vaddr +
                    ph->p_filesz);
                    DEBUG_PRINTF("length: 0x%04x\n", ph->p_memsz -
                    ph->p_filesz);
                    memset(ld_buffer + ph->p_vaddr + ph->p_filesz,
                    0, ph->p_memsz - ph->p_filesz);
                }
                copy_to_ld_buffer(handle, ld_buffer, ph,
                    toe_addr, toe_size);
                num_load_seg++;
            }
            break;
    }
}

```

```

        case PT_NOTE:
            DEBUG_PRINTF("SPE_LOAD found PT_NOTE\n");
            break;
    }
}

if (num_load_seg == 0)
{
    DEBUG_PRINTF ("no segments to load");
    errno = EINVAL;
    return -errno;
}

/* Remember where the code wants to be started */
ld_info->entry = ehdr->e_entry;
DEBUG_PRINTF ("entry = 0x%x\n", ehdr->e_entry);

return 0;
}

```

### 3.9.2.2 int \_base\_spe\_parse\_isolated\_elf( spe\_program\_handle\_t \* handle, uint64\_t \* addr, uint32\_t \* size )

Definition at line 111 of file elf\_loader.c.

References DEBUG\_PRINTF, and spe\_program\_handle::elf\_image.

```

{
    Elf32_Ehdr *ehdr = (Elf32_Ehdr *)handle->elf_image;
    Elf32_Phdr *phdr;

    if (!ehdr) {
        DEBUG_PRINTF("No ELF image has been loaded\n");
        errno = EINVAL;
        return -errno;
    }

    if (ehdr->e_phentsize != sizeof(*phdr)) {
        DEBUG_PRINTF("Invalid program header format (phdr size=%d)\n",
                     ehdr->e_phentsize);
        errno = EINVAL;
        return -errno;
    }

    if (ehdr->e_phnum != 1) {
        DEBUG_PRINTF("Invalid program header count (%d), expected 1\n",
                     ehdr->e_phnum);
        errno = EINVAL;
        return -errno;
    }

    phdr = (Elf32_Phdr *) (handle->elf_image + ehdr->e_phoff);

    if (phdr->p_type != PT_LOAD || phdr->p_memsz == 0) {
        DEBUG_PRINTF("SPE program segment is not loadable (type=%x)\n",
                     phdr->p_type);
        errno = EINVAL;
        return -errno;
    }

    if (addr)
        *addr = (uint64_t) (unsigned long)
            (handle->elf_image + phdr->p_offset);
}

```

```

    if (size)
        *size = phdr->p_memsz;

    return 0;
}

```

### 3.9.2.3 int \_base\_spe\_toe\_ear ( spe\_program\_handle\_t \* *speh* )

Definition at line 354 of file elf\_loader.c.

References spe\_program\_handle::elf\_image, and spe\_program\_handle::toe\_shadow.

Referenced by \_base\_spe\_image\_open().

```

{
    Elf32_Ehdr *ehdr;
    Elf32_Shdr *shdr, *sh;
    char *str_table;
    char **ch;
    int ret;
    long toe_size;

    ehdr = (Elf32_Ehdr*) (speh->elf_image);
    shdr = (Elf32_Shdr*) ((char*) ehdr + ehdr->e_shoff);
    str_table = (char*) ehdr + shdr[ehdr->e_shstrndx].sh_offset;

    toe_size = 0;
    for (sh = shdr; sh < &shdr[ehdr->e_shnum]; ++sh)
        if (strcmp(".toe", str_table + sh->sh_name) == 0)
            toe_size += sh->sh_size;

    ret = 0;
    if (toe_size > 0) {
        for (sh = shdr; sh < &shdr[ehdr->e_shnum]; ++sh)
            if (sh->sh_type == SHT_SYMTAB || sh->sh_type ==
                SHT_DYNSYM)
                ret = toe_check_syms(ehdr, sh);
        if (!ret && toe_size != 16) {
            /* Paranoia */
            fprintf(stderr, "Unexpected toe size of %ld\n",
                    toe_size);
            errno = EINVAL;
            ret = 1;
        }
    }
    if (!ret && toe_size) {
        /*
         * Allocate toe_shadow, and fill it with elf_image.
         */
        speh->toe_shadow = malloc(toe_size);
        if (speh->toe_shadow) {
            ch = (char**) speh->toe_shadow;
            if (sizeof(char*) == 8) {
                ch[0] = (char*) speh->elf_image;
                ch[1] = 0;
            } else {
                ch[0] = 0;
                ch[1] = (char*) speh->elf_image;
                ch[2] = 0;
                ch[3] = 0;
            }
        } else {
            errno = ENOMEM;
            ret = 1;
        }
    }
}

```

```

    }
    return ret;
}

```

### 3.9.2.4 int \_base\_spe\_verify\_spe\_elf\_image ( spe\_program\_handle\_t \* handle )

verifies integrity of an SPE image

Definition at line 99 of file elf\_loader.c.

References spe\_program\_handle::elf\_image.

Referenced by \_base\_spe\_emulated\_loader\_present(), and \_base\_spe\_image\_open().

```

{
    Elf32_Ehdr *ehdr;
    void *elf_start;

    elf_start = handle->elf_image;
    ehdr = (Elf32_Ehdr *) (handle->elf_image);

    return check_spe_elf(ehdr);
}

```

## 3.10 handler\_utils.h File Reference

### Data Structures

- struct [spe\\_reg128](#)

### Defines

- #define [LS\\_SIZE](#) 0x40000
- #define [LS\\_ADDR\\_MASK](#) ([LS\\_SIZE](#) - 1)
- #define [\\_\\_PRINTF](#)(fmt, args...) { [fprintf](#)([stderr](#), fmt , ## args); }
- #define [DEBUG\\_PRINTF](#)(fmt, args...)
- #define [LS\\_ARG\\_ADDR](#)(\_index) (&((struct [spe\\_reg128](#) \*) ((char \*) ls + ls\_args))[\_index])
- #define [DECL\\_RET](#)() struct [spe\\_reg128](#) \*ret = [LS\\_ARG\\_ADDR](#)(0)
- #define [GET\\_LS\\_PTR](#)(\_off) (void \*) ((char \*) ls + (\_off) & [LS\\_ADDR\\_MASK](#))
- #define [GET\\_LS\\_PTR\\_NULL](#)(\_off) (\_off) ? [GET\\_LS\\_PTR](#)(\_off) : NULL)
- #define [DECL\\_0\\_ARGS](#)() unsigned int ls\_args = (opdata & 0xffff)
- #define [DECL\\_1\\_ARGS](#)()
- #define [DECL\\_2\\_ARGS](#)()
- #define [DECL\\_3\\_ARGS](#)()
- #define [DECL\\_4\\_ARGS](#)()
- #define [DECL\\_5\\_ARGS](#)()
- #define [DECL\\_6\\_ARGS](#)()
- #define [PUT\\_LS\\_RC](#)(\_a, \_b, \_c, \_d)

### 3.10.1 Define Documentation

3.10.1.1 `#define __PRINTF( fmt, args... ) { fprintf(stderr,fmt ,## args); }`

Definition at line 32 of file handler\_utils.h.

3.10.1.2 `#define DEBUG_PRINTF( fmt, args... )`

Definition at line 36 of file handler\_utils.h.

3.10.1.3 `#define DECL_0_ARGS( ) unsigned int ls_args = (opdata & 0xffff)`

Definition at line 51 of file handler\_utils.h.

3.10.1.4 `#define DECL_1_ARGS( )`

**Value:**

```
DECL_0_ARGS(); \
    struct spe_reg128 *arg0 = LS_ARG_ADDR(0) \
```

Definition at line 54 of file handler\_utils.h.

3.10.1.5 `#define DECL_2_ARGS( )`

**Value:**

```
DECL_1_ARGS(); \
    struct spe_reg128 *arg1 = LS_ARG_ADDR(1) \
```

Definition at line 58 of file handler\_utils.h.

3.10.1.6 `#define DECL_3_ARGS( )`

**Value:**

```
DECL_2_ARGS(); \
    struct spe_reg128 *arg2 = LS_ARG_ADDR(2) \
```

Definition at line 62 of file handler\_utils.h.

3.10.1.7 `#define DECL_4_ARGS( )`

**Value:**

```
DECL_3_ARGS(); \
    struct spe_reg128 *arg3 = LS_ARG_ADDR(3) \
```

Definition at line 66 of file handler\_utils.h.

### 3.10.1.8 #define DECL\_5\_ARGS( )

**Value:**

```
DECL_4_ARGS();  
    struct spe_reg128 *arg4 = LS_ARG_ADDR(4)
```

Definition at line 70 of file handler\_utils.h.

### 3.10.1.9 #define DECL\_6\_ARGS( )

## Value:

```
DECL_5_ARGS();  
    struct spe_reg128 *arg5 = LS_ARG_ADDR(5)
```

Definition at line 74 of file handler\_utils.h.

3.10.1.10 #define DECL\_RET( ) struct spe\_reg128 \*ret = LS\_ARG\_ADDR(0)

Definition at line 42 of file `handler_utils.h`.

3.10.1.11 #define GET\_LS\_PTR( \_off ) (void \*) ((char \*) ls + ((off) & LS\_ADDR\_MASK))

Definition at line 45 of file handler\_utils.h.

3.10.1.12 #define GET\_LS\_PTR\_NULL( \_off ) (( \_off ) ? GET\_LS\_PTR(\_off) : NULL)

Definition at line 48 of file `handler_utils.h`.

3.10.1.13 #define LS\_ADDR\_MASK (LS\_SIZE - 1)

Definition at line 29 of file `handler_utils.h`.

3.10.1.14 #define LS\_ARG\_ADDR( \_index ) (&((struct spe\_reg128 \*) ((char \*) ls + ls\_args))[\_index])

Definition at line 39 of file handler\_utils.h.

3.10.1.15 #define LS\_SIZE 0x40000

Definition at line 28 of file `handler_utils.h`.

3.10.1.16 #define PUT\_LS\_RC( \_a, \_b, \_c, \_d )

## Value:

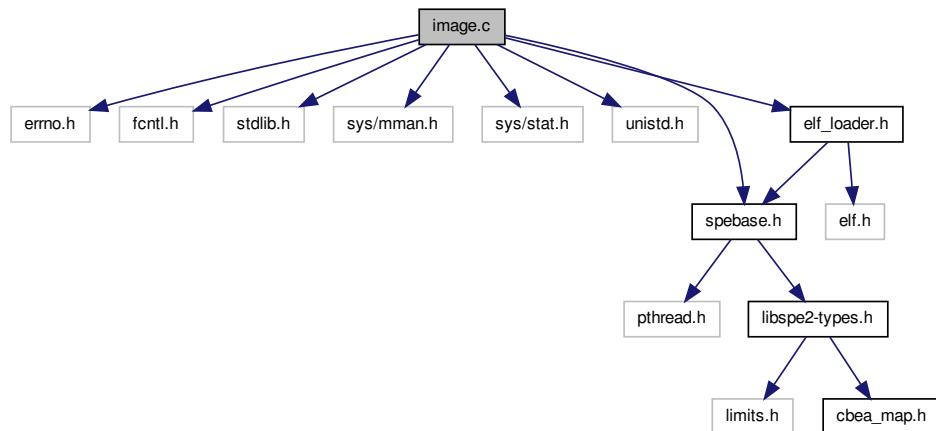
```
ret->slot[0] = (unsigned int) (_a); \\  
ret->slot[1] = (unsigned int) (_b); \\  
ret->slot[2] = (unsigned int) (_c); \\  
ret->slot[3] = (unsigned int) (_d); \\  
__asm__ __volatile__ ("sync" : : : "memory")
```

Definition at line 78 of file handler\_utils.h.

## 3.11 image.c File Reference

```
#include <errno.h>
#include <fcntl.h>
#include <stdlib.h>
#include <sys/mman.h>
#include <sys/stat.h>
#include <unistd.h>
#include "elf_loader.h"
#include "spebase.h"
```

Include dependency graph for image.c:



## Data Structures

- struct [image\\_handle](#)

## Functions

- [spe\\_program\\_handle\\_t \\* \\_base\\_spe\\_image\\_open](#) (const char \*filename)
- [int \\_base\\_spe\\_image\\_close](#) (spe\_program\_handle\_t \*handle)

### 3.11.1 Function Documentation

### 3.11.1.1 int \_base\_spe\_image\_close ( spe\_program\_handle\_t \* handle )

\_base\_spe\_image\_close unmaps an SPE ELF object that was previously mapped using spe\_open\_image.

#### Parameters

<i>handle</i>	handle to open file
---------------	---------------------

#### Return values

<i>0</i>	On success, spe_close_image returns 0.
<i>-1</i>	On failure, -1 is returned and errno is set appropriately. Possible values for errno: EINVAL From spe_close_image, this indicates that the file, specified by filename, was not previously mapped by a call to spe_open_image.

Definition at line 96 of file image.c.

References spe\_program\_handle::elf\_image, image\_handle::map\_size, image\_handle::speh, and spe\_program\_handle::toe\_shadow.

```
{
    int ret = 0;
    struct image_handle *ih;

    if (!handle) {
        errno = EINVAL;
        return -1;
    }

    ih = (struct image_handle *)handle;

    if (!ih->speh.elf_image || !ih->map_size) {
        errno = EINVAL;
        return -1;
    }

    if (ih->speh.toe_shadow)
        free(ih->speh.toe_shadow);

    ret = munmap(ih->speh.elf_image, ih->map_size );
    free(handle);

    return ret;
}
```

### 3.11.1.2 spe\_program\_handle\_t\* \_base\_spe\_image\_open ( const char \* filename )

\_base\_spe\_image\_open maps an SPE ELF executable indicated by filename into system memory and returns the mapped address appropriate for use by the spe\_create\_thread API. It is often more convenient/appropriate to use the loading methodologies where SPE ELF objects are converted to PPE static or shared libraries with symbols which point to the SPE ELF objects after these special libraries are loaded. These libraries are then linked with the associated PPE code to provide a direct symbol reference to the SPE ELF object. The symbols in this scheme are equivalent to the address returned from the spe\_open\_image function. SPE ELF objects loaded using this function are not shared with other processes, but SPE ELF objects loaded using the other scheme, mentioned above, can be shared if so desired.

#### Parameters

<i>filename</i>	Specifies the filename of an SPE ELF executable to be loaded and mapped into system memory.
-----------------	---

**Returns**

On success, `spe_open_image` returns the address at which the specified SPE ELF object has been mapped. On failure, `NULL` is returned and `errno` is set appropriately.

Possible values for `errno` include:

`EACCES` The calling process does not have permission to access the specified file.

`EFAULT` The `filename` parameter points to an address that was not contained in the calling process's address space.

A number of other `errno` values could be returned by the `open(2)`, `fstat(2)`, `mmap(2)`, `munmap(2)`, or `close(2)` system calls which may be utilized by the `spe_open_image` or `spe_close_image` functions.

**See also**

`spe_create_thread`

Definition at line 37 of file `image.c`.

References `_base_spe_toe_ear()`, `_base_spe_verify_spe_elf_image()`, `spe_program_handle::elf_image`, `spe_program_handle::handle_size`, `image_handle::map_size`, `image_handle::speh`, and `spe_program_handle::toe_shadow`.

```
{
    /* allocate an extra integer in the spe handle to keep the mapped size information */
    struct image_handle *ret;
    int binfd = -1, f_stat;
    struct stat statbuf;
    size_t ps = getpagesize ();

    ret = malloc(sizeof(struct image_handle));
    if (!ret)
        return NULL;

    ret->speh.handle_size = sizeof(spe_program_handle_t);
    ret->speh.toe_shadow = NULL;

    binfd = open(filename, O_RDONLY);
    if (binfd < 0)
        goto ret_err;

    f_stat = fstat(binfd, &statbuf);
    if (f_stat < 0)
        goto ret_err;

    /* Sanity: is it executable ?
     */
    if (!(statbuf.st_mode & (S_IXUSR | S_IXGRP | S_IXOTH))) {
        errno=EACCES;
        goto ret_err;
    }

    /* now store the size at the extra allocated space */
    ret->map_size = (statbuf.st_size + ps - 1) & ~(ps - 1);

    ret->speh.elf_image = mmap(NULL, ret->map_size,
                                PROT_WRITE | PROT_READ,
                                MAP_PRIVATE, binfd, 0);
    if (ret->speh.elf_image == MAP_FAILED)
```

```

        goto ret_err;

    /*Verify that this is a valid SPE ELF object*/
    if(( _base_spe_verify_spe_elf_image((spe_program_handle_t *)ret)))
        goto ret_err;

    if (_base_spe_toe_ear(&ret->speh))
        goto ret_err;

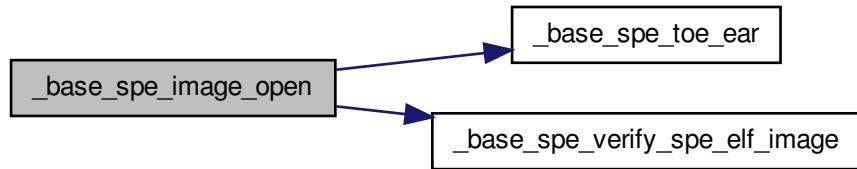
    /* ok */
    close(binfld);
    return (spe_program_handle_t *)ret;

    /* err & cleanup */
ret_err:
    if (binfd >= 0)
        close(binfd);

    free(ret);
    return NULL;
}

```

Here is the call graph for this function:



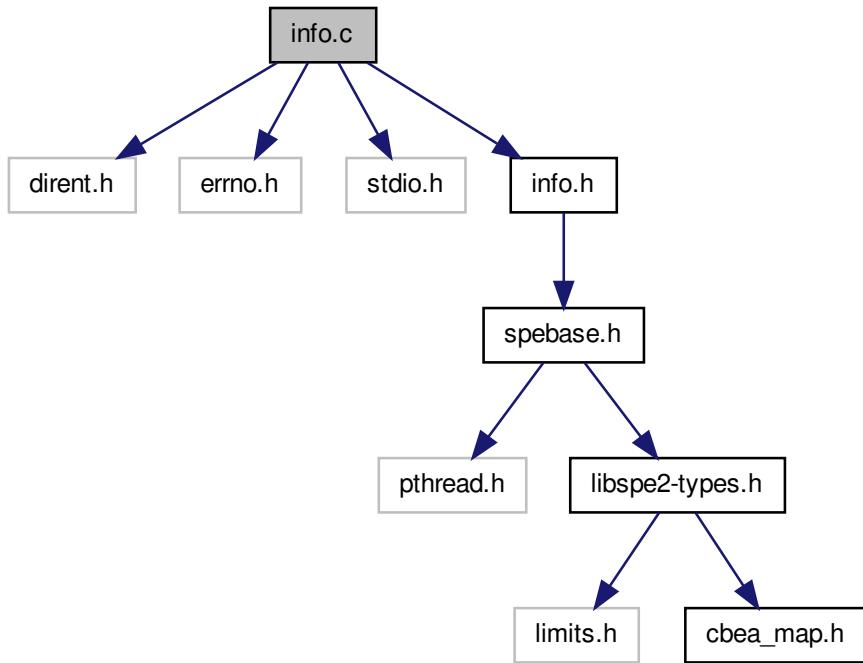
### 3.12 info.c File Reference

```

#include <dirent.h>
#include <errno.h>
#include <stdio.h>
#include "info.h"

```

Include dependency graph for info.c:



## Functions

- [int \\_base\\_spe\\_count\\_physical\\_cpus \(int cpu\\_node\)](#)
- [int \\_base\\_spe\\_count\\_usable\\_spes \(int cpu\\_node\)](#)
- [int \\_base\\_spe\\_count\\_physical\\_spes \(int cpu\\_node\)](#)
- [int \\_base\\_spe\\_cpu\\_info\\_get \(int info\\_requested, int cpu\\_node\)](#)

### 3.12.1 Function Documentation

#### 3.12.1.1 [int \\_base\\_spe\\_count\\_physical\\_cpus \( int cpu\\_node \)](#)

Definition at line 30 of file info.c.

References DEBUG\_PRINTF, and THREADS\_PER\_BE.

Referenced by [\\_base\\_spe\\_count\\_physical\\_spes\(\)](#), and [\\_base\\_spe\\_cpu\\_info\\_get\(\)](#).

```
{
    const char    *buff = "/sys/devices/system/cpu";
    DIR        *dirp;
    int ret = -2;
    struct dirent  *dptr;
```

```

DEBUG_PRINTF ("spe_count_physical_cpus()\n");

// make sure, cpu_node is in the correct range
if (cpu_node != -1) {
    errno = EINVAL;
    return -1;
}

// Count number of CPUs in /sys/devices/system/cpu
if((dirp=open(dirp))==NULL) {
    fprintf(stderr,"Error opening %s ",buff);
    perror("dirlist");
    errno = EINVAL;
    return -1;
}
while((dptr=readdir(dirp))) {
    ret++;
}
closedir(dirp);
return ret/THREADS_PER_BE;
}

```

### 3.12.1.2 int \_base\_spe\_count\_physical\_spes( int *cpu\_node* )

Definition at line 71 of file info.c.

References \_base\_spe\_count\_physical\_cpus(), and DEBUG\_PRINTF.

Referenced by \_base\_spe\_count\_usable\_spes(), and \_base\_spe\_cpu\_info\_get().

```

{
const char      *buff = "/sys/devices/system/spu";
DIR            *dirp;
int  ret = -2;
struct dirent  *dptr;
int  no_of_bes;

DEBUG_PRINTF ("spe_count_physical_spes()\n");

// make sure, cpu_node is in the correct range
no_of_bes = _base_spe_count_physical_cpus(-1);
if (cpu_node < -1 || cpu_node >= no_of_bes ) {
    errno = EINVAL;
    return -1;
}

// Count number of SPUs in /sys/devices/system/spu
if((dirp=open(buff))==NULL) {
    fprintf(stderr,"Error opening %s ",buff);
    perror("dirlist");
    errno = EINVAL;
    return -1;
}
while((dptr=readdir(dirp))) {
    ret++;
}
closedir(dirp);

if(cpu_node != -1) ret /= no_of_bes; // FIXME
return ret;
}

```

Here is the call graph for this function:



### 3.12.1.3 int \_base\_spe\_count\_usable\_spes ( int cpu\_node )

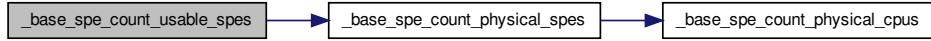
Definition at line 62 of file info.c.

References \_base\_spe\_count\_physical\_spes().

Referenced by \_base\_spe\_cpu\_info\_get().

```
{
    return _base_spe_count_physical_spes(cpu_node); // FIXME
}
```

Here is the call graph for this function:



### 3.12.1.4 int \_base\_spe\_cpu\_info\_get ( int info\_requested, int cpu\_node )

\_base\_spe\_info\_get

Definition at line 105 of file info.c.

References \_base\_spe\_count\_physical\_cpus(), \_base\_spe\_count\_physical\_spes(), \_base\_spe\_count\_usable\_spes(), SPE\_COUNT\_PHYSICAL\_CPU\_NODES, SPE\_COUNT\_PHYSICAL\_SPES, and SPE\_COUNT\_USABLE\_SPES.

```

{
    int ret = 0;
    errno = 0;

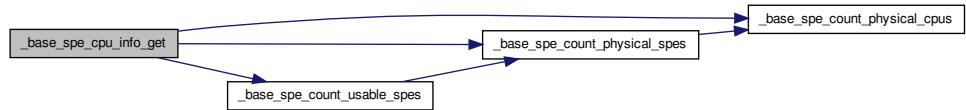
    switch (info_requested) {
        case SPE_COUNT_PHYSICAL_CPU_NODES:
            ret = _base_spe_count_physical_cpus(cpu_node);
            break;
        case SPE_COUNT_PHYSICAL_SPES:
            ret = _base_spe_count_physical_spes(cpu_node);
            break;
    }
}
```

```

        case SPE_COUNT_USABLE_SPES:
            ret = _base_spe_count_usable_spes(cpu_node);
            break;
        default:
            errno = EINVAL;
            ret = -1;
    }
    return ret;
}

```

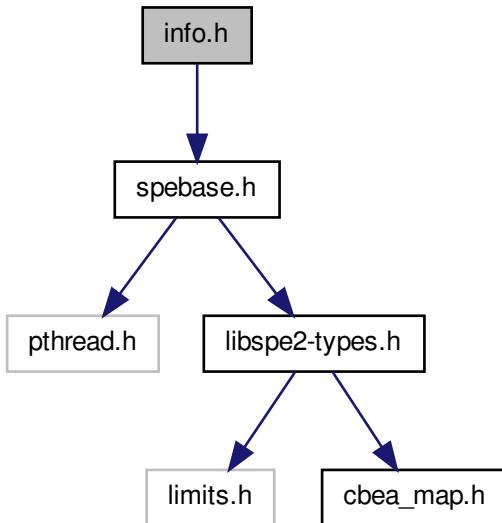
Here is the call graph for this function:



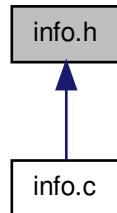
### 3.13 info.h File Reference

```
#include "spebase.h"
```

Include dependency graph for info.h:



This graph shows which files directly or indirectly include this file:



## Defines

- `#define THREADS_PER_BE 2`

## Functions

- `int _base_spe_count_physical_cpus (int cpu_node)`
- `int _base_spe_count_physical_spes (int cpu_node)`
- `int _base_spe_count_usable_spes (int cpu_node)`

### 3.13.1 Define Documentation

#### 3.13.1.1 `#define THREADS_PER_BE 2`

Definition at line 25 of file info.h.

Referenced by `_base_spe_count_physical_cpus()`.

### 3.13.2 Function Documentation

#### 3.13.2.1 `int _base_spe_count_physical_cpus ( int cpu_node )`

Definition at line 30 of file info.c.

References `DEBUG_PRINTF`, and `THREADS_PER_BE`.

Referenced by `_base_spe_count_physical_spes()`, and `_base_spe_cpu_info_get()`.

```
{  
    const char    *buff = "/sys/devices/system/cpu";  
    DIR        *dirp;  
    int ret = -2;  
    struct dirent *dptr;  
  
    DEBUG_PRINTF ("spe_count_physical_cpus ()\n");
```

```

// make sure, cpu_node is in the correct range
if (cpu_node != -1) {
    errno = EINVAL;
    return -1;
}

// Count number of CPUs in /sys/devices/system/cpu
if((dirp=open(dirp))==NULL) {
    fprintf(stderr,"Error opening %s ",buff);
    perror("dirlist");
    errno = EINVAL;
    return -1;
}
while((dptr=readdir(dirp))) {
    ret++;
}
closedir(dirp);
return ret/THREADS_PER_BE;
}

```

### 3.13.2.2 int \_base\_spe\_count\_physical\_spes ( int *cpu\_node* )

Definition at line 71 of file info.c.

References \_base\_spe\_count\_physical\_cpus(), and DEBUG\_PRINTF.

Referenced by \_base\_spe\_count\_usable\_spes(), and \_base\_spe\_cpu\_info\_get().

```

{
const char      *buff = "/sys/devices/system/spu";
DIR            *dirp;
int  ret = -2;
struct dirent  *dptr;
int  no_of_bes;

DEBUG_PRINTF ("spe_count_physical_spes()\n");

// make sure, cpu_node is in the correct range
no_of_bes = _base_spe_count_physical_cpus(-1);
if (cpu_node < -1 || cpu_node >= no_of_bes ) {
    errno = EINVAL;
    return -1;
}

// Count number of SPUs in /sys/devices/system/spu
if((dirp=open(buff))==NULL) {
    fprintf(stderr,"Error opening %s ",buff);
    perror("dirlist");
    errno = EINVAL;
    return -1;
}
while((dptr=readdir(dirp))) {
    ret++;
}
closedir(dirp);

if(cpu_node != -1) ret /= no_of_bes; // FIXME
return ret;
}

```

Here is the call graph for this function:



### 3.13.2.3 int \_base\_spe\_count\_usable\_spes ( int cpu\_node )

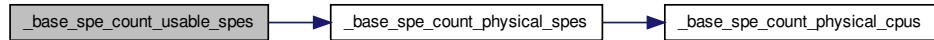
Definition at line 62 of file info.c.

References \_base\_spe\_count\_physical\_spes().

Referenced by \_base\_spe\_cpu\_info\_get().

```
{  
    return _base_spe_count_physical_spes(cpu_node); // FIXME  
}
```

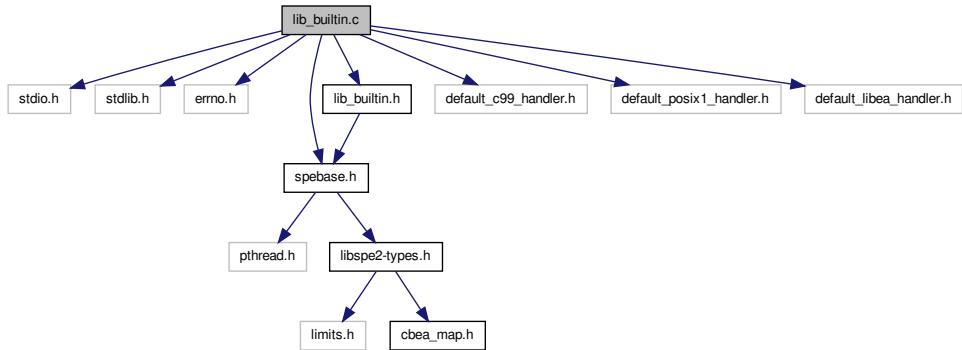
Here is the call graph for this function:



## 3.14 lib\_builtin.c File Reference

```
#include <stdio.h>  
#include <stdlib.h>  
#include <errno.h>  
#include "spebase.h"  
#include "lib_builtin.h"  
#include "default_c99_handler.h"  
#include "default_posix1_handler.h"  
#include "default_libea_handler.h"
```

Include dependency graph for lib\_builtin.c:



## Defines

- #define **HANDLER\_IDX**(x) (x & 0xff)

## Functions

- int **\_base\_spe\_callback\_handler\_register** (void \*handler, unsigned int callnum, unsigned int mode)
- int **\_base\_spe\_callback\_handler\_deregister** (unsigned int callnum)
- void \* **\_base\_spe\_callback\_handler\_query** (unsigned int callnum)
- int **\_base\_spe\_handle\_library\_callback** (struct **spe\_context** \*spe, int callnum, unsigned int npc)

### 3.14.1 Define Documentation

#### 3.14.1.1 #define HANDLER\_IDX( x ) (x & 0xff)

Definition at line 29 of file lib\_builtin.c.

### 3.14.2 Function Documentation

#### 3.14.2.1 int \_base\_spe\_callback\_handler\_deregister ( unsigned int callnum )

unregister a handler function for the specified number NOTE: unregistering a handler from call zero and one is ignored.

Definition at line 78 of file lib\_builtin.c.

References MAX\_CALLNUM, and RESERVED.

```
{
    errno = 0;
    if (callnum > MAX_CALLNUM) {
        errno = EINVAL;
        return -1;
    }
}
```

```

        if (callnum < RESERVED) {
            errno = EACCES;
            return -1;
        }
        if (handlers[callnum] == NULL) {
            errno = ESRCH;
            return -1;
        }

        handlers[callnum] = NULL;
        return 0;
    }
}

```

#### 3.14.2.2 void\* \_base\_spe\_callback\_handler\_query ( unsigned int callnum )

query a handler function for the specified number

Definition at line 98 of file lib\_builtin.c.

References MAX\_CALLNUM.

```

{
    errno = 0;

    if (callnum > MAX_CALLNUM) {
        errno = EINVAL;
        return NULL;
    }
    if (handlers[callnum] == NULL) {
        errno = ESRCH;
        return NULL;
    }
    return handlers[callnum];
}

```

#### 3.14.2.3 int \_base\_spe\_callback\_handler\_register ( void \* handler, unsigned int callnum, unsigned int mode )

register a handler function for the specified number NOTE: registering a handler to call zero and one is ignored.

Definition at line 40 of file lib\_builtin.c.

References MAX\_CALLNUM, RESERVED, SPE\_CALLBACK\_NEW, and SPE\_CALLBACK\_UPDATE.

```

{
    errno = 0;

    if (callnum > MAX_CALLNUM) {
        errno = EINVAL;
        return -1;
    }

    switch(mode) {
    case SPE_CALLBACK_NEW:
        if (callnum < RESERVED) {
            errno = EACCES;
            return -1;
        }
        if (handlers[callnum] != NULL) {
            errno = EACCES;
        }
    }
}

```

```

        return -1;
    }
    handlers[callnum] = handler;
    break;

case SPE_CALLBACK_UPDATE:
    if (handlers[callnum] == NULL) {
        errno = ESRCH;
        return -1;
    }
    handlers[callnum] = handler;
    break;
default:
    errno = EINVAL;
    return -1;
    break;
}
return 0;
}

```

### 3.14.2.4 int \_base\_spe\_handle\_library\_callback ( struct spe\_context \* *spe*, int *callnum*, unsigned int *npc* )

Definition at line 113 of file lib\_builtin.c.

References spe\_context::base\_private, DEBUG\_PRINTF, spe\_context\_base\_priv::flags, spe\_context\_base\_priv::mem\_mmap\_base, SPE\_EMULATE\_PARAM\_BUFFER, and SPE\_ISOLATE\_EMULATE.

Referenced by \_base\_spe\_context\_run().

```

{
    int (*handler) (void *, unsigned int);
    int rc;

    errno = 0;
    if (!handlers[callnum]) {
        DEBUG_PRINTF ("No SPE library handler registered for this call.\n");
    };
    errno=ENOSYS;
    return -1;
}

handler=handlers[callnum];

/* For emulated isolation mode, position the
 * npc so that the buffer for the PPE-assisted
 * library calls can be accessed. */
if (spe->base_private->flags & SPE_ISOLATE_EMULATE)
    npc = SPE_EMULATE_PARAM_BUFFER;

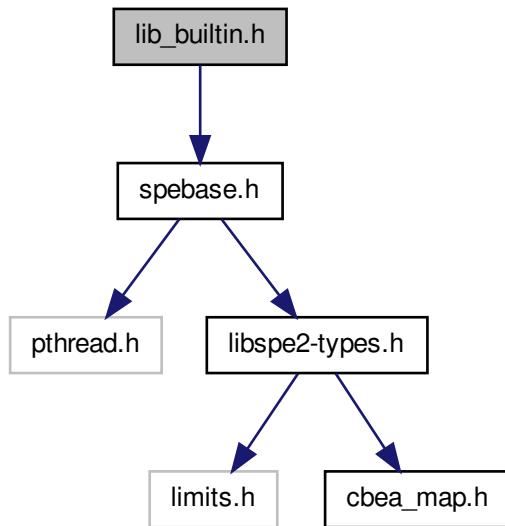
rc = handler(spe->base_private->mem_mmap_base, npc);
if (rc) {
    DEBUG_PRINTF ("SPE library call unsupported.\n");
    errno=ENOSYS;
    return rc;
}
return 0;
}

```

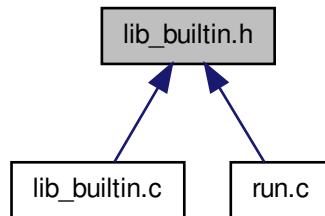
## 3.15 lib\_builtin.h File Reference

```
#include "spebase.h"
```

Include dependency graph for lib\_builtin.h:



This graph shows which files directly or indirectly include this file:



## Defines

- #define MAX\_CALLNUM 255
- #define RESERVED 4

## Functions

- int \_base\_spe\_handle\_library\_callback (struct spe\_context \*spe, int callnum, unsigned int npc)

### 3.15.1 Define Documentation

#### 3.15.1.1 #define MAX\_CALLNUM 255

Definition at line 25 of file lib\_builtin.h.

Referenced by \_base\_spe\_callback\_handler\_deregister(), \_base\_spe\_callback\_handler\_query(), and \_base\_spe\_callback\_handler\_register().

#### 3.15.1.2 #define RESERVED 4

Definition at line 26 of file lib\_builtin.h.

Referenced by \_base\_spe\_callback\_handler\_deregister(), and \_base\_spe\_callback\_handler\_register().

### 3.15.2 Function Documentation

#### 3.15.2.1 int \_base\_spe\_handle\_library\_callback ( struct spe\_context \* *spe*, int *callnum*, unsigned int *npc* )

Definition at line 113 of file lib\_builtin.c.

References spe\_context::base\_private, DEBUG\_PRINTF, spe\_context\_base\_priv::flags, spe\_context\_base\_priv::mem\_mmap\_base, SPE\_EMULATE\_PARAM\_BUFFER, and SPE\_ISOLATE\_EMULATE.

Referenced by \_base\_spe\_context\_run().

```
{
    int (*handler) (void *, unsigned int);
    int rc;

    errno = 0;
    if (!handlers[callnum]) {
        DEBUG_PRINTF ("No SPE library handler registered for this call.\n");
    );
        errno=ENOSYS;
        return -1;
    }

    handler=handlers[callnum];

    /* For emulated isolation mode, position the
     * npc so that the buffer for the PPE-assisted
     * library calls can be accessed. */
    if (spe->base_private->flags & SPE_ISOLATE_EMULATE)
        npc = SPE_EMULATE_PARAM_BUFFER;

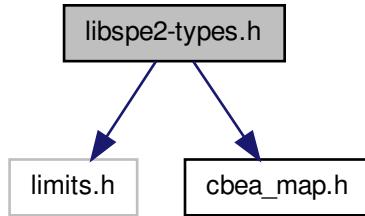
    rc = handler(spe->base_private->mem_mmap_base, npc);
    if (rc) {
        DEBUG_PRINTF ("SPE library call unsupported.\n");
        errno=ENOSYS;
        return rc;
    }
    return 0;
}
```

### 3.16 libspe2-types.h File Reference

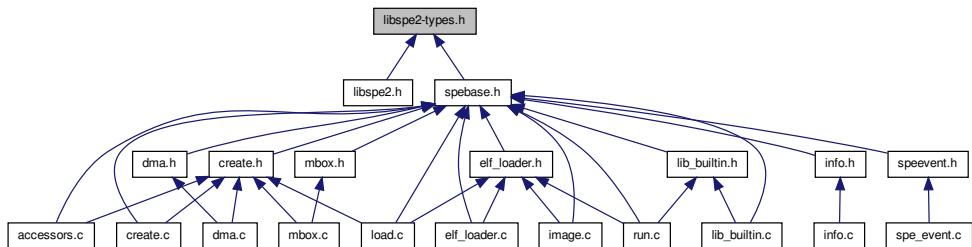
```
#include <limits.h>
```

```
#include "cbea_map.h"
```

Include dependency graph for libspe2-types.h:



This graph shows which files directly or indirectly include this file:



## Data Structures

- struct [spe\\_program\\_handle](#)
- struct [spe\\_context](#)
- struct [spe\\_gang\\_context](#)
- struct [spe\\_stop\\_info](#)
- union [spe\\_event\\_data](#)
- struct [spe\\_event\\_unit](#)

## Defines

- #define [SPE\\_CFG\\_SIGNIFY1\\_OR](#) 0x000000010
- #define [SPE\\_CFG\\_SIGNIFY2\\_OR](#) 0x000000020
- #define [SPE\\_MAP\\_PS](#) 0x000000040
- #define [SPE\\_ISOLATE](#) 0x000000080
- #define [SPE\\_ISOLATE\\_EMULATE](#) 0x000000100
- #define [SPE\\_EVENTS\\_ENABLE](#) 0x00001000

- #define `SPE_AFFINITY_MEMORY` 0x00002000
- #define `SPE_EXIT` 1
- #define `SPE_STOP_AND_SIGNAL` 2
- #define `SPE_RUNTIME_ERROR` 3
- #define `SPE_RUNTIME_EXCEPTION` 4
- #define `SPE_RUNTIME_FATAL` 5
- #define `SPE_CALLBACK_ERROR` 6
- #define `SPE_ISOLATION_ERROR` 7
- #define `SPE_SPU_STOPPED_BY_STOP` 0x02
- #define `SPE_SPU_HALT` 0x04
- #define `SPE_SPU_WAITING_ON_CHANNEL` 0x08
- #define `SPE_SPU_SINGLE_STEP` 0x10
- #define `SPE_SPU_INVALID_INSTR` 0x20
- #define `SPE_SPU_INVALID_CHANNEL` 0x40
- #define `SPE_DMA_ALIGNMENT` 0x0008
- #define `SPE_DMA_SEGMENTATION` 0x0020
- #define `SPE_DMA_STORAGE` 0x0040
- #define `SPE_INVALID_DMA` 0x0800
- #define `SIGSPE SIGURG`
- #define `SPE_EVENT_OUT_INTR_MBOX` 0x00000001
- #define `SPE_EVENT_IN_MBOX` 0x00000002
- #define `SPE_EVENT_TAG_GROUP` 0x00000004
- #define `SPE_EVENT_SPE_STOPPED` 0x00000008
- #define `SPE_EVENT_ALL_EVENTS`
- #define `SPE_MBOX_ALL_BLOCKING` 1
- #define `SPE_MBOX_ANY_BLOCKING` 2
- #define `SPE_MBOX_ANY_NONBLOCKING` 3
- #define `SPE_TAG_ALL` 1
- #define `SPE_TAG_ANY` 2
- #define `SPE_TAG_IMMEDIATE` 3
- #define `SPE_DEFAULT_ENTRY` `UINT_MAX`
- #define `SPE_RUN_USER_REGS` 0x00000001
- #define `SPE_NO_CALLBACKS` 0x00000002
- #define `SPE_CALLBACK_NEW` 1
- #define `SPE_CALLBACK_UPDATE` 2
- #define `SPE_COUNT_PHYSICAL_CPU_NODES` 1
- #define `SPE_COUNT_PHYSICAL_SPES` 2
- #define `SPE_COUNT_USABLE_SPES` 3
- #define `SPE_SIG_NOTIFY_REG_1` 0x0001
- #define `SPE_SIG_NOTIFY_REG_2` 0x0002

## Typedefs

- typedef struct `spe_program_handle` `spe_program_handle_t`
- typedef struct `spe_context` \* `spe_context_ptr_t`
- typedef struct `spe_gang_context` \* `spe_gang_context_ptr_t`
- typedef struct `spe_stop_info` `spe_stop_info_t`
- typedef union `spe_event_data` `spe_event_data_t`
- typedef struct `spe_event_unit` `spe_event_unit_t`
- typedef void \* `spe_event_handler_ptr_t`
- typedef int `spe_event_handler_t`

## Enumerations

- enum ps\_area {  
    SPE\_MSSYNC\_AREA, SPE\_MFC\_COMMAND\_AREA, SPE\_CONTROL\_AREA, SPE\_SIG\_NOTIFY\_1\_AREA,  
    SPE\_SIG\_NOTIFY\_2\_AREA }

### 3.16.1 Define Documentation

#### 3.16.1.1 #define SIGSPE SIGURG

SIGSPE maps to SIGURG

Definition at line 219 of file libspe2-types.h.

#### 3.16.1.2 #define SPE\_AFFINITY\_MEMORY 0x00002000

Definition at line 182 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_create().

#### 3.16.1.3 #define SPE\_CALLBACK\_ERROR 6

Definition at line 194 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

#### 3.16.1.4 #define SPE\_CALLBACK\_NEW 1

Definition at line 260 of file libspe2-types.h.

Referenced by \_base\_spe\_callback\_handler\_register().

#### 3.16.1.5 #define SPE\_CALLBACK\_UPDATE 2

Definition at line 261 of file libspe2-types.h.

Referenced by \_base\_spe\_callback\_handler\_register().

#### 3.16.1.6 #define SPE\_CFG\_SIGNOTIFY1\_OR 0x00000010

Flags for spe\_context\_create

Definition at line 176 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_create().

#### 3.16.1.7 #define SPE\_CFG\_SIGNOTIFY2\_OR 0x00000020

Definition at line 177 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_create().

**3.16.1.8 #define SPE\_COUNT\_PHYSICAL\_CPU\_NODES 1**

Definition at line 265 of file libspe2-types.h.

Referenced by \_base\_spe\_cpu\_info\_get().

**3.16.1.9 #define SPE\_COUNT\_PHYSICAL\_SPES 2**

Definition at line 266 of file libspe2-types.h.

Referenced by \_base\_spe\_cpu\_info\_get().

**3.16.1.10 #define SPE\_COUNT\_USABLE\_SPES 3**

Definition at line 267 of file libspe2-types.h.

Referenced by \_base\_spe\_cpu\_info\_get().

**3.16.1.11 #define SPE\_DEFAULT\_ENTRY UINT\_MAX**

Flags for \_base\_spe\_context\_run

Definition at line 253 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**3.16.1.12 #define SPE\_DMA\_ALIGNMENT 0x0008**

Runtime exceptions

Definition at line 210 of file libspe2-types.h.

**3.16.1.13 #define SPE\_DMA\_SEGMENTATION 0x0020**

Definition at line 212 of file libspe2-types.h.

**3.16.1.14 #define SPE\_DMA\_STORAGE 0x0040**

Definition at line 213 of file libspe2-types.h.

**3.16.1.15 #define SPE\_EVENT\_ALL\_EVENTS**

**Value:**

```
SPE_EVENT_OUT_INTR_MBOX | \
    SPE_EVENT_IN_MBOX | \
    SPE_EVENT_TAG_GROUP | \
    SPE_EVENT_SPE_STOPPED
```

Definition at line 229 of file libspe2-types.h.

---

**3.16.1.16 #define SPE\_EVENT\_IN\_MBOX 0x00000002**

Definition at line 225 of file libspe2-types.h.

Referenced by \_event\_spe\_event\_handler\_deregister(), and \_event\_spe\_event\_handler\_register().

**3.16.1.17 #define SPE\_EVENT\_OUT\_INTR\_MBOX 0x00000001**

Supported SPE events

Definition at line 224 of file libspe2-types.h.

Referenced by \_event\_spe\_event\_handler\_deregister(), and \_event\_spe\_event\_handler\_register().

**3.16.1.18 #define SPE\_EVENT\_SPE\_STOPPED 0x00000008**

Definition at line 227 of file libspe2-types.h.

Referenced by \_event\_spe\_event\_handler\_deregister(), and \_event\_spe\_event\_handler\_register().

**3.16.1.19 #define SPE\_EVENT\_TAG\_GROUP 0x00000004**

Definition at line 226 of file libspe2-types.h.

Referenced by \_event\_spe\_event\_handler\_deregister(), and \_event\_spe\_event\_handler\_register().

**3.16.1.20 #define SPE\_EVENTS\_ENABLE 0x00001000**

Definition at line 181 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_create(), and \_base\_spe\_context\_run().

**3.16.1.21 #define SPE\_EXIT 1**

Symbolic constants for stop reasons as returned in spe\_stop\_info\_t

Definition at line 189 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**3.16.1.22 #define SPE\_INVALID\_DMA 0x0800**

Definition at line 214 of file libspe2-types.h.

**3.16.1.23 #define SPE\_ISOLATE 0x00000080**

Definition at line 179 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_create(), \_base\_spe\_context\_run(), and \_base\_spe\_program\_load().

---

**3.16.1.24 #define SPE\_ISOLATE\_EMULATE 0x00000100**

Definition at line 180 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_create(), \_base\_spe\_context\_run(), \_base\_spe\_handle\_library\_callback(), and \_base\_spe\_program\_load().

**3.16.1.25 #define SPE\_ISOLATION\_ERROR 7**

Definition at line 195 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**3.16.1.26 #define SPE\_MAP\_PS 0x00000040**

Definition at line 178 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_create(), \_base\_spe\_in\_mbox\_status(), \_base\_spe\_in\_mbox\_write(), \_base\_spe\_mfcio\_tag\_status\_read(), \_base\_spe\_mssync\_start(), \_base\_spe\_mssync\_status(), \_base\_spe\_out\_intr\_mbox\_status(), \_base\_spe\_out\_mbox\_read(), \_base\_spe\_out\_mbox\_status(), \_base\_spe\_signal\_write(), and \_event\_spe\_event\_handler\_register().

**3.16.1.27 #define SPE\_MBOX\_ALL\_BLOCKING 1**

Behavior flags for mailbox read/write functions

Definition at line 237 of file libspe2-types.h.

Referenced by \_base\_spe\_in\_mbox\_write(), and \_base\_spe\_out\_intr\_mbox\_read().

**3.16.1.28 #define SPE\_MBOX\_ANY\_BLOCKING 2**

Definition at line 238 of file libspe2-types.h.

Referenced by \_base\_spe\_in\_mbox\_write(), and \_base\_spe\_out\_intr\_mbox\_read().

**3.16.1.29 #define SPE\_MBOX\_ANY\_NONBLOCKING 3**

Definition at line 239 of file libspe2-types.h.

Referenced by \_base\_spe\_in\_mbox\_write(), and \_base\_spe\_out\_intr\_mbox\_read().

**3.16.1.30 #define SPE\_NO\_CALLBACKS 0x00000002**

Definition at line 255 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**3.16.1.31 #define SPE\_RUN\_USER\_REGS 0x00000001**

Definition at line 254 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**3.16.1.32 #define SPE\_RUNTIME\_ERROR 3**

Definition at line 191 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**3.16.1.33 #define SPE\_RUNTIME\_EXCEPTION 4**

Definition at line 192 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**3.16.1.34 #define SPE\_RUNTIME\_FATAL 5**

Definition at line 193 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**3.16.1.35 #define SPE\_SIG\_NOTIFY\_REG\_1 0x0001**

Signal Targets

Definition at line 272 of file libspe2-types.h.

Referenced by \_base\_spe\_signal\_write().

**3.16.1.36 #define SPE\_SIG\_NOTIFY\_REG\_2 0x0002**

Definition at line 273 of file libspe2-types.h.

Referenced by \_base\_spe\_signal\_write().

**3.16.1.37 #define SPE\_SPU\_HALT 0x04**

Definition at line 201 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**3.16.1.38 #define SPE\_SPU\_INVALID\_CHANNEL 0x40**

Definition at line 205 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**3.16.1.39 #define SPE\_SPU\_INVALID\_INSTR 0x20**

Definition at line 204 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**3.16.1.40 #define SPE\_SPU\_SINGLE\_STEP 0x10**

Definition at line 203 of file libspe2-types.h.

**3.16.1.41 #define SPE\_SPU\_STOPPED\_BY\_STOP 0x02**

Runtime errors

Definition at line 200 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**3.16.1.42 #define SPE\_SPU\_WAITING\_ON\_CHANNEL 0x08**

Definition at line 202 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**3.16.1.43 #define SPE\_STOP\_AND\_SIGNAL 2**

Definition at line 190 of file libspe2-types.h.

Referenced by \_base\_spe\_context\_run().

**3.16.1.44 #define SPE\_TAG\_ALL 1**

Behavior flags tag status functions

Definition at line 245 of file libspe2-types.h.

Referenced by \_base\_spe\_mfcio\_tag\_status\_read().

**3.16.1.45 #define SPE\_TAG\_ANY 2**

Definition at line 246 of file libspe2-types.h.

Referenced by \_base\_spe\_mfcio\_tag\_status\_read().

**3.16.1.46 #define SPE\_TAG\_IMMEDIATE 3**

Definition at line 247 of file libspe2-types.h.

Referenced by \_base\_spe\_mfcio\_tag\_status\_read().

## 3.16.2 Typedef Documentation

**3.16.2.1 typedef struct spe\_context\* spe\_context\_ptr\_t**

spe\_context\_ptr\_t This pointer serves as the identifier for a specific SPE context throughout the API (where needed)

Definition at line 83 of file libspe2-types.h.

**3.16.2.2 typedef union spe\_event\_data spe\_event\_data\_t**

spe\_event\_data\_t User data to be associated with an event

**3.16.2.3 `typedef void* spe_event_handler_ptr_t`**

Definition at line 159 of file libspe2-types.h.

**3.16.2.4 `typedef int spe_event_handler_t`**

Definition at line 160 of file libspe2-types.h.

**3.16.2.5 `typedef struct spe_event_unit spe_event_unit_t`**

`spe_event_t`

**3.16.2.6 `typedef struct spe_gang_context* spe_gang_context_ptr_t`**

`spe_gang_context_ptr_t` This pointer serves as the identifier for a specific SPE gang context throughout the API (where needed)

Definition at line 106 of file libspe2-types.h.

**3.16.2.7 `typedef struct spe_program_handle spe_program_handle_t`**

SPE program handle Structure `spe_program_handle` per CESOF specification libspe2 applications usually only keep a pointer to the program handle and do not use the structure directly.

**3.16.2.8 `typedef struct spe_stop_info spe_stop_info_t`**

`spe_stop_info_t`

### 3.16.3 Enumeration Type Documentation

**3.16.3.1 `enum ps_area`**

**Enumerator:**

`SPE_MSSYNC_AREA`

`SPE_MFC_COMMAND_AREA`

`SPE_CONTROL_AREA`

`SPE_SIG_NOTIFY_1_AREA`

`SPE_SIG_NOTIFY_2_AREA`

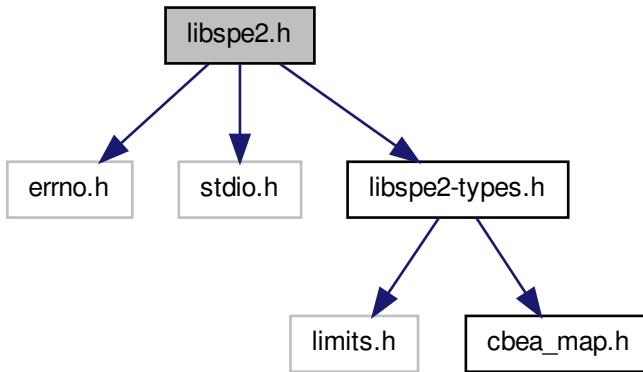
Definition at line 171 of file libspe2-types.h.

```
{ SPE_MSSYNC_AREA, SPE_MFC_COMMAND_AREA, SPE_CONTROL_AREA, SPE_SIG_NOTIFY_1_AREA,  
SPE_SIG_NOTIFY_2_AREA };
```

### 3.17 libspe2.h File Reference

```
#include <errno.h>
#include <stdio.h>
#include "libspe2-types.h"
```

Include dependency graph for libspe2.h:



### Functions

- `spe_context_ptr_t spe_context_create (unsigned int flags, spe_gang_context_ptr_t gang)`
- `spe_context_ptr_t spe_context_create_affinity (unsigned int flags, spe_context_ptr_t affinity_neighbor, spe_gang_context_ptr_t gang)`
- `int spe_context_destroy (spe_context_ptr_t spe)`
- `spe_gang_context_ptr_t spe_gang_context_create (unsigned int flags)`
- `int spe_gang_context_destroy (spe_gang_context_ptr_t gang)`
- `spe_program_handle_t * spe_image_open (const char *filename)`
- `int spe_image_close (spe_program_handle_t *program)`
- `int spe_program_load (spe_context_ptr_t spe, spe_program_handle_t *program)`
- `int spe_context_run (spe_context_ptr_t spe, unsigned int *entry, unsigned int runflags, void *argp, void *envp, spe_stop_info_t *stopinfo)`
- `int spe_stop_info_read (spe_context_ptr_t spe, spe_stop_info_t *stopinfo)`
- `spe_event_handler_ptr_t spe_event_handler_create (void)`
- `int spe_event_handler_destroy (spe_event_handler_ptr_t evhandler)`
- `int spe_event_handler_register (spe_event_handler_ptr_t evhandler, spe_event_unit_t *event)`
- `int spe_event_handler_deregister (spe_event_handler_ptr_t evhandler, spe_event_unit_t *event)`
- `int spe_event_wait (spe_event_handler_ptr_t evhandler, spe_event_unit_t *events, int max_events, int timeout)`
- `int spe_mfcio_put (spe_context_ptr_t spe, unsigned int ls, void *ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)`

- int `spe_mfcio_putb (spe_context_ptr_t spe, unsigned int ls, void *ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)`
- int `spe_mfcio_putf (spe_context_ptr_t spe, unsigned int ls, void *ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)`
- int `spe_mfcio_get (spe_context_ptr_t spe, unsigned int ls, void *ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)`
- int `spe_mfcio_getb (spe_context_ptr_t spe, unsigned int ls, void *ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)`
- int `spe_mfcio_getf (spe_context_ptr_t spe, unsigned int ls, void *ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)`
- int `spe_mfcio_tag_status_read (spe_context_ptr_t spe, unsigned int mask, unsigned int behavior, unsigned int *tag_status)`
- int `spe_out_mbox_read (spe_context_ptr_t spe, unsigned int *mbox_data, int count)`
- int `spe_out_mbox_status (spe_context_ptr_t spe)`
- int `spe_in_mbox_write (spe_context_ptr_t spe, unsigned int *mbox_data, int count, unsigned int behavior)`
- int `spe_in_mbox_status (spe_context_ptr_t spe)`
- int `spe_out_intr_mbox_read (spe_context_ptr_t spe, unsigned int *mbox_data, int count, unsigned int behavior)`
- int `spe_out_intr_mbox_status (spe_context_ptr_t spe)`
- int `spe_mssync_start (spe_context_ptr_t spe)`
- int `spe_mssync_status (spe_context_ptr_t spe)`
- int `spe_signal_write (spe_context_ptr_t spe, unsigned int signal_reg, unsigned int data)`
- void \* `spe_ls_area_get (spe_context_ptr_t spe)`
- int `spe_ls_size_get (spe_context_ptr_t spe)`
- void \* `spe_ps_area_get (spe_context_ptr_t spe, enum ps_area area)`
- int `spe_callback_handler_register (void *handler, unsigned int callnum, unsigned int mode)`
- int `spe_callback_handler_deregister (unsigned int callnum)`
- void \* `spe_callback_handler_query (unsigned int callnum)`
- int `spe_cpu_info_get (int info_requested, int cpu_node)`

### 3.17.1 Function Documentation

- 3.17.1.1 int `spe_callback_handler_deregister ( unsigned int callnum )`
- 3.17.1.2 void\* `spe_callback_handler_query ( unsigned int callnum )`
- 3.17.1.3 int `spe_callback_handler_register ( void * handler, unsigned int callnum, unsigned int mode )`
- 3.17.1.4 spe\_context\_ptr\_t `spe_context_create ( unsigned int flags, spe_gang_context_ptr_t gang )`
- 3.17.1.5 spe\_context\_ptr\_t `spe_context_create_affinity ( unsigned int flags, spe_context_ptr_t affinity_neighbor, spe_gang_context_ptr_t gang )`
- 3.17.1.6 int `spe_context_destroy ( spe_context_ptr_t spe )`
- 3.17.1.7 int `spe_context_run ( spe_context_ptr_t spe, unsigned int * entry, unsigned int runflags, void * argp, void * envp, spe_stop_info_t * stopinfo )`
- 3.17.1.8 int `spe_cpu_info_get ( int info_requested, int cpu_node )`

- 3.17.1.9 `spe_event_handler_ptr_t spe_event_handler_create( void )`
- 3.17.1.10 `int spe_event_handler_deregister( spe_event_handler_ptr_t evhandler, spe_event_unit_t * event )`
- 3.17.1.11 `int spe_event_handler_destroy( spe_event_handler_ptr_t evhandler )`
- 3.17.1.12 `int spe_event_handler_register( spe_event_handler_ptr_t evhandler, spe_event_unit_t * event )`
- 3.17.1.13 `int spe_event_wait( spe_event_handler_ptr_t evhandler, spe_event_unit_t * events, int max_events, int timeout )`
- 3.17.1.14 `spe_gang_context_ptr_t spe_gang_context_create( unsigned int flags )`
- 3.17.1.15 `int spe_gang_context_destroy( spe_gang_context_ptr_t gang )`
- 3.17.1.16 `int spe_image_close( spe_program_handle_t * program )`
- 3.17.1.17 `spe_program_handle_t* spe_image_open( const char * filename )`
- 3.17.1.18 `int spe_in_mbox_status( spe_context_ptr_t spe )`
- 3.17.1.19 `int spe_in_mbox_write( spe_context_ptr_t spe, unsigned int * mbox_data, int count, unsigned int behavior )`
- 3.17.1.20 `void* spe_ls_area_get( spe_context_ptr_t spe )`
- 3.17.1.21 `int spe_ls_size_get( spe_context_ptr_t spe )`
- 3.17.1.22 `int spe_mfcio_get( spe_context_ptr_t spe, unsigned int ls, void * ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid )`
- 3.17.1.23 `int spe_mfcio_getb( spe_context_ptr_t spe, unsigned int ls, void * ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid )`
- 3.17.1.24 `int spe_mfcio_getf( spe_context_ptr_t spe, unsigned int ls, void * ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid )`
- 3.17.1.25 `int spe_mfcio_put( spe_context_ptr_t spe, unsigned int ls, void * ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid )`
- 3.17.1.26 `int spe_mfcio_putb( spe_context_ptr_t spe, unsigned int ls, void * ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid )`
- 3.17.1.27 `int spe_mfcio_putf( spe_context_ptr_t spe, unsigned int ls, void * ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid )`
- 3.17.1.28 `int spe_mfcio_tag_status_read( spe_context_ptr_t spe, unsigned int mask, unsigned int behavior, unsigned int * tag_status )`
- 3.17.1.29 `int spe_mssync_start( spe_context_ptr_t spe )`

3.17.1.30 int spe\_mssync\_status ( spe\_context\_ptr\_t *spe* )

3.17.1.31 int spe\_out\_intr\_mbox\_read ( spe\_context\_ptr\_t *spe*, unsigned int \* *mbox\_data*, int *count*, unsigned int *behavior* )

3.17.1.32 int spe\_out\_intr\_mbox\_status ( spe\_context\_ptr\_t *spe* )

3.17.1.33 int spe\_out\_mbox\_read ( spe\_context\_ptr\_t *spe*, unsigned int \* *mbox\_data*, int *count* )

3.17.1.34 int spe\_out\_mbox\_status ( spe\_context\_ptr\_t *spe* )

3.17.1.35 int spe\_program\_load ( spe\_context\_ptr\_t *spe*, spe\_program\_handle\_t \* *program* )

3.17.1.36 void\* spe\_ps\_area\_get ( spe\_context\_ptr\_t *spe*, enum ps\_area *area* )

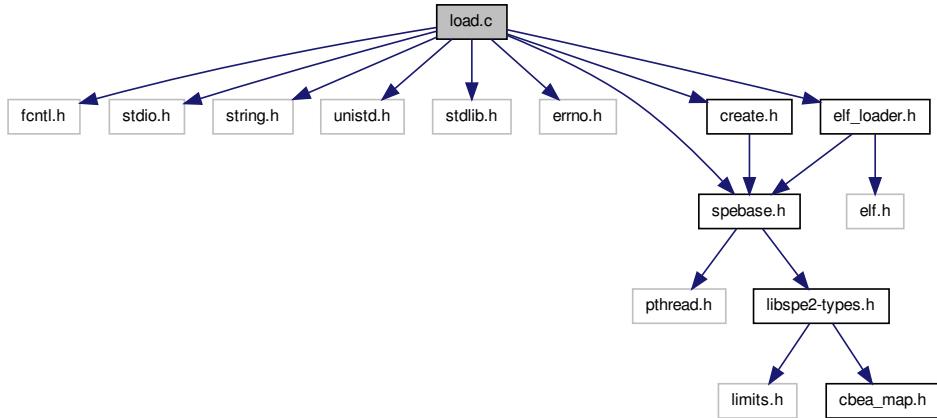
3.17.1.37 int spe\_signal\_write ( spe\_context\_ptr\_t *spe*, unsigned int *signal\_reg*, unsigned int *data* )

3.17.1.38 int spe\_stop\_info\_read ( spe\_context\_ptr\_t *spe*, spe\_stop\_info\_t \* *stopinfo* )

## 3.18 load.c File Reference

```
#include <fcntl.h>
#include <stdio.h>
#include <string.h>
#include <unistd.h>
#include <stdlib.h>
#include <errno.h>
#include "elf_loader.h"
#include "create.h"
#include "spebase.h"
```

Include dependency graph for load.c:



## Defines

- #define SPE\_EMULATED\_LOADER\_FILE "/usr/lib/spe/emulated-loader.bin"

## Functions

- void \_base\_spe\_program\_load\_complete (spe\_context\_ptr\_t spectx)
- int \_base\_spe\_emulated\_loader\_present (void)
- int \_base\_spe\_program\_load (spe\_context\_ptr\_t spe, spe\_program\_handle\_t \*program)

### 3.18.1 Define Documentation

#### 3.18.1.1 #define SPE\_EMULATED\_LOADER\_FILE "/usr/lib/spe/emulated-loader.bin"

Definition at line 31 of file load.c.

### 3.18.2 Function Documentation

#### 3.18.2.1 int \_base\_spe\_emulated\_loader\_present ( void )

Check if the emulated loader is present in the filesystem

#### Returns

Non-zero if the loader is available, otherwise zero.

Definition at line 159 of file load.c.

References \_base\_spe\_verify\_spe\_elf\_image().

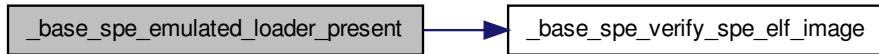
Referenced by \_base\_spe\_context\_create().

```
{
    spe_program_handle_t *loader = emulated_loader_program();

    if (!loader)
        return 0;

    return !_base_spe_verify_spe_elf_image(loader);
}
```

Here is the call graph for this function:



### 3.18.2.2 int \_base\_spe\_program\_load( spe\_context\_ptr\_t *spectx*, spe\_program\_handle\_t \* *program* )

\_base\_spe\_program\_load loads an ELF image into a context

#### Parameters

<i>spectx</i>	Specifies the SPE context
<i>program</i>	handle to the ELF image

Definition at line 203 of file load.c.

References \_base\_spe\_load\_spe\_elf(), \_base\_spe\_program\_load\_complete(), spe\_context::base\_private, DEBUG\_PRINTF, spe\_context\_base\_priv::emulated\_entry, spe\_ld\_info::entry, spe\_context\_base\_priv::entry, spe\_context\_base\_priv::flags, spe\_context\_base\_priv::loaded\_program, spe\_context\_base\_priv::mem\_mmap\_base, SPE\_ISOLATE, and SPE\_ISOLATE\_EMULATE.

```
{
    int rc = 0;
    struct spe_ld_info ld_info;

    spe->base_private->loaded_program = program;

    if (spe->base_private->flags & SPE_ISOLATE) {
        rc = spe_start_isolated_app(spe, program);

    } else if (spe->base_private->flags & SPE_ISOLATE_EMULATE) {
        rc = spe_start_emulated_isolated_app(spe, program, &ld_info);

    } else {
        rc = _base_spe_load_spe_elf(program,
                                     spe->base_private->mem_mmap_base, &ld_info);
        if (!rc)
            _base_spe_program_load_complete(spe);
    }

    if (rc != 0) {
        DEBUG_PRINTF ("Load SPE ELF failed..\n");
        return -1;
    }
}
```

```

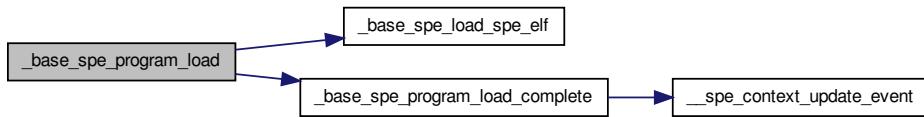
    }

    spe->base_private->entry = ld_info.entry;
    spe->base_private->emulated_entry = ld_info.entry;

    return 0;
}

```

Here is the call graph for this function:



### 3.18.2.3 void \_base\_spe\_program\_load\_complete ( spe\_context\_ptr\_t spectx )

Register the SPE program's start address with the oprofile and gdb, by writing to the object-id file.

Definition at line 38 of file load.c.

References \_\_spe\_context\_update\_event(), spe\_context::base\_private, DEBUG\_PRINTF, spe\_program\_handle::elf\_image, spe\_context\_base\_priv::fd\_spe\_dir, and spe\_context\_base\_priv::loaded\_program.

Referenced by \_base\_spe\_context\_run(), and \_base\_spe\_program\_load().

```

{
    int objfd, len;
    char buf[20];
    spe_program_handle_t *program;

    program = spectx->base_private->loaded_program;

    if (!program || !program->elf_image) {
        DEBUG_PRINTF("%s called, but no program loaded\n", __func__);
        return;
    }

    objfd = openat(spectx->base_private->fd_spe_dir, "object-id", O_RDWR);
    if (objfd < 0)
        return;

    len = sprintf(buf, "%p", program->elf_image);
    write(objfd, buf, len + 1);
    close(objfd);

    __spe_context_update_event();
}

```

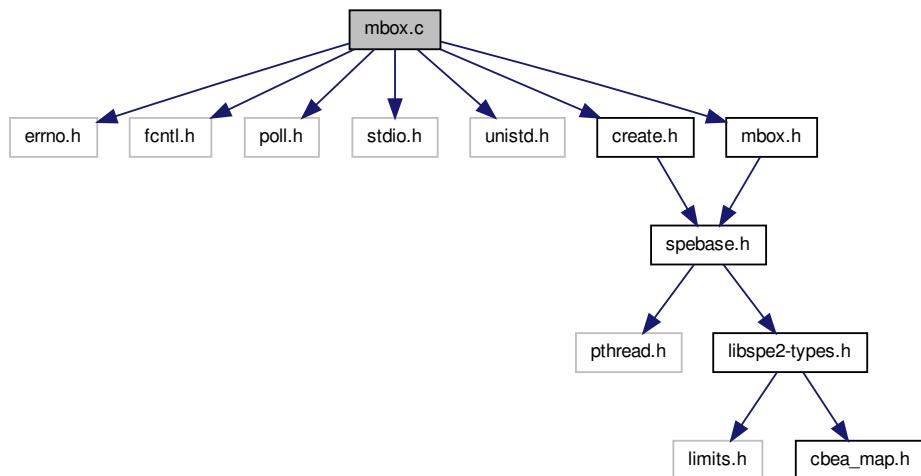
Here is the call graph for this function:



## 3.19 mbox.c File Reference

```
#include <errno.h>
#include <fcntl.h>
#include <poll.h>
#include <stdio.h>
#include <unistd.h>
#include "create.h"
#include "mbox.h"
```

Include dependency graph for mbox.c:



## Functions

- int [\\_base\\_spe\\_out\\_mbox\\_read](#) (spe\_context\_ptr\_t spectx, unsigned int mbox\_data[ ], int count)

- int `_base_spe_in_mbox_write` (`spe_context_ptr_t` `spectx`, `unsigned int *mbox_data`, `int count`, `int behavior_flag`)
- int `_base_spe_in_mbox_status` (`spe_context_ptr_t` `spectx`)
- int `_base_spe_out_mbox_status` (`spe_context_ptr_t` `spectx`)
- int `_base_spe_out_intr_mbox_status` (`spe_context_ptr_t` `spectx`)
- int `_base_spe_out_intr_mbox_read` (`spe_context_ptr_t` `spectx`, `unsigned int mbox_data[ ]`, `int count`, `int behavior_flag`)
- int `_base_spe_signal_write` (`spe_context_ptr_t` `spectx`, `unsigned int signal_reg`, `unsigned int data`)

### 3.19.1 Function Documentation

#### 3.19.1.1 int `_base_spe_in_mbox_status` ( `spe_context_ptr_t spectx` )

The `_base_spe_in_mbox_status` function fetches the status of the SPU inbound mailbox for the SPE thread specified by the `speid` parameter. A 0 value is return if the mailbox is full. A non-zero value specifies the number of available (32-bit) mailbox entries.

##### Parameters

<code>spectx</code>	Specifies the SPE context whose mailbox status is to be read.
---------------------	---

##### Returns

On success, returns the current status of the mailbox, respectively. On failure, -1 is returned.

##### See also

`spe_read_out_mbox`, `spe_write_in_mbox`, `read` (2)

Definition at line 202 of file `mbox.c`.

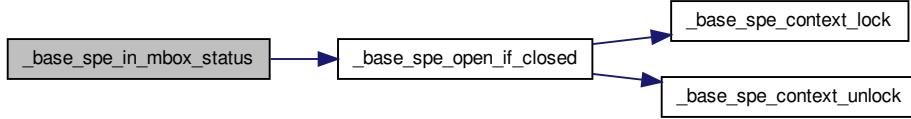
References `_base_spe_open_if_closed()`, `spe_context::base_private`, `spe_context_base_priv::cntl_mmap_base`, `FD_WBOX_STAT`, `spe_context_base_priv::flags`, `SPE_MAP_PS`, and `spe_spu_control_area::SPU_Mbox_Stat`.

```
{
    int rc, ret;
    volatile struct spe_spu_control_area *cntl_area =
        spectx->base_private->cntl_mmap_base;

    if (spectx->base_private->flags & SPE_MAP_PS) {
        ret = (cntl_area->SPU_Mbox_Stat >> 8) & 0xFF;
    } else {
        rc = read(_base_spe_open_if_closed(spectx, FD_WBOX_STAT, 0), &ret,
4);
        if (rc != 4)
            ret = -1;
    }

    return ret;
}
```

Here is the call graph for this function:



### 3.19.1.2 int \_base\_spe\_in\_mbox\_write ( spe\_context\_ptr\_t spectx, unsigned int \*mbox\_data, int count, int behavior\_flag )

Definition at line 112 of file mbox.c.

References `_base_spe_open_if_closed()`, `spe_context::base_private`, `FD_WBOX`, `FD_WBOX_NB`, `spe_context_base_priv::flags`, `SPE_MAP_PS`, `SPE_MBOX_ALL_BLOCKING`, `SPE_MBOX_ANY_BLOCKING`, and `SPE_MBOX_ANY_NONBLOCKING`.

```

{
    int rc;
    int total;
    unsigned int *aux;
    struct pollfd fds;

    if (mbox_data == NULL || count < 1) {
        errno = EINVAL;
        return -1;
    }

    switch (behavior_flag) {
    case SPE_MBOX_ALL_BLOCKING: // write all, even if blocking
        total = rc = 0;
        if (spectx->base_private->flags & SPE_MAP_PS) {
            do {
                aux = mbox_data + total;
                total += _base_spe_in_mbox_write_ps(spectx, aux,
                count - total);
                if (total < count) { // we could not write everyt
                hing, wait for space
                    fds.fd = _base_spe_open_if_closed(spectx,
                    FD_WBOX, 0);
                    fds.events = POLLOUT;
                    rc = poll(&fds, 1, -1);
                    if (rc == -1)
                        return -1;
                }
            } while (total < count);
        } else {
            while (total < 4*count) {
                rc = write(_base_spe_open_if_closed(spectx,
                FD_WBOX, 0),
                (const char *)mbox_data + total, 4*cou
                nt - total);
                if (rc == -1) {
                    break;
                }
                total += rc;
            }
        }
    }
}

```

```

        }
        total /=4;
    }
    break;

case SPE_MBOX_ANY_BLOCKING: // write at least one, even if blocking
    total = rc = 0;
    if (spectx->base_private->flags & SPE_MAP_PS) {
        do {
            total = _base_spe_in_mbox_write_ps(spectx, mbox_d
ata, count);
            if (total == 0) { // we could not anything, wait
for space
                fds.fd = _base_spe_open_if_closed(spectx,
FD_WBOX, 0);
                fds.events = POLLOUT;
                rc = poll(&fds, 1, -1);
                if (rc == -1)
                    return -1;
            }
        } while (total == 0);
    } else {
        rc = write(_base_spe_open_if_closed(spectx, FD_WBOX, 0), m
box_data, 4*count);
        total = rc/4;
    }
    break;

case SPE_MBOX_ANY_NONBLOCKING: // only write, if non blocking
    total = rc = 0;
    // write directly if we map the PS else write via spufs
    if (spectx->base_private->flags & SPE_MAP_PS) {
        total = _base_spe_in_mbox_write_ps(spectx, mbox_data, cou
nt);
    } else {
        rc = write(_base_spe_open_if_closed(spectx, FD_WBOX_NB, 0)
, mbox_data, 4*count);
        if (rc == -1 && errno == EAGAIN) {
            rc = 0;
            errno = 0;
        }
        total = rc/4;
    }
    break;

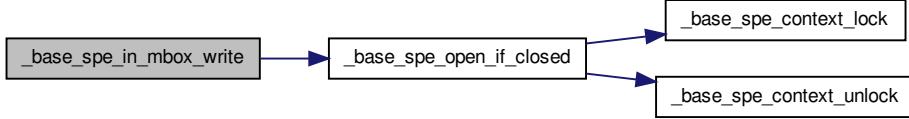
default:
    errno = EINVAL;
    return -1;
}

if (rc == -1) {
    errno = EIO;
    return -1;
}

return total;
}

```

Here is the call graph for this function:



### 3.19.1.3 int \_base\_spe\_out\_intr\_mbox\_read ( spe\_context\_ptr\_t spectx, unsigned int mbox\_data[], int count, int behavior\_flag )

The `_base_spe_out_intr_mbox_read` function reads the contents of the SPE outbound interrupting mailbox for the SPE context.

Definition at line 255 of file mbox.c.

References `_base_spe_open_if_closed()`, `FD_IBOX`, `FD_IBOX_NB`, `SPE_MBOX_ALL_BLOCKING`, `SPE_MBOX_ANY_BLOCKING`, and `SPE_MBOX_ANY_NONBLOCKING`.

```

{
    int rc;
    int total;

    if (mbox_data == NULL || count < 1) {
        errno = EINVAL;
        return -1;
    }

    switch (behavior_flag) {
        case SPE_MBOX_ALL_BLOCKING: // read all, even if blocking
            total = rc = 0;
            while (total < 4*count) {
                rc = read(_base_spe_open_if_closed(spectx, FD_IBOX, 0),
                          (char *)mbox_data + total, 4*count - total);
                if (rc == -1) {
                    break;
                }
                total += rc;
            }
            break;

        case SPE_MBOX_ANY_BLOCKING: // read at least one, even if blocking
            total = rc = read(_base_spe_open_if_closed(spectx, FD_IBOX, 0), mbox_
data, 4*count);
            break;

        case SPE_MBOX_ANY_NONBLOCKING: // only read, if non blocking
            rc = read(_base_spe_open_if_closed(spectx, FD_IBOX_NB, 0), mbox_da
ta, 4*count);
            if (rc == -1 && errno == EAGAIN) {
                rc = 0;
                errno = 0;
            }
            total = rc;
            break;
    }
}

```

```

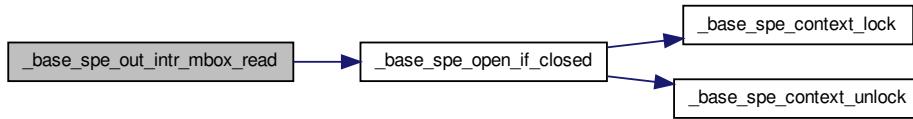
    default:
        errno = EINVAL;
        return -1;
    }

    if (rc == -1) {
        errno = EIO;
        return -1;
    }

    return rc / 4;
}

```

Here is the call graph for this function:



### 3.19.1.4 int \_base\_spe\_out\_intr\_mbox\_status ( spe\_context\_ptr\_t spectx )

The `_base_spe_out_intr_mbox_status` function fetches the status of the SPU outbound interrupt mailbox for the SPE thread specified by the `speid` parameter. A 0 value is returned if the mailbox is empty. A non-zero value specifies the number of 32-bit unread mailbox entries.

#### Parameters

<code>spectx</code>	Specifies the SPE context whose mailbox status is to be read.
---------------------	---

#### Returns

On success, returns the current status of the mailbox, respectively. On failure, -1 is returned.

#### See also

`spe_read_out_mbox`, `spe_write_in_mbox`, `read (2)`

Definition at line 238 of file `mbox.c`.

References `_base_spe_open_if_closed()`, `spe_context::base_private`, `spe_context_base_priv::ctl_mmap_base`, `FD_IBOX_STAT`, `spe_context_base_priv::flags`, `SPE_MAP_PS`, and `spe_spu_control_area::SPU_Mbox_Stat`.

```

{
    int rc, ret;
    volatile struct spe_spu_control_area *ctl_area =
        spectx->base_private->ctl_mmap_base;

    if (spectx->base_private->flags & SPE_MAP_PS) {
        ret = (ctl_area->SPU_Mbox_Stat >> 16) & 0xFF;
    } else {

```

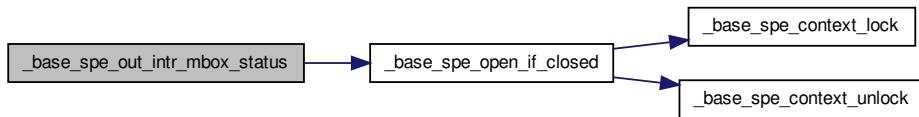
```

        rc = read(_base_spe_open_if_closed(spectx,FD_IBOX_STAT, 0), &ret,
4);
        if (rc != 4)
            ret = -1;

    }
    return ret;
}

```

Here is the call graph for this function:



### 3.19.1.5 int \_base\_spe\_out\_mbox\_read ( spe\_context\_ptr\_t spectx, unsigned int mbox\_data[], int count )

The `_base_spe_out_mbox_read` function reads the contents of the SPE outbound interrupting mailbox for the SPE thread speid.

The call will not block until the read request is satisfied, but instead return up to count currently available mailbox entries.

`spe_stat_out_intr_mbox` can be called to ensure that data is available prior to reading the outbound interrupting mailbox.

#### Parameters

<code>spectx</code>	Specifies the SPE thread whose outbound mailbox is to be read.
<code>mbox_data</code>	
<code>count</code>	

#### Return values

<code>&gt;0</code>	the number of 32-bit mailbox messages read
<code>=0</code>	no data available
<code>-1</code>	error condition and errno is set Possible values for errno: EINVAL speid is invalid Exxxx what else do we need here??

Definition at line 58 of file mbox.c.

References `_base_spe_open_if_closed()`, `spe_context::base_private`, `DEBUG_PRINTF`, `FD_MBOX`, `spe_context_base_priv::flags`, and `SPE_MAP_PS`.

```

{
    int rc;

    if (mbox_data == NULL || count < 1) {

```

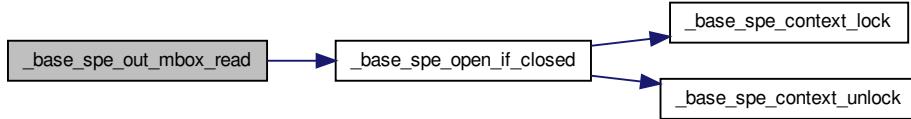
```

        errno = EINVAL;
        return -1;
    }

    if (spectx->base_private->flags & SPE_MAP_PS) {
        rc = _base_spe_out_mbox_read_ps(spectx, mbox_data, count);
    } else {
        rc = read(_base_spe_open_if_closed(spectx, FD_MBOX, 0), mbox_data,
count*4);
        DEBUG_PRINTF("%s read rc: %d\n", __FUNCTION__, rc);
        if (rc != -1) {
            rc /= 4;
        } else {
            if (errno == EAGAIN ) { // no data ready to be read
                errno = 0;
                rc = 0;
            }
        }
    }
    return rc;
}

```

Here is the call graph for this function:



### 3.19.1.6 int \_base\_spe\_out\_mbox\_status ( spe\_context\_ptr\_t spectx )

The `_base_spe_out_mbox_status` function fetches the status of the SPU outbound mailbox for the SPE thread specified by the `speid` parameter. A 0 value is returned if the mailbox is empty. A non-zero value specifies the number of 32-bit unread mailbox entries.

#### Parameters

<code>spectx</code>	Specifies the SPE context whose mailbox status is to be read.
---------------------	---

#### Returns

On success, returns the current status of the mailbox, respectively. On failure, -1 is returned.

#### See also

`spe_read_out_mbox`, `spe_write_in_mbox`, `read (2)`

Definition at line 220 of file `mbox.c`.

References `_base_spe_open_if_closed()`, `spe_context::base_private`, `spe_context_base_priv::cntl_mmap_base`, `FD_MBOX_STAT`, `spe_context_base_priv::flags`, `SPE_MAP_PS`, and `spe_spu_control_area::SPU_Mbox_Stat`.

```

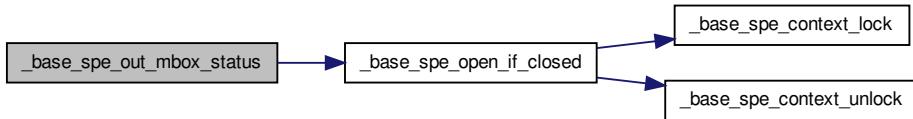
{
    int rc, ret;
    volatile struct spe_spu_control_area *cntl_area =
        spectx->base_private->cntl mmap_base;

    if (spectx->base_private->flags & SPE_MAP_PS) {
        ret = cntl_area->SPU_Mbox_Stat & 0xFF;
    } else {
        rc = read(_base_spe_open_if_closed(spectx, FD_MBOX_STAT, 0), &ret,
        4);
        if (rc != 4)
            ret = -1;
    }

    return ret;
}

```

Here is the call graph for this function:



### 3.19.1.7 int \_base\_spe\_signal\_write ( spe\_context\_ptr\_t *spectx*, unsigned int *signal\_reg*, unsigned int *data* )

The `_base_spe_signal_write` function writes data to the signal notification register specified by `signal_reg` for the SPE thread specified by the `speid` parameter.

#### Parameters

<code>spectx</code>	Specifies the SPE context whose signal register is to be written to.
<code>signal_reg</code>	Specified the signal notification register to be written. Valid signal notification registers are: SPE_SIG_NOTIFY_REG_1 SPE signal notification register 1 SPE_SIG_NOTIFY_REG_2 SPE signal notification register 2
<code>data</code>	The 32-bit data to be written to the specified signal notification register.

#### Returns

On success, `spe_write_signal` returns 0. On failure, -1 is returned.

#### See also

`spe_get_ps_area`, `spe_write_in_mbox`

Definition at line 307 of file mbox.c.

References `_base_spe_close_if_open()`, `_base_spe_open_if_closed()`, `spe_context::base_private`, `FD_SIG1`, `FD_SIG2`, `spe_context_base_priv::flags`, `spe_context_base_priv::signal1_mmap_base`, `spe_context_base_-`

priv::signal2\_mmap\_base, SPE\_MAP\_PS, SPE\_SIG\_NOTIFY\_REG\_1, SPE\_SIG\_NOTIFY\_REG\_2, spe\_sig\_notify\_1\_area::SPU\_Sig\_Notify\_1, and spe\_sig\_notify\_2\_area::SPU\_Sig\_Notify\_2.

```

{
    int rc;

    if (spectx->base_private->flags & SPE_MAP_PS) {
        if (signal_reg == SPE_SIG_NOTIFY_REG_1) {
            spe_sig_notify_1_area_t *sig = spectx->base_private->
signal1_mmap_base;

            sig->SPU_Sig_Notify_1 = data;
        } else if (signal_reg == SPE_SIG_NOTIFY_REG_2) {
            spe_sig_notify_2_area_t *sig = spectx->base_private->
signal2_mmap_base;

            sig->SPU_Sig_Notify_2 = data;
        } else {
            errno = EINVAL;
            return -1;
        }
        rc = 0;
    } else {
        if (signal_reg == SPE_SIG_NOTIFY_REG_1)
            rc = write(_base_spe_open_if_closed(spectx, FD_SIG1, 0), &
data, 4);
        else if (signal_reg == SPE_SIG_NOTIFY_REG_2)
            rc = write(_base_spe_open_if_closed(spectx, FD_SIG2, 0), &
data, 4);
        else {
            errno = EINVAL;
            return -1;
        }

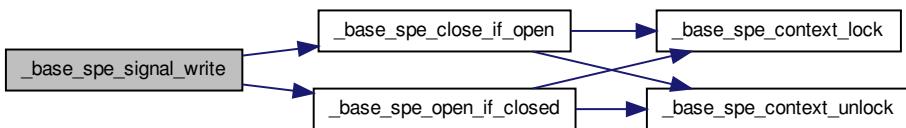
        if (rc == 4)
            rc = 0;

        if (signal_reg == SPE_SIG_NOTIFY_REG_1)
            _base_spe_close_if_open(spectx, FD_SIG1);
        else if (signal_reg == SPE_SIG_NOTIFY_REG_2)
            _base_spe_close_if_open(spectx, FD_SIG2);
    }
}

return rc;
}

```

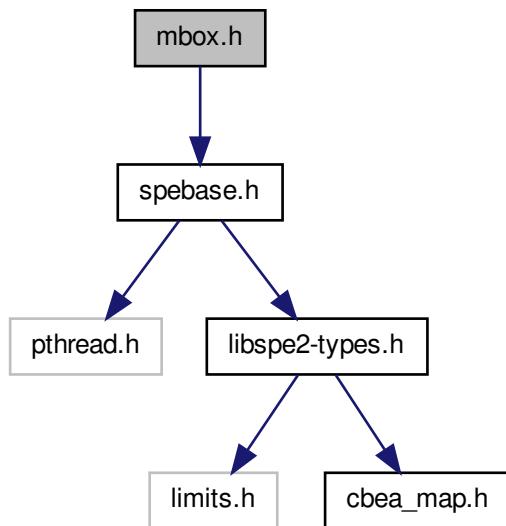
Here is the call graph for this function:



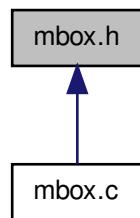
### 3.20 mbox.h File Reference

```
#include "spebase.h"
```

Include dependency graph for mbox.h:



This graph shows which files directly or indirectly include this file:

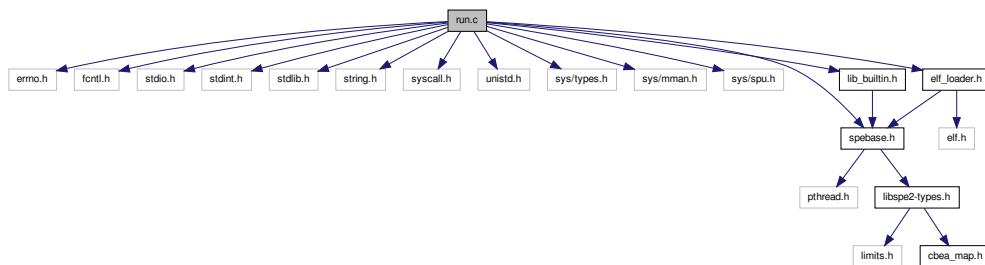


### 3.21 run.c File Reference

```
#include <errno.h>
```

```
#include <fcntl.h>
#include <stdio.h>
#include <stdint.h>
#include <stdlib.h>
#include <string.h>
#include <syscall.h>
#include <unistd.h>
#include <sys/types.h>
#include <sys/mman.h>
#include <sys/spu.h>
#include "elf_loader.h"
#include "lib_builtin.h"
#include "spebase.h"
```

Include dependency graph for run.c:



## Data Structures

- struct [spe\\_context\\_info](#)

## Defines

- #define [GNU\\_SOURCE](#) 1

## Functions

- int [\\_base\\_spe\\_context\\_run](#) ([spe\\_context\\_ptr\\_t](#) spe, unsigned int \*entry, unsigned int runflags, void \*argp, void \*envp, [spe\\_stop\\_info\\_t](#) \*stopinfo)

## Variables

- [\\_\\_thread struct spe\\_context\\_info \\* \\_\\_spe\\_current\\_active\\_context](#)

### 3.21.1 Define Documentation

#### 3.21.1.1 #define GNU\_SOURCE 1

Definition at line 20 of file run.c.

### 3.21.2 Function Documentation

#### 3.21.2.1 int \_base\_spe\_context\_run ( spe\_context\_ptr\_t spe, unsigned int \* entry, unsigned int runflags, void \* argp, void \* envp, spe\_stop\_info\_t \* stopinfo )

\_base\_spe\_context\_run starts execution of an SPE context with a loaded image

#### Parameters

<i>spectx</i>	Specifies the SPE context
<i>entry</i>	entry point for the SPE programm. If set to 0, entry point is determined by the ELF loader.
<i>runflags</i>	valid values are: SPE_RUN_USER_REGS Specifies that the SPE setup registers r3, r4, and r5 are initialized with the 48 bytes pointed to by argp. SPE_NO_CALLBACKS do not use built in library functions.
<i>argp</i>	An (optional) pointer to application specific data, and is passed as the second parameter to the SPE program.
<i>envp</i>	An (optional) pointer to environment specific data, and is passed as the third parameter to the SPE program.

Definition at line 99 of file run.c.

References \_\_spe\_current\_active\_context, \_base\_spe\_handle\_library\_callback(), \_base\_spe\_program\_load\_complete(), spe\_context::base\_private, DEBUG\_PRINTF, spe\_context\_base\_priv::emulated\_entry, spe\_context\_base\_priv::entry, spe\_context\_base\_priv::fd\_spe\_dir, spe\_context\_base\_priv::flags, LS\_SIZE, spe\_context\_base\_priv::mem\_mmap\_base, spe\_context\_info::npc, spe\_context\_info::prev, spe\_stop\_info::result, spe\_stop\_info::spe\_callback\_error, SPE\_CALLBACK\_ERROR, SPE\_DEFAULT\_ENTRY, SPE\_EVENTS\_ENABLE, SPE\_EXIT, spe\_stop\_info::spe\_exit\_code, spe\_context\_info::spe\_id, SPE\_ISOLATE, SPE\_ISOLATE\_EMULATE, spe\_stop\_info::spe\_isolation\_error, SPE\_ISOLATION\_ERROR, SPE\_NO\_CALLBACKS, SPE\_PROGRAM\_ISO\_LOAD\_COMPLETE, SPE\_PROGRAM\_ISOLATED\_STOP, SPE\_PROGRAM\_LIBRARY\_CALL, SPE\_PROGRAM\_NORMAL\_END, SPE\_RUN\_USER\_REGS, spe\_stop\_info::spe\_runtime\_error, SPE\_RUNTIME\_ERROR, spe\_stop\_info::spe\_runtime\_exception, SPE\_RUNTIME\_EXCEPTION, spe\_stop\_info::spe\_runtime\_fatal, SPE\_RUNTIME\_FATAL, spe\_stop\_info::spe\_signal\_code, SPE\_SPU\_HALT, SPE\_SPU\_INVALID\_CHANNEL, SPE\_SPU\_INVALID\_INSTR, SPE\_SPU\_STOPPED\_BY\_STOP, SPE\_SPU\_WAITING\_ON\_CHANNEL, SPE\_STOP\_AND\_SIGNAL, spe\_stop\_info::spu\_status, spe\_context\_info::status, spe\_stop\_info::stop\_reason, addr64::ui, and addr64::ull.

Referenced by \_event\_spe\_context\_run().

```
{
    int retval = 0, run_rc;
    unsigned int run_status, tmp_entry;
    spe_stop_info_t stopinfo_buf;
    struct spe_context_info this_context_info __attribute__((cleanup(cleanup
    peinfo)));
    /* If the caller hasn't set a stopinfo buffer, provide a buffer on the
     * stack instead. */
    if (!stopinfo)
```

```

stopinfo = &stopinfo_buf;

/* In emulated isolated mode, the npc will always return as zero.
 * use our private entry point instead */
if (spe->base_private->flags & SPE_ISOLATE_EMULATE)
    tmp_entry = spe->base_private->emulated_entry;

else if (*entry == SPE_DEFAULT_ENTRY)
    tmp_entry = spe->base_private->entry;
else
    tmp_entry = *entry;

/* If we're starting the SPE binary from its original entry point,
 * setup the arguments to main() */
if (tmp_entry == spe->base_private->entry &&
    !(spe->base_private->flags &
        (SPE_ISOLATE | SPE_ISOLATE_EMULATE))) {

    addr64 argp64, envp64, tid64, ls64;
    unsigned int regs[128][4];

    /* setup parameters */
    argp64.ull = (uint64_t)(unsigned long)argp;
    envp64.ull = (uint64_t)(unsigned long)envp;
    tid64.ull = (uint64_t)(unsigned long)spe;

    /* make sure the register values are 0 */
    memset(regs, 0, sizeof(regs));

    /* set sensible values for stack_ptr and stack_size */
    regs[1][0] = (unsigned int) LS_SIZE - 16;           /* stack_ptr */
    regs[2][0] = 0;                                     /
    /* stack_size ( 0 = default ) */

    if (runflags & SPE_RUN_USER_REGS) {
        /* When SPE_USER_REGS is set, argp points to an array
         * of 3x128b registers to be passed directly to the SPE
         * program.
         */
        memcpy(regs[3], argp, sizeof(unsigned int) * 12);
    } else {
        regs[3][0] = tid64.ui[0];
        regs[3][1] = tid64.ui[1];

        regs[4][0] = argp64.ui[0];
        regs[4][1] = argp64.ui[1];

        regs[5][0] = envp64.ui[0];
        regs[5][1] = envp64.ui[1];
    }

    /* Store the LS base address in R6 */
    ls64.ull = (uint64_t)(unsigned long)spe->base_private->
mem_mmap_base;
    regs[6][0] = ls64.ui[0];
    regs[6][1] = ls64.ui[1];

    if (set_regs(spe, regs))
        return -1;
}

/*Leave a trail of breadcrumbs for the debugger to follow */
if (!__spe_current_active_context) {
    __spe_current_active_context = &this_context_info;
    if (!__spe_current_active_context)
        return -1;
}

```

```

        __spe_current_active_context->prev = NULL;
    } else {
        struct spe_context_info *newinfo;
        newinfo = &this_context_info;
        if (!newinfo)
            return -1;
        newinfo->prev = __spe_current_active_context;
        __spe_current_active_context = newinfo;
    }
/*remember the ls-addr*/
__spe_current_active_context->spe_id = spe->base_private->fd_spe_dir;

do_run:
/*Remember the npc value*/
__spe_current_active_context->npc = tmp_entry;

/* run SPE context */
run_rc = spu_run(spe->base_private->fd_spe_dir,
                 &tmp_entry, &run_status);

/*Remember the npc value*/
__spe_current_active_context->npc = tmp_entry;
__spe_current_active_context->status = run_status;

DEBUG_PRINTF("spu_run returned run_rc=0x%08x, entry=0x%04x, "
             "ext_status=0x%04x.\n", run_rc, tmp_entry, run_status);

/* set up return values and stopinfo according to spu_run exit
 * conditions. This is overwritten on error.
 */
stopinfo->spu_status = run_rc;

if (spe->base_private->flags & SPE_ISOLATE_EMULATE) {
    /* save the entry point, and pretend that the npc is zero */
    spe->base_private->emulated_entry = tmp_entry;
    *entry = 0;
} else {
    *entry = tmp_entry;
}

/* Return with stopinfo set on syscall error paths */
if (run_rc == -1) {
    DEBUG_PRINTF("spu_run returned error %d, errno=%d\n",
                 run_rc, errno);
    stopinfo->stop_reason = SPE_RUNTIME_FATAL;
    stopinfo->result.spe_runtime_fatal = errno;
    retval = -1;

    /* For isolated contexts, pass EPERM up to the
     * caller.
     */
    if (!(spe->base_private->flags & SPE_ISOLATE
          && errno == EPERM))
        errno = EFAULT;
}

} else if (run_rc & SPE_SPU_INVALID_INSTR) {
    DEBUG_PRINTF("SPU has tried to execute an invalid "
                 "instruction. %d\n", run_rc);
    stopinfo->stop_reason = SPE_RUNTIME_ERROR;
    stopinfo->result.spe_runtime_error = SPE_SPU_INVALID_INSTR;
    errno = EFAULT;
    retval = -1;

} else if ((spe->base_private->flags & SPE_EVENTS_ENABLE) && run_status)
{
    /* Report asynchronous error if return val are set and
     * SPU events are enabled.
}

```

```

        */

stopinfo->stop_reason = SPE_RUNTIME_EXCEPTION;
stopinfo->result.spe_runtime_exception = run_status;
stopinfo->spu_status = -1;
errno = EIO;
retval = -1;

} else if (run_rc & SPE_SPU_STOPPED_BY_STOP) {
    /* Stop & signals are broken down into three groups
     * 1. SPE library call
     * 2. SPE user defined stop & signal
     * 3. SPE program end.
     *
     * These groups are signified by the 14-bit stop code:
     */
    int stopcode = (run_rc >> 16) & 0xffff;

    /* Check if this is a library callback, and callbacks are
     * allowed (ie, running without SPE_NO_CALLBACKS)
     */
    if ((stopcode & 0xff00) == SPE_PROGRAM_LIBRARY_CALL
        && !(runflags & SPE_NO_CALLBACKS)) {

        int callback_rc, callback_number = stopcode & 0xff;

        /* execute library callback */
        DEBUG_PRINTF("SPE library call: %d\n", callback_number);
        callback_rc = _base_spe_handle_library_callback(spe,
                                                       callback_
number, *entry);

        if (callback_rc) {
            /* library callback failed; set errno and
             * return immediately */
            DEBUG_PRINTF("SPE library call failed: %d\n",
                         callback_rc);
            stopinfo->stop_reason = SPE_CALLBACK_ERROR;
            stopinfo->result.spe_callback_error =
                callback_rc;
            errno = EFAULT;
            retval = -1;
        } else {
            /* successful library callback - restart the SPE
             * program at the next instruction */
            tmp_entry += 4;
            goto do_run;
        }
    }

} else if ((stopcode & 0xff00) == SPE_PROGRAM_NORMAL_END) {
    /* The SPE program has exited by exit(X) */
    stopinfo->stop_reason = SPE_EXIT;
    stopinfo->result.spe_exit_code = stopcode & 0xff;

    if (spe->base_private->flags & SPE_ISOLATE) {
        /* Issue an isolated exit, and re-run the SPE.
         * We should see a return value without the
         * 0x80 bit set. */
        if (!issue_isolated_exit(spe))
            goto do_run;
        retval = -1;
    }
}

} else if ((stopcode & 0xffff0) == SPE_PROGRAM_ISOLATED_STOP) {

    /* 0x2206: isolated app has been loaded by loader;
     * provide a hook for the debugger to catch this,
     * and restart
}

```

```

        */
        if (stopcode == SPE_PROGRAM_ISO_LOAD_COMPLETE) {
            _base_spe_program_load_complete(spe);
            goto do_run;
        } else {
            stopinfo->stop_reason = SPE_ISOLATION_ERROR;
            stopinfo->result.spe_isolation_error =
                stopcode & 0xf;
        }

    } else if (spe->base_private->flags & SPE_ISOLATE &&
               !(run_rc & 0x80)) {
        /* We've successfully exited isolated mode */
        retval = 0;

    } else {
        /* User defined stop & signal, including
         * callbacks when disabled */
        stopinfo->stop_reason = SPE_STOP_AND_SIGNAL;
        stopinfo->result.spe_signal_code = stopcode;
        retval = stopcode;
    }

} else if (run_rc & SPE_SPU_HALT) {
    DEBUG_PRINTF("SPU was stopped by halt. %d\n", run_rc);
    stopinfo->stop_reason = SPE_RUNTIME_ERROR;
    stopinfo->result.spe_runtime_error = SPE_SPU_HALT;
    errno = EFAULT;
    retval = -1;

} else if (run_rc & SPE_SPU_WAITING_ON_CHANNEL) {
    DEBUG_PRINTF("SPU is waiting on channel. %d\n", run_rc);
    stopinfo->stop_reason = SPE_RUNTIME_EXCEPTION;
    stopinfo->result.spe_runtime_exception = run_status;
    stopinfo->spu_status = -1;
    errno = EIO;
    retval = -1;

} else if (run_rc & SPE_SPU_INVALID_CHANNEL) {
    DEBUG_PRINTF("SPU has tried to access an invalid "
                 "channel. %d\n", run_rc);
    stopinfo->stop_reason = SPE_RUNTIME_ERROR;
    stopinfo->result.spe_runtime_error = SPE_SPU_INVALID_CHANNEL;
    errno = EFAULT;
    retval = -1;

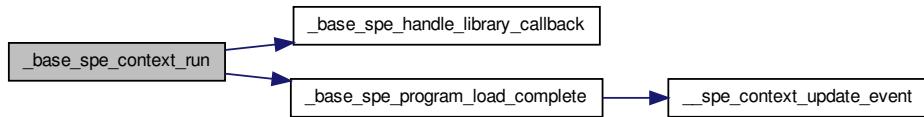
} else {
    DEBUG_PRINTF("spu_run returned invalid data: 0x%04x\n", run_rc);
    stopinfo->stop_reason = SPE_RUNTIME_FATAL;
    stopinfo->result.spe_runtime_fatal = -1;
    stopinfo->spu_status = -1;
    errno = EFAULT;
    retval = -1;

}

freespeinfo();
return retval;
}

```

Here is the call graph for this function:



### 3.21.3 Variable Documentation

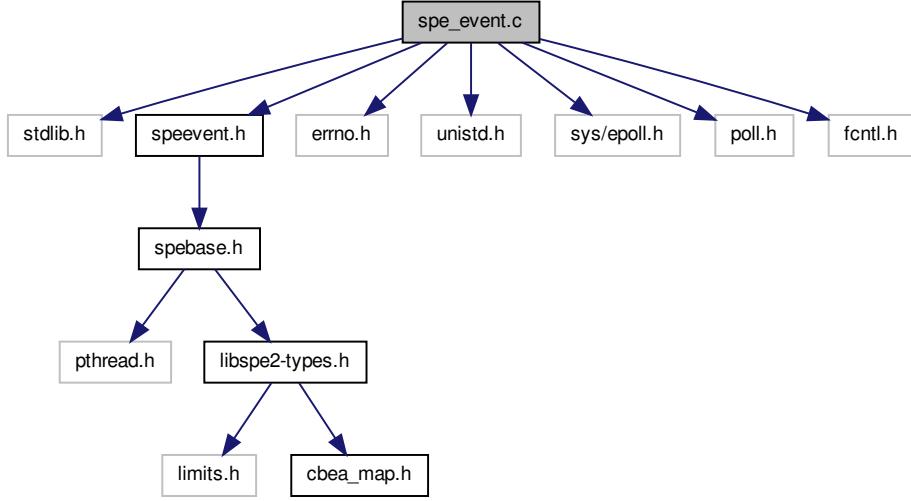
#### 3.21.3.1 `_thread struct spe_context_info* __spe_current_active_context`

Referenced by `_base_spe_context_run()`.

## 3.22 spe\_event.c File Reference

```
#include <stdlib.h>
#include "speevent.h"
#include <errno.h>
#include <unistd.h>
#include <sys/epoll.h>
#include <poll.h>
#include <fcntl.h>
```

Include dependency graph for spe\_event.c:



## Defines

- #define \_\_SPE\_EVENT\_ALL
- #define \_\_SPE\_EPOLL\_SIZE 10
- #define \_\_SPE\_EPOLL\_FD\_GET(handler) (\*(int\*)(handler))
- #define \_\_SPE\_EPOLL\_FD\_SET(handler, fd) (\*(int\*)(handler) = (fd))
- #define \_\_SPE\_EVENT\_CONTEXT\_PRIV\_GET(spe) ((spe\_context\_event\_priv\_ptr\_t)(spe)->event\_private)
- #define \_\_SPE\_EVENT\_CONTEXT\_PRIV\_SET(spe, evctx) ((spe)->event\_private = (evctx))
- #define \_\_SPE\_EVENTS\_ENABLED(spe) ((spe)->base\_private->flags & SPE\_EVENTS\_ENABLE)

## Functions

- void \_event\_spe\_context\_lock (spe\_context\_ptr\_t spe)
- void \_event\_spe\_context\_unlock (spe\_context\_ptr\_t spe)
- int \_event\_spe\_stop\_info\_read (spe\_context\_ptr\_t spe, spe\_stop\_info\_t \*stopinfo)
- spe\_event\_handler\_ptr\_t \_event\_spe\_event\_handler\_create (void)
- int \_event\_spe\_event\_handler\_destroy (spe\_event\_handler\_ptr\_t evhandler)
- int \_event\_spe\_event\_handler\_register (spe\_event\_handler\_ptr\_t evhandler, spe\_event\_unit\_t \*event)
- int \_event\_spe\_event\_handler\_deregister (spe\_event\_handler\_ptr\_t evhandler, spe\_event\_unit\_t \*event)
- int \_event\_spe\_event\_wait (spe\_event\_handler\_ptr\_t evhandler, spe\_event\_unit\_t \*events, int max\_events, int timeout)
- int \_event\_spe\_context\_finalize (spe\_context\_ptr\_t spe)
- struct spe\_context\_event\_priv \* \_event\_spe\_context\_initialize (spe\_context\_ptr\_t spe)
- int \_event\_spe\_context\_run (spe\_context\_ptr\_t spe, unsigned int \*entry, unsigned int runflags, void \*argp, void \*envp, spe\_stop\_info\_t \*stopinfo)

### 3.22.1 Define Documentation

**3.22.1.1 #define \_\_SPE\_EPOLL\_FD\_GET( handler ) (\*(int\*)(handler))**

Definition at line 37 of file spe\_event.c.

Referenced by \_event\_spe\_event\_handler\_deregister(), \_event\_spe\_event\_handler\_destroy(), \_event\_spe\_event\_handler\_register(), and \_event\_spe\_event\_wait().

**3.22.1.2 #define \_\_SPE\_EPOLL\_FD\_SET( handler, fd ) (\*(int\*)(handler) = (fd))**

Definition at line 38 of file spe\_event.c.

Referenced by \_event\_spe\_event\_handler\_create().

**3.22.1.3 #define \_\_SPE\_EPOLL\_SIZE 10**

Definition at line 35 of file spe\_event.c.

Referenced by \_event\_spe\_event\_handler\_create().

**3.22.1.4 #define \_\_SPE\_EVENT\_ALL**

**Value:**

```
( SPE_EVENT_OUT_INTR_MBOX | SPE_EVENT_IN_MBOX | \
SPE_EVENT_TAG_GROUP | SPE_EVENT_SPE_STOPPED )
```

Definition at line 31 of file spe\_event.c.

Referenced by \_event\_spe\_event\_handler\_deregister(), and \_event\_spe\_event\_handler\_register().

**3.22.1.5 #define \_\_SPE\_EVENT\_CONTEXT\_PRIV\_GET( spe ) ((spe\_context\_event\_priv\_ptr\_t)(spe)->event\_private)**

Definition at line 40 of file spe\_event.c.

Referenced by \_event\_spe\_context\_finalize(), \_event\_spe\_context\_lock(), \_event\_spe\_context\_run(), \_event\_spe\_context\_unlock(), \_event\_spe\_event\_handler\_deregister(), \_event\_spe\_event\_handler\_register(), and \_event\_spe\_stop\_info\_read().

**3.22.1.6 #define \_\_SPE\_EVENT\_CONTEXT\_PRIV\_SET( spe, evctx ) ((spe)->event\_private = (evctx))**

Definition at line 42 of file spe\_event.c.

Referenced by \_event\_spe\_context\_finalize().

**3.22.1.7 #define \_\_SPE\_EVENTS\_ENABLED( spe ) ((spe)->base\_private->flags & SPE\_EVENTS\_ENABLE)**

Definition at line 45 of file spe\_event.c.

Referenced by \_event\_spe\_event\_handler\_deregister(), and \_event\_spe\_event\_handler\_register().

### 3.22.2 Function Documentation

#### 3.22.2.1 int \_event\_spe\_context\_finalize ( spe\_context\_ptr\_t spe )

Definition at line 416 of file spe\_event.c.

References \_\_SPE\_EVENT\_CONTEXT\_PRIV\_GET, \_\_SPE\_EVENT\_CONTEXT\_PRIV\_SET, spe\_context\_event\_priv::lock, spe\_context\_event\_priv::stop\_event\_pipe, and spe\_context\_event\_priv::stop\_event\_read\_lock.

```
{
    spe_context_event_priv_ptr_t evctx;

    if (!spe) {
        errno = ESRCH;
        return -1;
    }

    evctx = __SPE_EVENT_CONTEXT_PRIV_GET(spe);
    __SPE_EVENT_CONTEXT_PRIV_SET(spe, NULL);

    close(evctx->stop_event_pipe[0]);
    close(evctx->stop_event_pipe[1]);

    pthread_mutex_destroy(&evctx->lock);
    pthread_mutex_destroy(&evctx->stop_event_read_lock);

    free(evctx);

    return 0;
}
```

#### 3.22.2.2 struct spe\_context\_event\_priv\* \_event\_spe\_context\_initialize ( spe\_context\_ptr\_t spe ) [read]

Definition at line 439 of file spe\_event.c.

References spe\_context\_event\_priv::events, spe\_context\_event\_priv::lock, spe\_event\_unit::spe, spe\_context\_event\_priv::stop\_event\_pipe, and spe\_context\_event\_priv::stop\_event\_read\_lock.

```
{
    spe_context_event_priv_ptr_t evctx;
    int rc;
    int i;

    evctx = calloc(1, sizeof(*evctx));
    if (!evctx) {
        return NULL;
    }

    rc = pipe(evctx->stop_event_pipe);
    if (rc == -1) {
        free(evctx);
        return NULL;
    }
    rc = fcntl(evctx->stop_event_pipe[0], F_GETFL);
    if (rc != -1) {
        rc = fcntl(evctx->stop_event_pipe[0], F_SETFL, rc | O_NONBLOCK);
    }
    if (rc == -1) {
        close(evctx->stop_event_pipe[0]);
        close(evctx->stop_event_pipe[1]);
    }
}
```

```

    free(evctx);
    errno = EIO;
    return NULL;
}

for (i = 0; i < sizeof(evctx->events) / sizeof(evctx->events[0]); i++) {
    evctx->events[i].spe = spe;
}

pthread_mutex_init(&evctx->lock, NULL);
pthread_mutex_init(&evctx->stop_event_read_lock, NULL);

return evctx;
}

```

### 3.22.2.3 void \_event\_spe\_context\_lock( spe\_context\_ptr\_t spe )

Definition at line 49 of file spe\_event.c.

References \_\_SPE\_EVENT\_CONTEXT\_PRIV\_GET.

Referenced by \_event\_spe\_event\_handler\_deregister(), \_event\_spe\_event\_handler\_register(), and \_event\_spe\_event\_wait().

```

{
    pthread_mutex_lock(&__SPE_EVENT_CONTEXT_PRIV_GET(spe)->lock);
}

```

### 3.22.2.4 int \_event\_spe\_context\_run( spe\_context\_ptr\_t spe, unsigned int \* entry, unsigned int runflags, void \* argp, void \* envp, spe\_stop\_info\_t \* stopinfo )

Definition at line 477 of file spe\_event.c.

References \_\_SPE\_EVENT\_CONTEXT\_PRIV\_GET, \_base\_spe\_context\_run(), and spe\_context\_event\_priv::stop\_event\_pipe.

```

{
    spe_context_event_priv_ptr_t evctx;
    spe_stop_info_t stopinfo_buf;
    int rc;

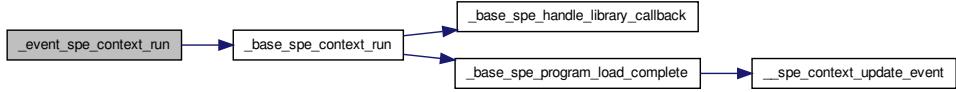
    if (!stopinfo) {
        stopinfo = &stopinfo_buf;
    }
    rc = _base_spe_context_run(spe, entry, runflags, argp, envp, stopinfo);

    evctx = __SPE_EVENT_CONTEXT_PRIV_GET(spe);
    if (write(evctx->stop_event_pipe[1], stopinfo, sizeof(*stopinfo)) != sizeof(*stopinfo)) {
        /* error check. */
    }

    return rc;
}

```

Here is the call graph for this function:



### 3.22.2.5 void \_event\_spe\_context\_unlock( spe\_context\_ptr\_t spe )

Definition at line 54 of file spe\_event.c.

References \_\_SPE\_EVENT\_CONTEXT\_PRIV\_GET.

Referenced by \_event\_spe\_event\_handler\_deregister(), \_event\_spe\_event\_handler\_register(), and \_event\_spe\_event\_wait().

```
{
    pthread_mutex_unlock(&__SPE_EVENT_CONTEXT_PRIV_GET(spe)->lock);
}
```

### 3.22.2.6 spe\_event\_handler\_ptr\_t \_event\_spe\_event\_handler\_create( void )

Definition at line 110 of file spe\_event.c.

References \_\_SPE\_EPOLL\_FD\_SET, and \_\_SPE\_EPOLL\_SIZE.

```
{
    int epfd;
    spe_event_handler_t *evhandler;

    evhandler = calloc(1, sizeof(*evhandler));
    if (!evhandler) {
        return NULL;
    }

    epfd = epoll_create(__SPE_EPOLL_SIZE);
    if (epfd == -1) {
        free(evhandler);
        return NULL;
    }

    __SPE_EPOLL_FD_SET(evhandler, epfd);

    return evhandler;
}
```

### 3.22.2.7 int \_event\_spe\_event\_handler\_deregister( spe\_event\_handler\_ptr\_t evhandler, spe\_event\_unit\_t \* event )

Definition at line 273 of file spe\_event.c.

References \_\_base\_spe\_event\_source\_acquire(), \_\_SPE\_EPOLL\_FD\_GET, \_\_SPE\_EVENT\_ALL, \_\_SPE\_EVENT\_CONTEXT\_PRIV\_GET, \_\_SPE\_EVENT\_IN\_MBOX, \_\_SPE\_EVENT\_OUT\_INTR\_MBOX, \_\_SPE\_EVENT\_SPE\_STOPPED, \_\_SPE\_EVENT\_TAG\_GROUP, \_\_SPE\_EVENTS\_ENABLED, \_event\_spe\_context\_lock(), \_event\_spe\_context\_unlock(), spe\_context\_event\_priv::events, spe\_event\_unit::events, FD\_IBOX, FD\_MFC, FD\_WBOX, spe\_event\_unit::spe, SPE\_EVENT\_IN\_MBOX, SPE\_EVENT\_OUT\_INTR\_MBOX, SPE\_EVENT\_SPE\_STOPPED, SPE\_EVENT\_TAG\_GROUP, and spe\_context\_event\_priv::stop\_event\_pipe.

```

{
    int epfd;
    const int ep_op = EPOLL_CTL_DEL;
    spe_context_event_priv_ptr_t evctx;
    int fd;

    if (!evhandler) {
        errno = ESRCH;
        return -1;
    }
    if (!event || !event->spe) {
        errno = EINVAL;
        return -1;
    }
    if (!__SPE_EVENTS_ENABLED(event->spe)) {
        errno = ENOTSUP;
        return -1;
    }

    epfd = __SPE_EPOLL_FD_GET(evhandler);
    evctx = __SPE_EVENT_CONTEXT_PRIV_GET(event->spe);

    if (event->events & ~__SPE_EVENT_ALL) {
        errno = ENOTSUP;
        return -1;
    }

    _event_spe_context_lock(event->spe); /* for spe->event_private->events */

    if (event->events & SPE_EVENT_OUT_INTR_MBOX) {
        fd = __base_spe_event_source_acquire(event->spe, FD_IBOX);
        if (fd == -1) {
            _event_spe_context_unlock(event->spe);
            return -1;
        }
        if (epoll_ctl(epfd, ep_op, fd, NULL) == -1) {
            _event_spe_context_unlock(event->spe);
            return -1;
        }
        evctx->events[__SPE_EVENT_OUT_INTR_MBOX].events = 0;
    }

    if (event->events & SPE_EVENT_IN_MBOX) {
        fd = __base_spe_event_source_acquire(event->spe, FD_WBOX);
        if (fd == -1) {
            _event_spe_context_unlock(event->spe);
            return -1;
        }
        if (epoll_ctl(epfd, ep_op, fd, NULL) == -1) {
            _event_spe_context_unlock(event->spe);
            return -1;
        }
        evctx->events[__SPE_EVENT_IN_MBOX].events = 0;
    }

    if (event->events & SPE_EVENT_TAG_GROUP) {
        fd = __base_spe_event_source_acquire(event->spe, FD_MFC);
        if (fd == -1) {

```

```

        _event_spe_context_unlock(event->spe);
        return -1;
    }
    if (epoll_ctl(epfd, ep_op, fd, NULL) == -1) {
        _event_spe_context_unlock(event->spe);
        return -1;
    }
    evctx->events[___SPE_EVENT_TAG_GROUP].events = 0;
}

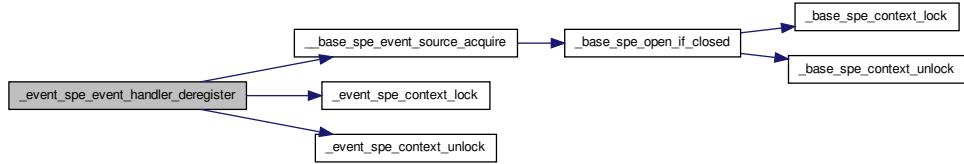
if (event->events & SPE_EVENT_SPE_STOPPED) {
    fd = evctx->stop_event_pipe[0];
    if (epoll_ctl(epfd, ep_op, fd, NULL) == -1) {
        _event_spe_context_unlock(event->spe);
        return -1;
    }
    evctx->events[___SPE_EVENT_SPE_STOPPED].events = 0;
}

_event_spe_context_unlock(event->spe);

return 0;
}

```

Here is the call graph for this function:



### 3.22.2.8 int \_event\_spe\_event\_handler\_destroy ( spe\_event\_handler\_ptr\_t evhandler )

Definition at line 135 of file spe\_event.c.

References \_\_\_SPE\_EPOLL\_FD\_GET.

```

{
    int epfd;

    if (!evhandler) {
        errno = ESRCH;
        return -1;
    }

    epfd = ___SPE_EPOLL_FD_GET(evhandler);
    close(epfd);

    free(evhandler);
    return 0;
}

```

### 3.22.2.9 int \_event\_spe\_event\_handler\_register ( spe\_event\_handler\_ptr\_t evhandler, spe\_event\_unit\_t \* event )

Definition at line 155 of file spe\_event.c.

References \_\_base\_spe\_event\_source\_acquire(), \_\_SPE\_EPOLL\_FD\_GET, \_\_SPE\_EVENT\_ALL, \_\_SPE\_EVENT\_CONTEXT\_PRIV\_GET, \_\_SPE\_EVENT\_IN\_MBOX, \_\_SPE\_EVENT\_OUT\_INTR\_MBOX, \_\_SPE\_EVENT\_SPE\_STOPPED, \_\_SPE\_EVENT\_TAG\_GROUP, \_\_SPE\_EVENTS\_ENABLED, \_event\_spe\_context\_lock(), \_event\_spe\_context\_unlock(), spe\_context::base\_private, spe\_event\_unit::data, spe\_context\_event\_priv::events, spe\_event\_unit::events, FD\_IBOX, FD\_MFC, FD\_WBOX, spe\_context\_base\_priv::flags, spe\_event\_data::ptr, spe\_event\_unit::spe, SPE\_EVENT\_IN\_MBOX, SPE\_EVENT\_OUT\_INTR\_MBOX, SPE\_EVENT\_SPE\_STOPPED, SPE\_EVENT\_TAG\_GROUP, SPE\_MAP\_PS, and spe\_context\_event\_priv::stop\_event\_pipe.

```
{
    int epfd;
    const int ep_op = EPOLL_CTL_ADD;
    spe_context_event_priv_ptr_t evctx;
    spe_event_unit_t *ev_buf;
    struct epoll_event ep_event;
    int fd;

    if (!evhandler) {
        errno = ESRCH;
        return -1;
    }
    if (!event || !event->spe) {
        errno = EINVAL;
        return -1;
    }
    if (!__SPE_EVENTS_ENABLED(event->spe)) {
        errno = ENOTSUP;
        return -1;
    }

    epfd = __SPE_EPOLL_FD_GET(evhandler);
    evctx = __SPE_EVENT_CONTEXT_PRIV_GET(event->spe);

    if (event->events & ~__SPE_EVENT_ALL) {
        errno = ENOTSUP;
        return -1;
    }

    _event_spe_context_lock(event->spe); /* for spe->event_private->events */

    if (event->events & SPE_EVENT_OUT_INTR_MBOX) {
        fd = __base_spe_event_source_acquire(event->spe, FD_IBOX);
        if (fd == -1) {
            _event_spe_context_unlock(event->spe);
            return -1;
        }

        ev_buf = &evctx->events[__SPE_EVENT_OUT_INTR_MBOX];
        ev_buf->events = SPE_EVENT_OUT_INTR_MBOX;
        ev_buf->data = event->data;

        ep_event.events = EPOLLIN;
        ep_event.data.ptr = ev_buf;
        if (epoll_ctl(epfd, ep_op, fd, &ep_event) == -1) {
            _event_spe_context_unlock(event->spe);
            return -1;
        }
    }

    if (event->events & SPE_EVENT_IN_MBOX) {
```

```
fd = __base_spe_event_source_acquire(event->spe, FD_WBOX);
if (fd == -1) {
    _event_spe_context_unlock(event->spe);
    return -1;
}

ev_buf = &evctx->events[___SPE_EVENT_IN_MBOX];
ev_buf->events = SPE_EVENT_IN_MBOX;
ev_buf->data = event->data;

ep_event.events = EPOLLOUT;
ep_event.data.ptr = ev_buf;
if (epoll_ctl(epfd, ep_op, fd, &ep_event) == -1) {
    _event_spe_context_unlock(event->spe);
    return -1;
}
}

if (event->events & SPE_EVENT_TAG_GROUP) {
    fd = __base_spe_event_source_acquire(event->spe, FD_MFC);
    if (fd == -1) {
        _event_spe_context_unlock(event->spe);
        return -1;
    }

    if (event->spe->base_private->flags & SPE_MAP_PS) {
        _event_spe_context_unlock(event->spe);
        errno = ENOTSUP;
        return -1;
    }

    ev_buf = &evctx->events[___SPE_EVENT_TAG_GROUP];
    ev_buf->events = SPE_EVENT_TAG_GROUP;
    ev_buf->data = event->data;

    ep_event.events = EPOLLIN;
    ep_event.data.ptr = ev_buf;
    if (epoll_ctl(epfd, ep_op, fd, &ep_event) == -1) {
        _event_spe_context_unlock(event->spe);
        return -1;
    }
}

if (event->events & SPE_EVENT_SPE_STOPPED) {
    fd = evctx->stop_event_pipe[0];

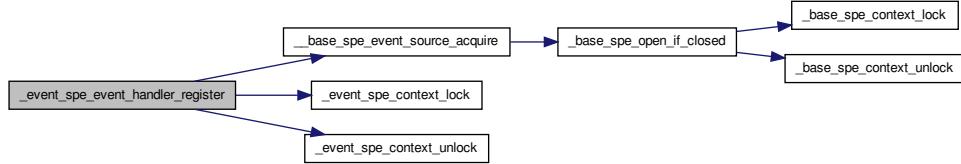
    ev_buf = &evctx->events[___SPE_EVENT_SPE_STOPPED];
    ev_buf->events = SPE_EVENT_SPE_STOPPED;
    ev_buf->data = event->data;

    ep_event.events = EPOLLIN;
    ep_event.data.ptr = ev_buf;
    if (epoll_ctl(epfd, ep_op, fd, &ep_event) == -1) {
        _event_spe_context_unlock(event->spe);
        return -1;
    }
}

_event_spe_context_unlock(event->spe);

return 0;
}
```

Here is the call graph for this function:



### 3.22.2.10 int \_event\_spe\_event\_wait( spe\_event\_handler\_ptr\_t evhandler, spe\_event\_unit\_t \*events, int max\_events, int timeout )

Definition at line 360 of file spe\_event.c.

References `__SPE_EPOLL_FD_GET`, `_event_spe_context_lock()`, `_event_spe_context_unlock()`, and `spe_event_unit::spe`.

```

{
    int epfd;
    struct epoll_event *ep_events;
    int rc;

    if (!evhandler) {
        errno = ESRCH;
        return -1;
    }
    if (!events || max_events <= 0) {
        errno = EINVAL;
        return -1;
    }

    epfd = __SPE_EPOLL_FD_GET(evhandler);

    ep_events = malloc(sizeof(*ep_events) * max_events);
    if (!ep_events) {
        return -1;
    }

    for ( ; ; ) {
        rc = epoll_wait(epfd, ep_events, max_events, timeout);
        if (rc == -1) { /* error */
            if (errno == EINTR) {
                if (timeout >= 0) { /* behave as timeout */
                    rc = 0;
                    break;
                }
                /* else retry */
            }
            else {
                break;
            }
        }
        else if (rc > 0) {
            int i;
            for (i = 0; i < rc; i++) {
                spe_event_unit_t *ev = (spe_event_unit_t *) (ep_events[i].data.ptr);
                _event_spe_context_lock(ev->spe); /* lock ev itself */

```

```

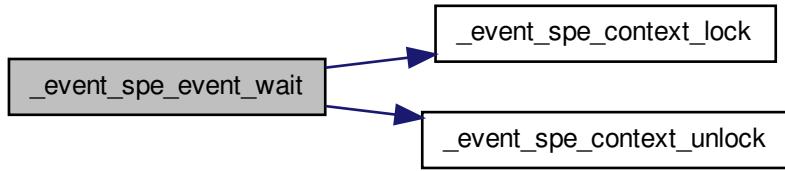
        events[i] = *ev;
        _event_spe_context_unlock(ev->spe);
    }
    break;
}
else /* timeout */
{
    break;
}
}

free(ep_events);

return rc;
}

```

Here is the call graph for this function:



### 3.22.2.11 int \_event\_spe\_stop\_info\_read ( spe\_context\_ptr\_t spe, spe\_stop\_info\_t \* stopinfo )

Definition at line 59 of file spe\_event.c.

References `__SPE_EVENT_CONTEXT_PRIV_GET`, `spe_context_event_priv::stop_event_pipe`, and `spe_context_event_priv::stop_event_read_lock`.

```

{
    spe_context_event_priv_ptr_t evctx;
    int rc;
    int fd;
    size_t total;

    evctx = __SPE_EVENT_CONTEXT_PRIV_GET(spe);
    fd = evctx->stop_event_pipe[0];

    pthread_mutex_lock(&evctx->stop_event_read_lock); /* for atomic read */

    rc = read(fd, stopinfo, sizeof(*stopinfo));
    if (rc == -1) {
        pthread_mutex_unlock(&evctx->stop_event_read_lock);
        return -1;
    }

    total = rc;
    while (total < sizeof(*stopinfo)) { /* this loop will be executed in few cases
        */
        struct pollfd fds;

```

```

    fds.fd = fd;
    fds.events = POLLIN;
    rc = poll(&fds, 1, -1);
    if (rc == -1) {
        if (errno != EINTR) {
            break;
        }
    }
    else if (rc == 1) {
        rc = read(fd, (char *)stopinfo + total, sizeof(*stopinfo) - total);
        if (rc == -1) {
            if (errno != EAGAIN) {
                break;
            }
        }
        else {
            total += rc;
        }
    }
}
pthread_mutex_unlock(&evctx->stop_event_read_lock);

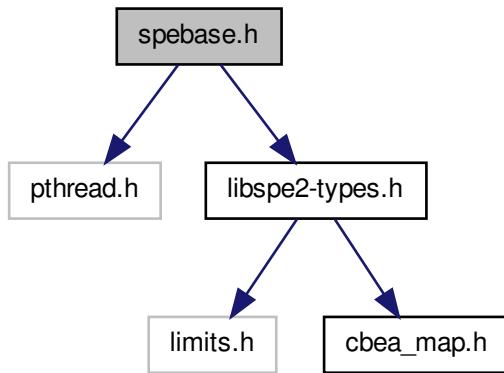
return rc == -1 ? -1 : 0;
}

```

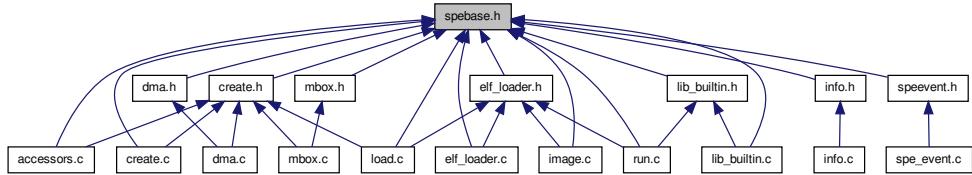
### 3.23 spebase.h File Reference

```
#include <pthread.h>
#include "libspe2-types.h"
```

Include dependency graph for spebase.h:



This graph shows which files directly or indirectly include this file:



## Data Structures

- struct `spe_context_base_priv`
- struct `spe_gang_context_base_priv`

## Defines

- #define `_PRINTF`(fmt, args...) { fprintf(stderr,fmt , ## args); }
- #define `DEBUG_PRINTF`(fmt, args...)
- #define `LS_SIZE` 0x40000
- #define `PSMAP_SIZE` 0x20000
- #define `MFC_SIZE` 0x1000
- #define `MSS_SIZE` 0x1000
- #define `CNTL_SIZE` 0x1000
- #define `SIGNAL_SIZE` 0x1000
- #define `MSSYNC_OFFSET` 0x000000
- #define `MFC_OFFSET` 0x03000
- #define `CNTL_OFFSET` 0x04000
- #define `SIGNAL1_OFFSET` 0x14000
- #define `SIGNAL2_OFFSET` 0x1c000
- #define `SPE_EMULATE_PARAM_BUFFER` 0x3e000
- #define `SPE_PROGRAM_NORMAL_END` 0x2000
- #define `SPE_PROGRAM_LIBRARY_CALL` 0x2100
- #define `SPE_PROGRAM_ISOLATED_STOP` 0x2200
- #define `SPE_PROGRAM_ISO_LOAD_COMPLETE` 0x2206

## Enumerations

- enum `fd_name` {
   
    `FD_MBOX`, `FD_MBOX_STAT`, `FD_IBOX`, `FD_IBOX_NB`,
   
    `FD_IBOX_STAT`, `FD_WBOX`, `FD_WBOX_NB`, `FD_WBOX_STAT`,
   
    `FD_SIG1`, `FD_SIG2`, `FD_MFC`, `FD_MSS`,
   
    `NUM_MBOX_FDS` }

## Functions

- `spe_context_ptr_t _base_spe_context_create` (unsigned int flags, `spe_gang_context_ptr_t` gctx, `spe_context_ptr_t` aff\_spe)
- `spe_gang_context_ptr_t _base_spe_gang_context_create` (unsigned int flags)
- `int _base_spe_program_load` (`spe_context_ptr_t` spectx, `spe_program_handle_t` \*program)
- `void _base_spe_program_load_complete` (`spe_context_ptr_t` spectx)
- `int _base_spe_emulated_loader_present` (void)
- `int _base_spe_context_destroy` (`spe_context_ptr_t` spectx)
- `int _base_spe_gang_context_destroy` (`spe_gang_context_ptr_t` gctx)
- `int _base_spe_context_run` (`spe_context_ptr_t` spe, unsigned int \*entry, unsigned int runflags, void \*argp, void \*envp, `spe_stop_info_t` \*stopinfo)
- `int _base_spe_image_close` (`spe_program_handle_t` \*handle)
- `spe_program_handle_t * _base_spe_image_open` (const char \*filename)
- `int _base_spe_mfcio_put` (`spe_context_ptr_t` spectx, unsigned int ls, void \*ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)
- `int _base_spe_mfcio_putb` (`spe_context_ptr_t` spectx, unsigned int ls, void \*ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)
- `int _base_spe_mfcio_puff` (`spe_context_ptr_t` spectx, unsigned int ls, void \*ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)
- `int _base_spe_mfcio_get` (`spe_context_ptr_t` spectx, unsigned int ls, void \*ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)
- `int _base_spe_mfcio_getb` (`spe_context_ptr_t` spectx, unsigned int ls, void \*ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)
- `int _base_spe_mfcio_getf` (`spe_context_ptr_t` spectx, unsigned int ls, void \*ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid)
- `int _base_spe_out_mbox_read` (`spe_context_ptr_t` spectx, unsigned int mbox\_data[ ], int count)
- `int _base_spe_in_mbox_write` (`spe_context_ptr_t` spectx, unsigned int mbox\_data[ ], int count, int behavior\_flag)
- `int _base_spe_in_mbox_status` (`spe_context_ptr_t` spectx)
- `int _base_spe_out_mbox_status` (`spe_context_ptr_t` spectx)
- `int _base_spe_out_intr_mbox_status` (`spe_context_ptr_t` spectx)
- `int _base_spe_out_intr_mbox_read` (`spe_context_ptr_t` spectx, unsigned int mbox\_data[ ], int count, int behavior\_flag)
- `int _base_spe_signal_write` (`spe_context_ptr_t` spectx, unsigned int signal\_reg, unsigned int data)
- `int _base_spe_callback_handler_register` (void \*handler, unsigned int callnum, unsigned int mode)
- `int _base_spe_callback_handler_deregister` (unsigned int callnum)
- `void * _base_spe_callback_handler_query` (unsigned int callnum)
- `int _base_spe_stop_reason_get` (`spe_context_ptr_t` spectx)
- `int _base_spe_mfcio_tag_status_read` (`spe_context_ptr_t` spectx, unsigned int mask, unsigned int behavior, unsigned int \*tag\_status)
- `int _base_spe_stop_event_source_get` (`spe_context_ptr_t` spectx)
- `int _base_spe_stop_event_target_get` (`spe_context_ptr_t` spectx)
- `int _base_spe_stop_status_get` (`spe_context_ptr_t` spectx)
- `int _base_spe_event_source_acquire` (struct `spe_context` \*spectx, enum `fd_name` fdesc)
- `void _base_spe_event_source_release` (struct `spe_context` \*spectx, enum `fd_name` fdesc)
- `void * _base_spe_ps_area_get` (struct `spe_context` \*spectx, enum `ps_area` area)
- `int _base_spe_spe_dir_get` (struct `spe_context` \*spectx)
- `void * _base_spe_ls_area_get` (struct `spe_context` \*spectx)
- `int _base_spe_ls_size_get` (`spe_context_ptr_t` spe)
- `void _base_spe_context_lock` (`spe_context_ptr_t` spe, enum `fd_name` fd)

- void `_base_spe_context_unlock` (`spe_context_ptr_t` spe, enum `fd_name` fd)
- int `_base_spe_cpu_info_get` (int info\_requested, int cpu\_node)
- void `__spe_context_update_event` (void)
- int `_base_spe_mssync_start` (`spe_context_ptr_t` spectx)
- int `_base_spe_mssync_status` (`spe_context_ptr_t` spectx)

### 3.23.1 Detailed Description

`spebase.h` contains the public API funtions

Definition in file `spebase.h`.

### 3.23.2 Define Documentation

#### 3.23.2.1 `#define __PRINTF( fmt, args... ) { fprintf(stderr,fmt ,## args); }`

Definition at line 34 of file `spebase.h`.

#### 3.23.2.2 `#define CNTL_OFFSET 0x04000`

Definition at line 124 of file `spebase.h`.

Referenced by `_base_spe_context_create()`.

#### 3.23.2.3 `#define CNTL_SIZE 0x1000`

Definition at line 119 of file `spebase.h`.

Referenced by `_base_spe_context_create()`.

#### 3.23.2.4 `#define DEBUG_PRINTF( fmt, args... )`

Definition at line 38 of file `spebase.h`.

#### 3.23.2.5 `#define LS_SIZE 0x40000`

Definition at line 115 of file `spebase.h`.

#### 3.23.2.6 `#define MFC_OFFSET 0x03000`

Definition at line 123 of file `spebase.h`.

Referenced by `_base_spe_context_create()`.

#### 3.23.2.7 `#define MFC_SIZE 0x1000`

Definition at line 117 of file `spebase.h`.

Referenced by `_base_spe_context_create()`.

**3.23.2.8 #define MSS\_SIZE 0x1000**

Definition at line 118 of file spebase.h.

Referenced by \_base\_spe\_context\_create().

**3.23.2.9 #define MSSYNC\_OFFSET 0x00000**

Definition at line 122 of file spebase.h.

Referenced by \_base\_spe\_context\_create().

**3.23.2.10 #define PSMAP\_SIZE 0x20000**

Definition at line 116 of file spebase.h.

Referenced by \_base\_spe\_context\_create().

**3.23.2.11 #define SIGNAL1\_OFFSET 0x14000**

Definition at line 125 of file spebase.h.

Referenced by \_base\_spe\_context\_create().

**3.23.2.12 #define SIGNAL2\_OFFSET 0x1c000**

Definition at line 126 of file spebase.h.

Referenced by \_base\_spe\_context\_create().

**3.23.2.13 #define SIGNAL\_SIZE 0x1000**

Definition at line 120 of file spebase.h.

Referenced by \_base\_spe\_context\_create().

**3.23.2.14 #define SPE\_EMULATE\_PARAM\_BUFFER 0x3e000**

Location of the PPE-assisted library call buffer for emulated isolation contexts.

Definition at line 132 of file spebase.h.

Referenced by \_base\_spe\_handle\_library\_callback().

**3.23.2.15 #define SPE\_PROGRAM\_ISO\_LOAD\_COMPLETE 0x2206**

Definition at line 143 of file spebase.h.

Referenced by \_base\_spe\_context\_run().

**3.23.2.16 #define SPE\_PROGRAM\_ISOLATED\_STOP 0x2200**

Isolated exit codes: 0x220x

Definition at line 142 of file spebase.h.

Referenced by \_base\_spe\_context\_run().

**3.23.2.17 #define SPE\_PROGRAM\_LIBRARY\_CALL 0x2100**

Definition at line 137 of file spebase.h.

Referenced by \_base\_spe\_context\_run().

**3.23.2.18 #define SPE\_PROGRAM\_NORMAL\_END 0x2000**

Definition at line 136 of file spebase.h.

Referenced by \_base\_spe\_context\_run().

### 3.23.3 Enumeration Type Documentation

**3.23.3.1 enum fd\_name**

NOTE: NUM\_MBOX\_FDS must always be the last element in the enumeration

**Enumerator:**

*FD\_MBOX*  
*FD\_MBOX\_STAT*  
*FD\_IBOX*  
*FD\_IBOX\_NB*  
*FD\_IBOX\_STAT*  
*FD\_WBOX*  
*FD\_WBOX\_NB*  
*FD\_WBOX\_STAT*  
*FD\_SIG1*  
*FD\_SIG2*  
*FD\_MFC*  
*FD\_MSS*  
*NUM\_MBOX\_FDS*

Definition at line 42 of file spebase.h.

```
{  
    FD_MBOX,  
    FD_MBOX_STAT,  
    FD_IBOX,  
    FD_IBOX_NB,  
    FD_IBOX_STAT,  
    FD_WBOX,
```

```

FD_WBOX_NB,
FD_WBOX_STAT,
FD_SIG1,
FD_SIG2,
FD_MFC,
FD_MSS,
NUM_MBOX_FDS
};
```

### 3.23.4 Function Documentation

#### 3.23.4.1 int \_\_base\_spe\_event\_source\_acquire ( struct spe\_context \* *spectx*, enum fd\_name *fdesc* )

`__base_spe_event_source_acquire` opens a file descriptor to the specified event source

##### Parameters

<i>spectx</i>	Specifies the SPE context
<i>fdesc</i>	Specifies the event source

#### 3.23.4.2 void \_\_base\_spe\_event\_source\_release ( struct spe\_context \* *spectx*, enum fd\_name *fdesc* )

`__base_spe_event_source_release` releases the file descriptor to the specified event source

##### Parameters

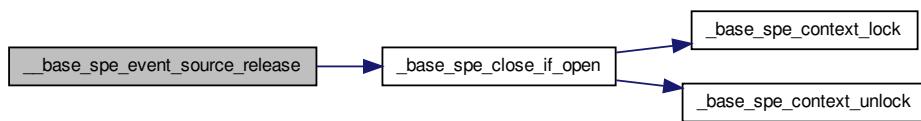
<i>spectx</i>	Specifies the SPE context
<i>fdesc</i>	Specifies the event source

Definition at line 79 of file accessors.c.

References `_base_spe_close_if_open()`.

```
{
    __base_spe_close_if_open(spe, fdesc);
}
```

Here is the call graph for this function:



#### 3.23.4.3 int \_\_base\_spe\_spe\_dir\_get ( struct spe\_context \* *spectx* )

`__base_spe_spe_dir_get` return the file descriptor of the SPE directory in spufs

**Parameters**

<i>spectx</i>	Specifies the SPE context
---------------	---------------------------

**3.23.4.4 int \_\_base\_spe\_stop\_event\_source\_get( spe\_context\_ptr\_t spe )**

*\_\_base\_spe\_stop\_event\_source\_get*

**Parameters**

<i>spectx</i>	Specifies the SPE context
---------------	---------------------------

speevent users read from this end

Definition at line 92 of file accessors.c.

References `spe_context::base_private`, and `spe_context_base_priv::ev_pipe`.

```
{  
    return spe->base_private->ev_pipe[1];  
}
```

**3.23.4.5 int \_\_base\_spe\_stop\_event\_target\_get( spe\_context\_ptr\_t spe )**

*\_\_base\_spe\_stop\_event\_target\_get*

**Parameters**

<i>spectx</i>	Specifies the SPE context
---------------	---------------------------

speevent writes to this end

Definition at line 100 of file accessors.c.

References `spe_context::base_private`, and `spe_context_base_priv::ev_pipe`.

```
{  
    return spe->base_private->ev_pipe[0];  
}
```

**3.23.4.6 void \_\_spe\_context\_update\_event( void )**

*\_\_spe\_context\_update\_event* internal function for gdb notification.

Referenced by `_base_spe_context_destroy()`, and `_base_spe_program_load_complete()`.

**3.23.4.7 int \_\_base\_spe\_callback\_handler\_deregister( unsigned int callnum )**

unregister a handler function for the specified number NOTE: unregistering a handler from call zero and one is ignored.

Definition at line 78 of file lib\_builtin.c.

References `MAX_CALLNUM`, and `RESERVED`.

```

{
    errno = 0;
    if (callnum > MAX_CALLNUM) {
        errno = EINVAL;
        return -1;
    }
    if (callnum < RESERVED) {
        errno = EACCES;
        return -1;
    }
    if (handlers[callnum] == NULL) {
        errno = ESRCH;
        return -1;
    }
    handlers[callnum] = NULL;
    return 0;
}

```

### 3.23.4.8 void\* \_base\_spe\_callback\_handler\_query ( unsigned int callnum )

query a handler function for the specified number

Definition at line 98 of file lib\_builtin.c.

References MAX\_CALLNUM.

```

{
    errno = 0;

    if (callnum > MAX_CALLNUM) {
        errno = EINVAL;
        return NULL;
    }
    if (handlers[callnum] == NULL) {
        errno = ESRCH;
        return NULL;
    }
    return handlers[callnum];
}

```

### 3.23.4.9 int \_base\_spe\_callback\_handler\_register ( void \* handler, unsigned int callnum, unsigned int mode )

register a handler function for the specified number NOTE: registering a handler to call zero and one is ignored.

Definition at line 40 of file lib\_builtin.c.

References MAX\_CALLNUM, RESERVED, SPE\_CALLBACK\_NEW, and SPE\_CALLBACK\_UPDATE.

```

{
    errno = 0;

    if (callnum > MAX_CALLNUM) {
        errno = EINVAL;
        return -1;
    }

    switch(mode) {
        case SPE_CALLBACK_NEW:

```

```

        if (callnum < RESERVED) {
            errno = EACCES;
            return -1;
        }
        if (handlers[callnum] != NULL) {
            errno = EACCES;
            return -1;
        }
        handlers[callnum] = handler;
        break;

    case SPE_CALLBACK_UPDATE:
        if (handlers[callnum] == NULL) {
            errno = ESRCH;
            return -1;
        }
        handlers[callnum] = handler;
        break;
    default:
        errno = EINVAL;
        return -1;
        break;
    }
    return 0;
}

```

### 3.23.4.10 `spe_context_ptr_t _base_spe_context_create ( unsigned int flags, spe_gang_context_ptr_t gctx, spe_context_ptr_t aff_spe )`

`_base_spe_context_create` creates a single SPE context, i.e., the corresponding directory is created in SPUFS either as a subdirectory of a gang or individually (maybe this is best considered a gang of one)

#### Parameters

<i>flags</i>	
<i>gctx</i>	specify NULL if not belonging to a gang
<i>aff_spe</i>	specify NULL to skip affinity information

Definition at line 183 of file create.c.

References `_base_spe_emulated_loader_present()`, `spe_gang_context::base_private`, `spe_context::base_private`, `spe_context_base_priv::cntl_mmap_base`, `CNTL_OFFSET`, `CNTL_SIZE`, `DEBUG_PRINTF`, `spe_context_base_priv::fd_lock`, `spe_context_base_priv::fd_spe_dir`, `spe_context_base_priv::flags`, `spe_gang_context_base_priv::gangname`, `spe_context_base_priv::loaded_program`, `LS_SIZE`, `spe_context_base_priv::mem_mmap_base`, `spe_context_base_priv::mfc_mmap_base`, `MFC_OFFSET`, `MFC_SIZE`, `MSS_SIZE`, `spe_context_base_priv::mssync_mmap_base`, `MSSYNC_OFFSET`, `NUM_MBOX_FDS`, `spe_context_base_priv::psmap_mmap_base`, `PSMAP_SIZE`, `spe_context_base_priv::signal1_mmap_base`, `SIGNAL1_OFFSET`, `spe_context_base_priv::signal2_mmap_base`, `SIGNAL2_OFFSET`, `SIGNAL_SIZE`, `SPE_AFFINITY_MEMORY`, `SPE_CFG_SIGNIFY1_OR`, `SPE_CFG_SIGNIFY2_OR`, `SPE_EVENTS_ENABLE`, `spe_context_base_priv::spe_fds_array`, `SPE_ISOLATE`, `SPE_ISOLATE_EMULATE`, and `SPE_MAP_PS`.

```

{
    char pathname[256];
    int i, aff_spe_fd = 0;
    unsigned int spu_createflags = 0;
    struct spe_context *spe = NULL;
    struct spe_context_base_priv *priv;

    /* We need a loader present to run in emulated isolated mode */

```

```

if (flags & SPE_ISOLATE_EMULATE
    && !_base_spe_emulated_loader_present()) {
    errno = EINVAL;
    return NULL;
}

/* Put some sane defaults into the SPE context */
spe = malloc(sizeof(*spe));
if (!spe) {
    DEBUG_PRINTF("ERROR: Could not allocate spe context.\n");
    return NULL;
}
memset(spe, 0, sizeof(*spe));

spe->base_private = malloc(sizeof(*spe->base_private));
if (!spe->base_private) {
    DEBUG_PRINTF("ERROR: Could not allocate "
                 "spe->base_private context.\n");
    free(spe);
    return NULL;
}

/* just a convenience variable */
priv = spe->base_private;

priv->fd_spe_dir = -1;
priv->mem_mmap_base = MAP_FAILED;
priv->psmmap_mmap_base = MAP_FAILED;
priv->mssync_mmap_base = MAP_FAILED;
priv->mfc_mmap_base = MAP_FAILED;
priv->cntl_mmap_base = MAP_FAILED;
priv->signall_mmap_base = MAP_FAILED;
priv->signal2_mmap_base = MAP_FAILED;
priv->loaded_program = NULL;

for (i = 0; i < NUM_MBOX_FDS; i++) {
    priv->spe_fds_array[i] = -1;
    pthread_mutex_init(&priv->fd_lock[i], NULL);
}

/* initialise spu_createflags */
if (flags & SPE_ISOLATE) {
    flags |= SPE_MAP_PS;
    spu_createflags |= SPU_CREATE_ISOLATE | SPU_CREATE_NOSCHED;
}

if (flags & SPE_EVENTS_ENABLE)
    spu_createflags |= SPU_CREATE_EVENTS_ENABLED;

if (aff_spe)
    spu_createflags |= SPU_CREATE_AFFINITY_SPU;

if (flags & SPE_AFFINITY_MEMORY)
    spu_createflags |= SPU_CREATE_AFFINITY_MEM;

/* Make the SPUFS directory for the SPE */
if (gctx == NULL)
    sprintf(pathname, "/spu/spethread-%i-%lu",
            getpid(), (unsigned long)spe);
else
    sprintf(pathname, "/spu/%s/spethread-%i-%lu",
            gctx->base_private->gangname, getpid(),
            (unsigned long)spe);

if (aff_spe)
    aff_spe_fd = aff_spe->base_private->fd_spe_dir;

```

```

priv->fd_spe_dir = spu_create(pathname, spu_createflags,
                               S_IRUSR | S_IWUSR | S_IXUSR, aff_spe_fd);

if (priv->fd_spe_dir < 0) {
    int errno_saved = errno; /* save errno to prevent being overwritten */
    DEBUG_PRINTF("ERROR: Could not create SPE %s\n", pathname);
    perror("spu_create()");
    free_spe_context(spe);
    /* we mask most errors, but leave ENODEV, etc */
    switch (errno_saved) {
        case ENOTSUP:
        case EEXIST:
        case EINVAL:
        case EBUSY:
        case EPERM:
        case ENODEV:
            errno = errno_saved; /* restore errno */
            break;
        default:
            errno = EFAULT;
            break;
    }
    return NULL;
}

priv->flags = flags;

/* Map the required areas into process memory */
priv->mem_mmap_base = mapfileat(priv->fd_spe_dir, "mem", LS_SIZE);
if (priv->mem_mmap_base == MAP_FAILED) {
    DEBUG_PRINTF("ERROR: Could not map SPE memory.\n");
    free_spe_context(spe);
    errno = ENOMEM;
    return NULL;
}

if (flags & SPE_MAP_PS) {
    /* It's possible to map the entire problem state area with
     * one mmap - try this first */
    priv->psmap_mmap_base = mapfileat(priv->fd_spe_dir,
                                       "psmap", PSMAP_SIZE);

    if (priv->psmap_mmap_base != MAP_FAILED) {
        priv->mssync_mmap_base =
            priv->psmap_mmap_base + MSSYNC_OFFSET;
        priv->mfc_mmap_base =
            priv->psmap_mmap_base + MFC_OFFSET;
        priv->cntl_mmap_base =
            priv->psmap_mmap_base + CNTL_OFFSET;
        priv->signal1_mmap_base =
            priv->psmap_mmap_base + SIGNAL1_OFFSET;
        priv->signal2_mmap_base =
            priv->psmap_mmap_base + SIGNAL2_OFFSET;
    } else {
        /* map each region separately */
        priv->mfc_mmap_base =
            mapfileat(priv->fd_spe_dir, "mfc", MFC_SIZE);
        priv->mssync_mmap_base =
            mapfileat(priv->fd_spe_dir, "mss", MSS_SIZE);
        priv->cntl_mmap_base =
            mapfileat(priv->fd_spe_dir, "cntl", CNTL_SIZE);
        priv->signal1_mmap_base =
            mapfileat(priv->fd_spe_dir, "signal1",
                      SIGNAL_SIZE);
        priv->signal2_mmap_base =

```

```

        mapfileat(priv->fd_spe_dir, "signal2",
                  SIGNAL_SIZE);

    if (priv->mfc_mmap_base == MAP_FAILED ||
        priv->cntl_mmap_base == MAP_FAILED ||
        priv->signal1_mmap_base == MAP_FAILED ||
        priv->signal2_mmap_base == MAP_FAILED) {
        DEBUG_PRINTF("ERROR: Could not map SPE "
                     "PS memory.\n");
        free_spe_context(spe);
        errno = ENOMEM;
        return NULL;
    }
}

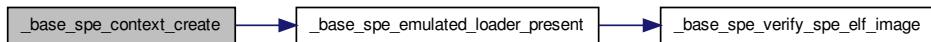
if (flags & SPE_CFG_SIGNIFY1_OR) {
    if (setsignify(priv->fd_spe_dir, "signal1_type")) {
        DEBUG_PRINTF("ERROR: Could not open SPE "
                     "signal1_type file.\n");
        free_spe_context(spe);
        errno = EFAULT;
        return NULL;
    }
}

if (flags & SPE_CFG_SIGNIFY2_OR) {
    if (setsignify(priv->fd_spe_dir, "signal2_type")) {
        DEBUG_PRINTF("ERROR: Could not open SPE "
                     "signal2_type file.\n");
        free_spe_context(spe);
        errno = EFAULT;
        return NULL;
    }
}

return spe;
}

```

Here is the call graph for this function:



### 3.23.4.11 int \_base\_spe\_context\_destroy ( spe\_context\_ptr\_t spectx )

\_base\_spe\_context\_destroy cleans up what is left when an SPE executable has exited. Closes open file handles and unmaps memory areas.

#### Parameters

<i>spectx</i>	Specifies the SPE context
---------------	---------------------------

Definition at line 418 of file create.c.

References \_\_spe\_context\_update\_event().

```
{
    int ret = free_spe_context(spe);

    __spe_context_update_event();

    return ret;
}
```

Here is the call graph for this function:



#### 3.23.4.12 void \_base\_spe\_context\_lock ( spe\_context\_ptr\_t spe, enum fd\_name fd )

\_base\_spe\_context\_lock locks members of the SPE context

##### Parameters

<i>spectx</i>	Specifies the SPE context
<i>fd</i>	Specifies the file

Definition at line 91 of file create.c.

References spe\_context::base\_private, and spe\_context\_base\_priv::fd\_lock.

Referenced by \_base\_spe\_close\_if\_open(), and \_base\_spe\_open\_if\_closed().

```
{
    pthread_mutex_lock(&spe->base_private->fd_lock[fdesc]);
}
```

#### 3.23.4.13 int \_base\_spe\_context\_run ( spe\_context\_ptr\_t spe, unsigned int \* entry, unsigned int runflags, void \* argp, void \* envp, spe\_stop\_info\_t \* stopinfo )

\_base\_spe\_context\_run starts execution of an SPE context with a loaded image

##### Parameters

<i>spectx</i>	Specifies the SPE context
<i>entry</i>	entry point for the SPE programm. If set to 0, entry point is determined by the ELF loader.
<i>runflags</i>	valid values are: SPE_RUN_USER_REGS Specifies that the SPE setup registers r3, r4, and r5 are initialized with the 48 bytes pointed to by argp. SPE_NO_CALLBACKS do not use built in library functions.

<i>argp</i>	An (optional) pointer to application specific data, and is passed as the second parameter to the SPE program.
<i>envp</i>	An (optional) pointer to environment specific data, and is passed as the third parameter to the SPE program.

Definition at line 99 of file run.c.

References \_\_spe\_current\_active\_context, \_base\_spe\_handle\_library\_callback(), \_base\_spe\_program\_load\_complete(), spe\_context::base\_private, DEBUG\_PRINTF, spe\_context\_base\_priv::emulated\_entry, spe\_context\_base\_priv::entry, spe\_context\_base\_priv::fd\_spe\_dir, spe\_context\_base\_priv::flags, LS\_SIZE, spe\_context\_base\_priv::mem\_mmap\_base, spe\_context\_info::npc, spe\_context\_info::prev, spe\_stop\_info::result, spe\_stop\_info::spe\_callback\_error, SPE\_CALLBACK\_ERROR, SPE\_DEFAULT\_ENTRY, SPE\_EVENTS\_ENABLE, SPE\_EXIT, spe\_stop\_info::spe\_exit\_code, spe\_context\_info::spe\_id, SPE\_ISOLATE, SPE\_ISOLATE\_EMULATE, spe\_stop\_info::spe\_isolation\_error, SPE\_ISOLATION\_ERROR, SPE\_NO\_CALLBACKS, SPE\_PROGRAM\_ISO\_LOAD\_COMPLETE, SPE\_PROGRAM\_ISOLATED\_STOP, SPE\_PROGRAM\_LIBRARY\_CALL, SPE\_PROGRAM\_NORMAL\_END, SPE\_RUN\_USER\_REGS, spe\_stop\_info::spe\_runtime\_error, SPE\_RUNTIME\_ERROR, spe\_stop\_info::spe\_runtime\_exception, SPE\_RUNTIME\_EXCEPTION, spe\_stop\_info::spe\_runtime\_fatal, SPE\_RUNTIME\_FATAL, spe\_stop\_info::spe\_signal\_code, SPE\_SPU\_HALT, SPE\_SPU\_INVALID\_CHANNEL, SPE\_SPU\_INVALID\_INSTR, SPE\_SPU\_STOPPED\_BY\_STOP, SPE\_SPU\_WAITING\_ON\_CHANNEL, SPE\_STOP\_AND\_SIGNAL, spe\_stop\_info::spu\_status, spe\_context\_info::status, spe\_stop\_info::stop\_reason, addr64::ui, and addr64::ull.

Referenced by \_event\_spe\_context\_run().

```
{
    int retval = 0, run_rc;
    unsigned int run_status, tmp_entry;
    spe_stop_info_t stopinfo_buf;
    struct spe_context_info this_context_info __attribute__((cleanup(cleanups
    peinfo)));
    /* If the caller hasn't set a stopinfo buffer, provide a buffer on the
     * stack instead. */
    if (!stopinfo)
        stopinfo = &stopinfo_buf;
    /* In emulated isolated mode, the npc will always return as zero.
     * use our private entry point instead */
    if (spe->base_private->flags & SPE_ISOLATE_EMULATE)
        tmp_entry = spe->base_private->emulated_entry;
    else if (*entry == SPE_DEFAULT_ENTRY)
        tmp_entry = spe->base_private->entry;
    else
        tmp_entry = *entry;
    /* If we're starting the SPE binary from its original entry point,
     * setup the arguments to main() */
    if (tmp_entry == spe->base_private->entry &&
        !(spe->base_private->flags &
        (SPE_ISOLATE | SPE_ISOLATE_EMULATE))) {
        addr64 argp64, envp64, tid64, ls64;
        unsigned int regs[128][4];
        /* setup parameters */
        argp64.ull = (uint64_t)(unsigned long)argp;
        envp64.ull = (uint64_t)(unsigned long)envp;
        tid64.ull = (uint64_t)(unsigned long)spe;
    }
}
```

```

/* make sure the register values are 0 */
memset(regs, 0, sizeof(regs));

/* set sensible values for stack_ptr and stack_size */
regs[1][0] = (unsigned int) LS_SIZE - 16;           /* stack_ptr */
regs[2][0] = 0;
/* stack_size ( 0 = default ) */

if (runflags & SPE_RUN_USER_REGS) {
    /* When SPE_USER_REGS is set, argp points to an array
     * of 3x128b registers to be passed directly to the SPE
     * program.
     */
    memcpy(regs[3], argp, sizeof(unsigned int) * 12);
} else {
    regs[3][0] = tid64.ui[0];
    regs[3][1] = tid64.ui[1];

    regs[4][0] = argp64.ui[0];
    regs[4][1] = argp64.ui[1];

    regs[5][0] = envp64.ui[0];
    regs[5][1] = envp64.ui[1];
}

/* Store the LS base address in R6 */
ls64.ull = (uint64_t)(unsigned long)spe->base_private->
mem_mmap_base;
regs[6][0] = ls64.ui[0];
regs[6][1] = ls64.ui[1];

if (set_regs(spe, regs))
    return -1;
}

/*Leave a trail of breadcrumbs for the debugger to follow */
if (!__spe_current_active_context) {
    __spe_current_active_context = &this_context_info;
    if (!__spe_current_active_context)
        return -1;
    __spe_current_active_context->prev = NULL;
} else {
    struct spe_context_info *newinfo;
    newinfo = &this_context_info;
    if (!newinfo)
        return -1;
    newinfo->prev = __spe_current_active_context;
    __spe_current_active_context = newinfo;
}
/*remember the ls-addr*/
__spe_current_active_context->spe_id = spe->base_private->fd_spe_dir;

do_run:
/*Remember the npc value*/
__spe_current_active_context->npc = tmp_entry;

/* run SPE context */
run_rc = spu_run(spe->base_private->fd_spe_dir,
                 &tmp_entry, &run_status);

/*Remember the npc value*/
__spe_current_active_context->npc = tmp_entry;
__spe_current_active_context->status = run_status;

DEBUG_PRINTF("spu_run returned run_rc=0x%08x, entry=0x%04x, "
             "ext_status=0x%04x.\n", run_rc, tmp_entry, run_status);

```

```

/* set up return values and stopinfo according to spu_run exit
 * conditions. This is overwritten on error.
 */
stopinfo->spu_status = run_rc;

if (spe->base_private->flags & SPE_ISOLATE_EMULATE) {
    /* save the entry point, and pretend that the npc is zero */
    spe->base_private->emulated_entry = tmp_entry;
    *entry = 0;
} else {
    *entry = tmp_entry;
}

/* Return with stopinfo set on syscall error paths */
if (run_rc == -1) {
    DEBUG_PRINTF("spu_run returned error %d, errno=%d\n",
                 run_rc, errno);
    stopinfo->stop_reason = SPE_RUNTIME_FATAL;
    stopinfo->result.spe_runtime_fatal = errno;
    retval = -1;

    /* For isolated contexts, pass EPERM up to the
     * caller.
     */
    if (!(spe->base_private->flags & SPE_ISOLATE
          && errno == EPERM))
        errno = EFAULT;
}

} else if (run_rc & SPE_SPU_INVALID_INSTR) {
    DEBUG_PRINTF("SPU has tried to execute an invalid "
                 "instruction. %d\n", run_rc);
    stopinfo->stop_reason = SPE_RUNTIME_ERROR;
    stopinfo->result.spe_runtime_error = SPE_SPU_INVALID_INSTR;
    errno = EFAULT;
    retval = -1;

} else if ((spe->base_private->flags & SPE_EVENTS_ENABLE) && run_status)
{
    /* Report asynchronous error if return val are set and
     * SPU events are enabled.
     */
    stopinfo->stop_reason = SPE_RUNTIME_EXCEPTION;
    stopinfo->result.spe_runtime_exception = run_status;
    stopinfo->spu_status = -1;
    errno = EIO;
    retval = -1;

} else if (run_rc & SPE_SPU_STOPPED_BY_STOP) {
    /* Stop & signals are broken down into three groups
     * 1. SPE library call
     * 2. SPE user defined stop & signal
     * 3. SPE program end.
     *
     * These groups are signified by the 14-bit stop code:
     */
    int stopcode = (run_rc >> 16) & 0xffff;

    /* Check if this is a library callback, and callbacks are
     * allowed (ie, running without SPE_NO_CALLBACKS)
     */
    if ((stopcode & 0xff00) == SPE_PROGRAM_LIBRARY_CALL
        && !(runflags & SPE_NO_CALLBACKS)) {

        int callback_rc, callback_number = stopcode & 0xff;

        /* execute library callback */
        DEBUG_PRINTF("SPE library call: %d\n", callback_number);
}

```

```

        callback_rc = _base_spe_handle_library_callback(spe,
                                                       callback_
                                                       number, *entry);

        if (callback_rc) {
            /* library callback failed; set errno and
             * return immediately */
            DEBUG_PRINTF("SPE library call failed: %d\n",
                         callback_rc);
            stopinfo->stop_reason = SPE_CALLBACK_ERROR;
            stopinfo->result.spe_callback_error =
                callback_rc;
            errno = EFAULT;
            retval = -1;
        } else {
            /* successful library callback - restart the SPE
             * program at the next instruction */
            tmp_entry += 4;
            goto do_run;
        }

    } else if ((stopcode & 0xff00) == SPE_PROGRAM_NORMAL_END) {
        /* The SPE program has exited by exit(X) */
        stopinfo->stop_reason = SPE_EXIT;
        stopinfo->result.spe_exit_code = stopcode & 0xff;

        if (spe->base_private->flags & SPE_ISOLATE) {
            /* Issue an isolated exit, and re-run the SPE.
             * We should see a return value without the
             * 0x80 bit set. */
            if (!issue_isolated_exit(spe))
                goto do_run;
            retval = -1;
        }
    }

} else if ((stopcode & 0xffff0) == SPE_PROGRAM_ISOLATED_STOP) {

    /* 0x2206: isolated app has been loaded by loader;
     * provide a hook for the debugger to catch this,
     * and restart
     */
    if (stopcode == SPE_PROGRAM_ISO_LOAD_COMPLETE) {
        _base_spe_program_load_complete(spe);
        goto do_run;
    } else {
        stopinfo->stop_reason = SPE_ISOLATION_ERROR;
        stopinfo->result.spe_isolation_error =
            stopcode & 0xf;
    }

} else if (spe->base_private->flags & SPE_ISOLATE &&
           !(run_rc & 0x80)) {
    /* We've successfully exited isolated mode */
    retval = 0;

} else {
    /* User defined stop & signal, including
     * callbacks when disabled */
    stopinfo->stop_reason = SPE_STOP_AND_SIGNAL;
    stopinfo->result.spe_signal_code = stopcode;
    retval = stopcode;
}

} else if (run_rc & SPE_SPU_HALT) {
    DEBUG_PRINTF("SPU was stopped by halt. %d\n", run_rc);
    stopinfo->stop_reason = SPE_RUNTIME_ERROR;
    stopinfo->result.spe_runtime_error = SPE_SPU_HALT;
}

```

```

        errno = EFAULT;
        retval = -1;

    } else if (run_rc & SPE_SPU_WAITING_ON_CHANNEL) {
        DEBUG_PRINTF("SPU is waiting on channel. %d\n", run_rc);
        stopinfo->stop_reason = SPE_RUNTIME_EXCEPTION;
        stopinfo->result.spe_runtime_exception = run_status;
        stopinfo->spu_status = -1;
        errno = EIO;
        retval = -1;

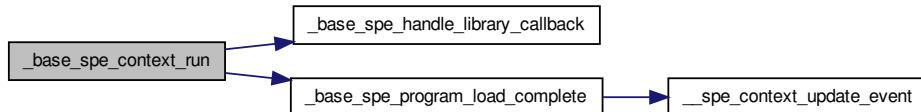
    } else if (run_rc & SPE_SPU_INVALID_CHANNEL) {
        DEBUG_PRINTF("SPU has tried to access an invalid "
                     "channel. %d\n", run_rc);
        stopinfo->stop_reason = SPE_RUNTIME_ERROR;
        stopinfo->result.spe_runtime_error = SPE_SPU_INVALID_CHANNEL;
        errno = EFAULT;
        retval = -1;

    } else {
        DEBUG_PRINTF("spu_run returned invalid data: 0x%04x\n", run_rc);
        stopinfo->stop_reason = SPE_RUNTIME_FATAL;
        stopinfo->result.spe_runtime_fatal = -1;
        stopinfo->spu_status = -1;
        errno = EFAULT;
        retval = -1;
    }

    freespeinfo();
    return retval;
}

```

Here is the call graph for this function:



### 3.23.4.14 void \_base\_spe\_context\_unlock( spe\_context\_ptr\_t spe, enum fd\_name fd )

\_base\_spe\_context\_unlock unlocks members of the SPE context

#### Parameters

<i>spectx</i>	Specifies the SPE context
<i>fd</i>	Specifies the file

Definition at line 96 of file create.c.

References spe\_context::base\_private, and spe\_context\_base\_priv::fd\_lock.

Referenced by \_base\_spe\_close\_if\_open(), and \_base\_spe\_open\_if\_closed().

```
{
    pthread_mutex_unlock(&spe->base_private->fd_lock[fdesc]);
}
```

#### 3.23.4.15 int \_base\_spe\_cpu\_info\_get( int info\_requested, int cpu\_node )

\_base\_spe\_info\_get

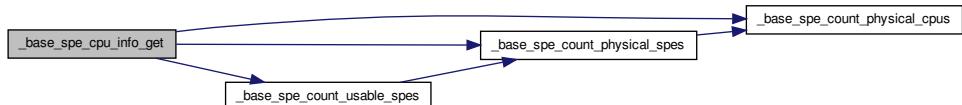
Definition at line 105 of file info.c.

References \_base\_spe\_count\_physical\_cpus(), \_base\_spe\_count\_physical\_spes(), \_base\_spe\_count\_usable\_spes(), SPE\_COUNT\_PHYSICAL\_CPU\_NODES, SPE\_COUNT\_PHYSICAL\_SPES, and SPE\_COUNT\_USABLE\_SPES.

```
{
    int ret = 0;
    errno = 0;

    switch (info_requested) {
        case SPE_COUNT_PHYSICAL_CPU_NODES:
            ret = _base_spe_count_physical_cpus(cpu_node);
            break;
        case SPE_COUNT_PHYSICAL_SPES:
            ret = _base_spe_count_physical_spes(cpu_node);
            break;
        case SPE_COUNT_USABLE_SPES:
            ret = _base_spe_count_usable_spes(cpu_node);
            break;
        default:
            errno = EINVAL;
            ret = -1;
    }
    return ret;
}
```

Here is the call graph for this function:



#### 3.23.4.16 int \_base\_spe\_emulated\_loader\_present( void )

Check if the emulated loader is present in the filesystem

#### Returns

Non-zero if the loader is available, otherwise zero.

Definition at line 159 of file load.c.

References \_base\_spe\_verify\_spe\_elf\_image().

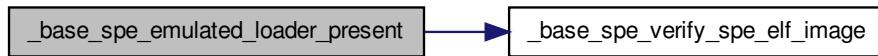
Referenced by \_base\_spe\_context\_create().

```
{
    spe_program_handle_t *loader = emulated_loader_program();

    if (!loader)
        return 0;

    return !_base_spe_verify_spe_elf_image(loader);
}
```

Here is the call graph for this function:



### 3.23.4.17 spe\_gang\_context\_ptr\_t \_base\_spe\_gang\_context\_create( unsigned int flags )

creates the directory in SPUFS that will contain all SPEs that are considered a gang Note: I would like to generalize this to a "group" or "set" Additional attributes maintained at the group level should be used to define scheduling constraints such "temporal" (e.g., scheduled all at the same time, i.e., a gang) "topology" (e.g., "closeness" of SPEs for optimal communication)

Definition at line 376 of file create.c.

References spe\_gang\_context::base\_private, DEBUG\_PRINTF, and spe\_gang\_context\_base\_priv::gangname.

```
{
    char pathname[256];
    struct spe_gang_context_base_priv *pgctx = NULL;
    struct spe_gang_context *gctx = NULL;

    gctx = malloc(sizeof(*gctx));
    if (!gctx) {
        DEBUG_PRINTF("ERROR: Could not allocate spe context.\n");
        return NULL;
    }
    memset(gctx, 0, sizeof(*gctx));

    pgctx = malloc(sizeof(*pgctx));
    if (!pgctx) {
        DEBUG_PRINTF("ERROR: Could not allocate spe context.\n");
        free(gctx);
        return NULL;
    }
    memset(pgctx, 0, sizeof(*pgctx));

    gctx->base_private = pgctx;

    sprintf(gctx->base_private->gangname, "gang-%i-%lu", getpid(),
            (unsigned long)gctx);
    sprintf(pathname, "/spu/%s", gctx->base_private->gangname);

    gctx->base_private->fd_gang_dir = spu_create(pathname, SPU_CREATE GANG,
                                                S_IRUSR | S_IWUSR | S_IXUSR);
```

```

        if (gctx->base_private->fd_gang_dir < 0) {
            DEBUG_PRINTF("ERROR: Could not create Gang %s\n", pathname);
            free_spe_gang_context(gctx);
            errno = EFAULT;
            return NULL;
        }

        gctx->base_private->flags = flags;

        return gctx;
    }
}

```

**3.23.4.18 int \_base\_spe\_gang\_context\_destroy ( spe\_gang\_context\_ptr\_t gctx )**

\_base\_spe\_gang\_context\_destroy destroys a gang context and frees associated resources

**Parameters**

<i>gctx</i>	Specifies the SPE gang context
-------------	--------------------------------

Definition at line 427 of file create.c.

```
{
    return free_spe_gang_context(gctx);
}
```

**3.23.4.19 int \_base\_spe\_image\_close ( spe\_program\_handle\_t \* handle )**

\_base\_spe\_image\_close unmaps an SPE ELF object that was previously mapped using spe\_open\_image.

**Parameters**

<i>handle</i>	handle to open file
---------------	---------------------

**Return values**

<i>0</i>	On success, spe_close_image returns 0.
<i>-1</i>	On failure, -1 is returned and errno is set appropriately. Possible values for errno: EINVAL From spe_close_image, this indicates that the file, specified by filename, was not previously mapped by a call to spe_open_image.

Definition at line 96 of file image.c.

References spe\_program\_handle::elf\_image, image\_handle::map\_size, image\_handle::speh, and spe\_program\_handle::toe\_shadow.

```
{
    int ret = 0;
    struct image_handle *ih;

    if (!handle) {
        errno = EINVAL;
        return -1;
    }
}
```

```

ih = (struct image_handle *)handle;

if (!ih->speh.elf_image || !ih->map_size) {
    errno = EINVAL;
    return -1;
}

if (ih->speh.toe_shadow)
    free(ih->speh.toe_shadow);

ret = munmap(ih->speh.elf_image, ih->map_size );
free(handle);

return ret;
}

```

### 3.23.4.20 spe\_program\_handle\_t\* \_base\_spe\_image\_open ( const char \* *filename* )

\_base\_spe\_image\_open maps an SPE ELF executable indicated by filename into system memory and returns the mapped address appropriate for use by the spe\_create\_thread API. It is often more convenient/appropriate to use the loading methodologies where SPE ELF objects are converted to PPE static or shared libraries with symbols which point to the SPE ELF objects after these special libraries are loaded. These libraries are then linked with the associated PPE code to provide a direct symbol reference to the SPE ELF object. The symbols in this scheme are equivalent to the address returned from the spe\_open\_image function. SPE ELF objects loaded using this function are not shared with other processes, but SPE ELF objects loaded using the other scheme, mentioned above, can be shared if so desired.

#### Parameters

<i>filename</i>	Specifies the filename of an SPE ELF executable to be loaded and mapped into system memory.
-----------------	---

#### Returns

On success, spe\_open\_image returns the address at which the specified SPE ELF object has been mapped. On failure, NULL is returned and errno is set appropriately.

Possible values for errno include:

EACCES The calling process does not have permission to access the specified file.

EFAULT The filename parameter points to an address that was not contained in the calling process's address space.

A number of other errno values could be returned by the open(2), fstat(2), mmap(2), munmap(2), or close(2) system calls which may be utilized by the spe\_open\_image or spe\_close\_image functions.

#### See also

[spe\\_create\\_thread](#)

Definition at line 37 of file image.c.

References [\\_base\\_spe\\_toe\\_ear\(\)](#), [\\_base\\_spe\\_verify\\_spe\\_elf\\_image\(\)](#), [spe\\_program\\_handle::elf\\_image](#), [spe\\_program\\_handle::handle\\_size](#), [image\\_handle::map\\_size](#), [image\\_handle::speh](#), and [spe\\_program\\_handle::toe\\_shadow](#).

```
{
    /* allocate an extra integer in the spe handle to keep the mapped size in
formation */
```

```

    struct image_handle *ret;
    int binfid = -1, f_stat;
    struct stat statbuf;
    size_t ps = getpagesize ();

    ret = malloc(sizeof(struct image_handle));
    if (!ret)
        return NULL;

    ret->speh.handle_size = sizeof(spe_program_handle_t);
    ret->speh.toe_shadow = NULL;

    binfid = open(filename, O_RDONLY);
    if (binfid < 0)
        goto ret_err;

    f_stat = fstat(binfid, &statbuf);
    if (f_stat < 0)
        goto ret_err;

    /* Sanity: is it executable ?
     */
    if (!(statbuf.st_mode & (S_IXUSR | S_IXGRP | S_IXOTH))) {
        errno=EACCES;
        goto ret_err;
    }

    /* now store the size at the extra allocated space */
    ret->map_size = (statbuf.st_size + ps - 1) & ~(ps - 1);

    ret->speh.elf_image = mmap(NULL, ret->map_size,
                               PROT_WRITE | PROT_READ,
                               MAP_PRIVATE, binfid, 0);
    if (ret->speh.elf_image == MAP_FAILED)
        goto ret_err;

    /*Verify that this is a valid SPE ELF object*/
    if (_base_spe_verify_spe_elf_image((spe_program_handle_t *)ret))
        goto ret_err;

    if (_base_spe_toe_ear(&ret->speh))
        goto ret_err;

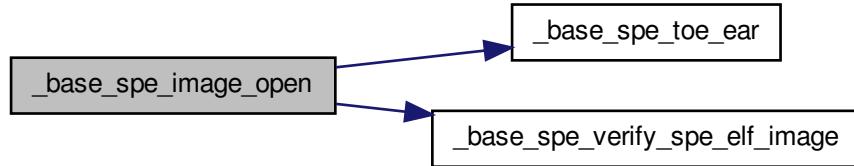
    /* ok */
    close(binfid);
    return (spe_program_handle_t *)ret;

    /* err & cleanup */
ret_err:
    if (binfid >= 0)
        close(binfid);

    free(ret);
    return NULL;
}

```

Here is the call graph for this function:



### 3.23.4.21 int \_base\_spe\_in\_mbox\_status ( spe\_context\_ptr\_t spectx )

The `_base_spe_in_mbox_status` function fetches the status of the SPU inbound mailbox for the SPE thread specified by the `speid` parameter. A 0 value is return if the mailbox is full. A non-zero value specifies the number of available (32-bit) mailbox entries.

#### Parameters

<code>spectx</code>	Specifies the SPE context whose mailbox status is to be read.
---------------------	---

#### Returns

On success, returns the current status of the mailbox, respectively. On failure, -1 is returned.

#### See also

`spe_read_out_mbox`, `spe_write_in_mbox`, `read` (2)

Definition at line 202 of file mbox.c.

References `_base_spe_open_if_closed()`, `spe_context::base_private`, `spe_context_base_priv::cntl_mmap_base`, `FD_WBOX_STAT`, `spe_context_base_priv::flags`, `SPE_MAP_PS`, and `spe_spu_control_area::SPU_Mbox_Stat`.

```

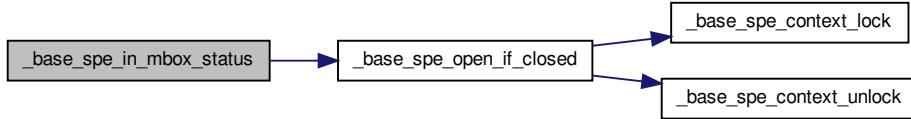
{
    int rc, ret;
    volatile struct spe_spu_control_area *cntl_area =
        spectx->base_private->cntl_mmap_base;

    if (spectx->base_private->flags & SPE_MAP_PS) {
        ret = (cntl_area->SPU_Mbox_Stat >> 8) & 0xFF;
    } else {
        rc = read(_base_spe_open_if_closed(spectx, FD_WBOX_STAT, 0), &ret,
4);
        if (rc != 4)
            ret = -1;
    }

    return ret;
}

```

Here is the call graph for this function:



#### 3.23.4.22 int \_base\_spe\_in\_mbox\_write ( spe\_context\_ptr\_t spectx, unsigned int mbox\_data[], int count, int behavior\_flag )

The `_base_spe_in_mbox_write` function writes `mbox_data` to the SPE inbound mailbox for the SPE thread `speid`.

If the behavior flag indicates `ALL_BLOCKING` the call will try to write exactly `count` mailbox entries and block until the write request is satisfied, i.e., exactly `count` mailbox entries have been written. If the behavior flag indicates `ANY_BLOCKING` the call will try to write up to `count` mailbox entries, and block until the write request is satisfied, i.e., at least 1 mailbox entry has been written. If the behavior flag indicates `ANY_NON_BLOCKING` the call will not block until the write request is satisfied, but instead write whatever is immediately possible and return the number of mailbox entries written. `spe_stat_in_mbox` can be called to ensure that data can be written prior to calling the function.

#### Parameters

<code>spectx</code>	Specifies the SPE thread whose outbound mailbox is to be read.
<code>mbox_data</code>	
<code>count</code>	
<code>behavior_flag</code>	<code>ALL_BLOCKING</code> <code>ANY_BLOCKING</code> <code>ANY_NON_BLOCKING</code>

#### Return values

$\geq 0$	the number of 32-bit mailbox messages written
$-1$	error condition and <code>errno</code> is set Possible values for <code>errno</code> : <code>EINVAL</code> <code>spectx</code> is invalid <code>Exxxx</code> what else do we need here??

#### 3.23.4.23 void\* \_base\_spe\_ls\_area\_get ( struct spe\_context \* spectx )

`_base_spe_ls_area_get` returns a pointer to the start of the memory mapped local store area

#### Parameters

<code>spectx</code>	Specifies the SPE context
---------------------	---------------------------

### 3.23.4.24 int \_base\_spe\_ls\_size\_get ( spe\_context\_ptr\_t spe )

\_base\_spe\_ls\_size\_get returns the size of the local store area

#### Parameters

<i>spectx</i>	Specifies the SPE context
---------------	---------------------------

Definition at line 105 of file accessors.c.

References LS\_SIZE.

```
{
    return LS_SIZE;
}
```

### 3.23.4.25 int \_base\_spe\_mfcio\_get ( spe\_context\_ptr\_t spectx, unsigned int ls, void \* ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid )

The \_base\_spe\_mfcio\_get function places a get DMA command on the proxy command queue of the SPE thread specified by speid. The get command transfers size bytes of data starting at the effective address specified by ea to the local store address specified by ls. The DMA is identified by the tag id specified by tag and performed according to the transfer class and replacement class specified by tid and rid respectively.

#### Parameters

<i>spectx</i>	Specifies the SPE context
<i>ls</i>	Specifies the starting local store destination address.
<i>ea</i>	Specifies the starting effective address source address.
<i>size</i>	Specifies the size, in bytes, to be transferred.
<i>tag</i>	Specifies the tag id used to identify the DMA command.
<i>tid</i>	Specifies the transfer class identifier of the DMA command.
<i>rid</i>	Specifies the replacement class identifier of the DMA command.

#### Returns

On success, return 0. On failure, -1 is returned.

Definition at line 160 of file dma.c.

References MFC\_CMD\_GET.

```
{
    return spe_do_mfc_get(spectx, ls, ea, size, tag, tid, rid, MFC_CMD_GET);
}
```

### 3.23.4.26 int \_base\_spe\_mfcio\_getb ( spe\_context\_ptr\_t spectx, unsigned int ls, void \* ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid )

The \_base\_spe\_mfcio\_getb function is identical to \_base\_spe\_mfcio\_get except that it places a getb (get with barrier) DMA command on the proxy command queue. The barrier form ensures that this command and all sequence commands with the same tag identifier as this command are locally ordered with respect to all previously issued commands with the same tag group and command queue.

**Parameters**

<i>spectx</i>	Specifies the SPE context
<i>ls</i>	Specifies the starting local store destination address.
<i>ea</i>	Specifies the starting effective address source address.
<i>size</i>	Specifies the size, in bytes, to be transferred.
<i>tag</i>	Specifies the tag id used to identify the DMA command.
<i>tid</i>	Specifies the transfer class identifier of the DMA command.
<i>rid</i>	Specifies the replacement class identifier of the DMA command.

**Returns**

On success, return 0. On failure, -1 is returned.

Definition at line 171 of file dma.c.

References MFC\_CMD\_GETB.

```
{
    return spe_do_mfc_get(spectx, ls, ea, size, tag, rid, MFC_CMD_GETB);
}
```

### 3.23.4.27 int \_base\_spe\_mfcio\_getf ( spe\_context\_ptr\_t spectx, unsigned int ls, void \* ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid )

The \_base\_spe\_mfcio\_getf function is identical to \_base\_spe\_mfcio\_get except that it places a getf (get with fence) DMA command on the proxy command queue. The fence form ensure that this command is locally ordered with respect to all previously issued commands with the same tag group and command queue.

**Parameters**

<i>spectx</i>	Specifies the SPE context
<i>ls</i>	Specifies the starting local store destination address.
<i>ea</i>	Specifies the starting effective address source address.
<i>size</i>	Specifies the size, in bytes, to be transferred.
<i>tag</i>	Specifies the tag id used to identify the DMA command.
<i>tid</i>	Specifies the transfer class identifier of the DMA command.
<i>rid</i>	Specifies the replacement class identifier of the DMA command.

**Returns**

On success, return 0. On failure, -1 is returned.

Definition at line 182 of file dma.c.

References MFC\_CMD\_GETF.

```
{
    return spe_do_mfc_get(spectx, ls, ea, size, tag, tid, rid, MFC_CMD_GETF);
}
```

---

**3.23.4.28 int \_base\_spe\_mfcio\_put( spe\_context\_ptr\_t spectx, unsigned int ls, void \* ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid )**

The \_base\_spe\_mfcio\_put function places a put DMA command on the proxy command queue of the SPE thread specified by speid. The put command transfers size bytes of data starting at the local store address specified by ls to the effective address specified by ea. The DMA is identified by the tag id specified by tag and performed according transfer class and replacement class specified by tid and rid respectively.

**Parameters**

<i>spectx</i>	Specifies the SPE context
<i>ls</i>	Specifies the starting local store destination address.
<i>ea</i>	Specifies the starting effective address source address.
<i>size</i>	Specifies the size, in bytes, to be transferred.
<i>tag</i>	Specifies the tag id used to identify the DMA command.
<i>tid</i>	Specifies the transfer class identifier of the DMA command.
<i>rid</i>	Specifies the replacement class identifier of the DMA command.

**Returns**

On success, return 0. On failure, -1 is returned.

Definition at line 126 of file dma.c.

References MFC\_CMD\_PUT.

```
{
    return spe_do_mfc_put(spectx, ls, ea, size, tag, tid, rid, MFC_CMD_PUT);
}
```

---

**3.23.4.29 int \_base\_spe\_mfcio\_putb( spe\_context\_ptr\_t spectx, unsigned int ls, void \* ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid )**

The \_base\_spe\_mfcio\_putb function is identical to \_base\_spe\_mfcio\_put except that it places a putb (put with barrier) DMA command on the proxy command queue. The barrier form ensures that this command and all sequence commands with the same tag identifier as this command are locally ordered with respect to all previously issued commands with the same tag group and command queue.

**Parameters**

<i>spectx</i>	Specifies the SPE context
<i>ls</i>	Specifies the starting local store destination address.
<i>ea</i>	Specifies the starting effective address source address.
<i>size</i>	Specifies the size, in bytes, to be transferred.
<i>tag</i>	Specifies the tag id used to identify the DMA command.
<i>tid</i>	Specifies the transfer class identifier of the DMA command.
<i>rid</i>	Specifies the replacement class identifier of the DMA command.

**Returns**

On success, return 0. On failure, -1 is returned.

Definition at line 137 of file dma.c.

References MFC\_CMD\_PUTB.

```
{
    return spe_do_mfc_put(spectx, ls, ea, size, tag, tid, rid, MFC_CMD_PUTB);
}
```

#### 3.23.4.30 int \_base\_spe\_mfcio\_putf( spe\_context\_ptr\_t spectx, unsigned int ls, void \* ea, unsigned int size, unsigned int tag, unsigned int tid, unsigned int rid )

The \_base\_spe\_mfcio\_putf function is identical to \_base\_spe\_mfcio\_put except that it places a putf (put with fence) DMA command on the proxy command queue. The fence form ensures that this command is locally ordered with respect to all previously issued commands with the same tag group and command queue.

#### Parameters

<i>spectx</i>	Specifies the SPE context
<i>ls</i>	Specifies the starting local store destination address.
<i>ea</i>	Specifies the starting effective address source address.
<i>size</i>	Specifies the size, in bytes, to be transferred.
<i>tag</i>	Specifies the tag id used to identify the DMA command.
<i>tid</i>	Specifies the transfer class identifier of the DMA command.
<i>rid</i>	Specifies the replacement class identifier of the DMA command.

#### Returns

On success, return 0. On failure, -1 is returned.

Definition at line 148 of file dma.c.

References MFC\_CMD\_PUTF.

```
{
    return spe_do_mfc_put(spectx, ls, ea, size, tag, tid, rid, MFC_CMD_PUTF);
}
```

#### 3.23.4.31 int \_base\_spe\_mfcio\_tag\_status\_read( spe\_context\_ptr\_t spectx, unsigned int mask, unsigned int behavior, unsigned int \* tag\_status )

\_base\_spe\_mfcio\_tag\_status\_read

No Idea

Definition at line 307 of file dma.c.

References spe\_context\_base\_priv::active\_tagmask, spe\_context::base\_private, spe\_context\_base\_priv::flags, SPE\_MAP\_PS, SPE\_TAG\_ALL, SPE\_TAG\_ANY, and SPE\_TAG\_IMMEDIATE.

```
{
    if ( mask != 0 ) {
        if (!(spectx->base_private->flags & SPE_MAP_PS))
            mask = 0;
    } else {
        if ((spectx->base_private->flags & SPE_MAP_PS))
            mask = spectx->base_private->active_tagmask;
    }
}
```

```

        if (!tag_status) {
            errno = EINVAL;
            return -1;
        }

        switch (behavior) {
        case SPE_TAG_ALL:
            return spe_mfcio_tag_status_read_all(spectx, mask, tag_status);
        case SPE_TAG_ANY:
            return spe_mfcio_tag_status_read_any(spectx, mask, tag_status);
        case SPE_TAG_IMMEDIATE:
            return spe_mfcio_tag_status_read_immediate(spectx, mask, tag_status);
        default:
            errno = EINVAL;
            return -1;
        }
    }
}

```

### 3.23.4.32 int \_base\_spe\_mssync\_start( spe\_context\_ptr\_t spectx )

\_base\_spe\_mssync\_start starts Multisource Synchronisation

#### Parameters

<i>spectx</i>	Specifies the SPE context
---------------	---------------------------

Definition at line 335 of file dma.c.

References \_base\_spe\_open\_if\_closed(), spe\_context::base\_private, FD\_MSS, spe\_context\_base\_priv::flags, spe\_mssync\_area::MFC\_MSSync, spe\_context\_base\_priv::mssync\_mmap\_base, and SPE\_MAP\_PS.

```

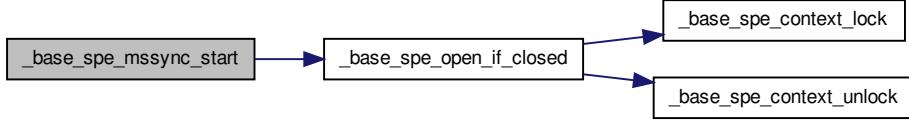
{
    int ret, fd;
    unsigned int data = 1; /* Any value can be written here */

    volatile struct spe_mssync_area *mss_area =
        spectx->base_private->mssync_mmap_base;

    if (spectx->base_private->flags & SPE_MAP_PS) {
        mss_area->MFC_MSSync = data;
        return 0;
    } else {
        fd = _base_spe_open_if_closed(spectx, FD_MSS, 0);
        if (fd != -1) {
            ret = write(fd, &data, sizeof (data));
            if ((ret < 0) && (errno != EIO)) {
                perror("spe_mssync_start: internal error");
            }
            return ret < 0 ? -1 : 0;
        } else
            return -1;
    }
}

```

Here is the call graph for this function:



#### 3.23.4.33 int `_base_spe_mssync_status ( spe_context_ptr_t spectx )`

`_base_spe_mssync_status` retrieves status of Multisource Synchronisation

##### Parameters

<code>spectx</code>	Specifies the SPE context
---------------------	---------------------------

Definition at line 359 of file dma.c.

References `_base_spe_open_if_closed()`, `spe_context::base_private`, `FD_MSS`, `spe_context_base_priv::flags`, `spe_mssync_area::MFC_MSSync`, `spe_context_base_priv::mssync_mmap_base`, and `SPE_MAP_PS`.

```

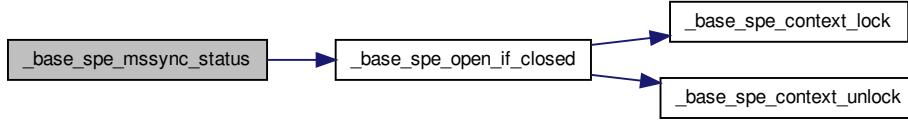
{
    int ret, fd;
    unsigned int data;

    volatile struct spe_mssync_area *mss_area =
        spectx->base_private->mssync_mmap_base;

    if (spectx->base_private->flags & SPE_MAP_PS) {
        return mss_area->MFC_MSSync;
    } else {
        fd = _base_spe_open_if_closed(spectx, FD_MSS, 0);
        if (fd != -1) {
            ret = read(fd, &data, sizeof (data));
            if ((ret < 0) && (errno != EIO)) {
                perror("spe_mssync_start: internal error");
            }
            return ret < 0 ? -1 : data;
        } else
            return -1;
    }
}

```

Here is the call graph for this function:



### 3.23.4.34 int \_base\_spe\_out\_intr\_mbox\_read ( spe\_context\_ptr\_t spectx, unsigned int mbox\_data[], int count, int behavior\_flag )

The `_base_spe_out_intr_mbox_read` function reads the contents of the SPE outbound interrupting mailbox for the SPE context.

Definition at line 255 of file mbox.c.

References `_base_spe_open_if_closed()`, `FD_IBOX`, `FD_IBOX_NB`, `SPE_MBOX_ALL_BLOCKING`, `SPE_MBOX_ANY_BLOCKING`, and `SPE_MBOX_ANY_NONBLOCKING`.

```

{
    int rc;
    int total;

    if (mbox_data == NULL || count < 1) {
        errno = EINVAL;
        return -1;
    }

    switch (behavior_flag) {
    case SPE_MBOX_ALL_BLOCKING: // read all, even if blocking
        total = rc = 0;
        while (total < 4*count) {
            rc = read(_base_spe_open_if_closed(spectx, FD_IBOX, 0),
                      (char *)mbox_data + total, 4*count - total);
            if (rc == -1) {
                break;
            }
            total += rc;
        }
        break;

    case SPE_MBOX_ANY_BLOCKING: // read at least one, even if blocking
        total = rc = read(_base_spe_open_if_closed(spectx, FD_IBOX, 0), mbox_
        data, 4*count);
        break;

    case SPE_MBOX_ANY_NONBLOCKING: // only read, if non blocking
        rc = read(_base_spe_open_if_closed(spectx, FD_IBOX_NB, 0), mbox_da
        ta, 4*count);
        if (rc == -1 && errno == EAGAIN) {
            rc = 0;
            errno = 0;
        }
        total = rc;
        break;
    }
}
  
```

```

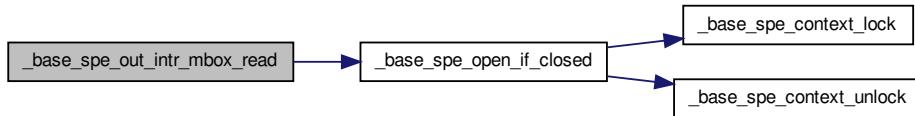
    default:
        errno = EINVAL;
        return -1;
    }

    if (rc == -1) {
        errno = EIO;
        return -1;
    }

    return rc / 4;
}

```

Here is the call graph for this function:



#### 3.23.4.35 int \_base\_spe\_out.intr.mbox.status ( spe\_context\_ptr\_t spectx )

The `_base_spe_out.intr.mbox.status` function fetches the status of the SPU outbound interrupt mailbox for the SPE thread specified by the `speid` parameter. A 0 value is returned if the mailbox is empty. A non-zero value specifies the number of 32-bit unread mailbox entries.

##### Parameters

<code>spectx</code>	Specifies the SPE context whose mailbox status is to be read.
---------------------	---

##### Returns

On success, returns the current status of the mailbox, respectively. On failure, -1 is returned.

##### See also

`spe_read_out_mbox`, `spe_write_in_mbox`, `read (2)`

Definition at line 238 of file `mbox.c`.

References `_base_spe_open_if_closed()`, `spe_context::base_private`, `spe_context_base_priv::ctl_mmap_base`, `FD_IBOX_STAT`, `spe_context_base_priv::flags`, `SPE_MAP_PS`, and `spe_spu_control_area::SPU_Mbox_Stat`.

```

{
    int rc, ret;
    volatile struct spe_spu_control_area *ctl_area =
        spectx->base_private->ctl_mmap_base;

    if (spectx->base_private->flags & SPE_MAP_PS) {
        ret = (ctl_area->SPU_Mbox_Stat >> 16) & 0xFF;
    } else {

```

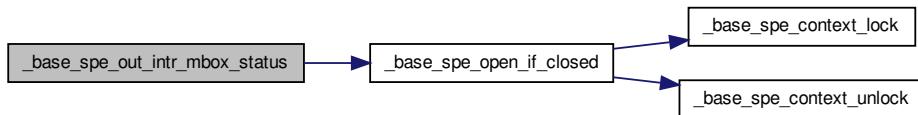
```

        rc = read(_base_spe_open_if_closed(spectx,FD_IBOX_STAT, 0), &ret,
4);
        if (rc != 4)
            ret = -1;

    }
    return ret;
}

```

Here is the call graph for this function:



### 3.23.4.36 int \_base\_spe\_out\_mbox\_read ( spe\_context\_ptr\_t spectx, unsigned int mbox\_data[], int count )

The \_base\_spe\_out\_mbox\_read function reads the contents of the SPE outbound interrupting mailbox for the SPE thread speid.

The call will not block until the read request is satisfied, but instead return up to count currently available mailbox entries.

spe\_stat\_out\_intr\_mbox can be called to ensure that data is available prior to reading the outbound interrupting mailbox.

#### Parameters

<i>spectx</i>	Specifies the SPE thread whose outbound mailbox is to be read.
<i>mbox_data</i>	
<i>count</i>	

#### Return values

>0	the number of 32-bit mailbox messages read
=0	no data available
-1	error condition and errno is set Possible values for errno: EINVAL speid is invalid Exxxx what else do we need here??

Definition at line 58 of file mbox.c.

References \_base\_spe\_open\_if\_closed(), spe\_context::base\_private, DEBUG\_PRINTF, FD\_MBOX, spe\_context\_base\_priv::flags, and SPE\_MAP\_PS.

```
{
    int rc;
```

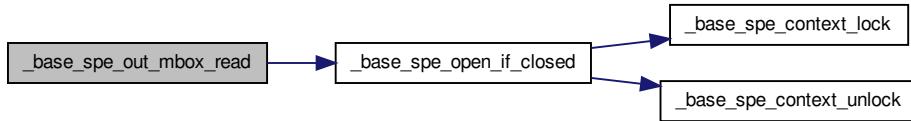
```

        if (mbox_data == NULL || count < 1) {
            errno = EINVAL;
            return -1;
        }

        if (spectx->base_private->flags & SPE_MAP_PS) {
            rc = _base_spe_out_mbox_read_ps(spectx, mbox_data, count);
        } else {
            rc = read(_base_spe_open_if_closed(spectx, FD_MBOX, 0), mbox_data,
count*4);
            DEBUG_PRINTF("%s read rc: %d\n", __FUNCTION__, rc);
            if (rc != -1) {
                rc /= 4;
            } else {
                if (errno == EAGAIN) { // no data ready to be read
                    errno = 0;
                    rc = 0;
                }
            }
        }
        return rc;
    }
}

```

Here is the call graph for this function:



#### 3.23.4.37 int \_base\_spe\_out\_mbox\_status( spe\_context\_ptr\_t spectx )

The `_base_spe_out_mbox_status` function fetches the status of the SPU outbound mailbox for the SPE thread specified by the `speid` parameter. A 0 value is returned if the mailbox is empty. A non-zero value specifies the number of 32-bit unread mailbox entries.

##### Parameters

<code>spectx</code>	Specifies the SPE context whose mailbox status is to be read.
---------------------	---

##### Returns

On success, returns the current status of the mailbox, respectively. On failure, -1 is returned.

##### See also

`spe_read_out_mbox`, `spe_write_in_mbox`, `read` (2)

Definition at line 220 of file `mbox.c`.

References `_base_spe_open_if_closed()`, `spe_context::base_private`, `spe_context_base_priv::cntl_mmap_base`, `FD_MBOX_STAT`, `spe_context_base_priv::flags`, `SPE_MAP_PS`, and `spe_spu_control_area::SPU_Mbox_Stat`.

```

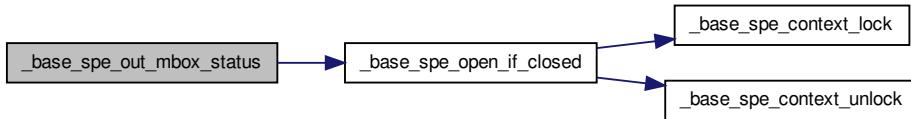
{
    int rc, ret;
    volatile struct spe_spu_control_area *cntl_area =
        spectx->base_private->cntl mmap_base;

    if (spectx->base_private->flags & SPE_MAP_PS) {
        ret = cntl_area->SPU_Mbox_Stat & 0xFF;
    } else {
        rc = read(_base_spe_open_if_closed(spectx, FD_MBOX_STAT, 0), &ret,
        4);
        if (rc != 4)
            ret = -1;
    }

    return ret;
}

```

Here is the call graph for this function:



### 3.23.4.38 int \_base\_spe\_program\_load ( spe\_context\_ptr\_t *spectx*, spe\_program\_handle\_t \* *program* )

\_base\_spe\_program\_load loads an ELF image into a context

#### Parameters

<i>spectx</i>	Specifies the SPE context
<i>program</i>	handle to the ELF image

Definition at line 203 of file load.c.

References \_base\_spe\_load\_spe\_elf(), \_base\_spe\_program\_load\_complete(), spe\_context::base\_private, DEBUG\_PRINTF, spe\_context\_base\_priv::emulated\_entry, spe\_ld\_info::entry, spe\_context\_base\_priv::entry, spe\_context\_base\_priv::flags, spe\_context\_base\_priv::loaded\_program, spe\_context\_base\_priv::mem mmap\_base, SPE\_ISOLATE, and SPE\_ISOLATE\_EMULATE.

```

{
    int rc = 0;
    struct spe_ld_info ld_info;

    spe->base_private->loaded_program = program;

    if (spe->base_private->flags & SPE_ISOLATE) {
        rc = spe_start_isolated_app(spe, program);

    } else if (spe->base_private->flags & SPE_ISOLATE_EMULATE) {
        rc = spe_start_emulated_isolated_app(spe, program, &ld_info);
    }
}

```

```

    } else {
        rc = _base_spe_load_spe_elf(program,
                                     spe->base_private->mem_mmap_base, &ld_info);
        if (!rc)
            _base_spe_program_load_complete(spe);
    }

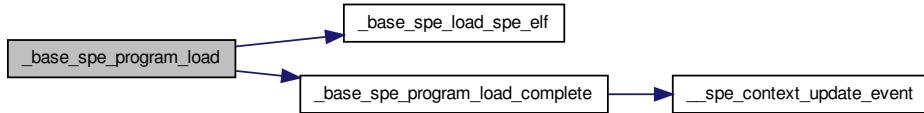
    if (rc != 0)
        DEBUG_PRINTF ("Load SPE ELF failed..\n");
    return -1;
}

spe->base_private->entry = ld_info.entry;
spe->base_private->emulated_entry = ld_info.entry;

return 0;
}

```

Here is the call graph for this function:



### 3.23.4.39 void `_base_spe_program_load_complete ( spe_context_ptr_t spectx )`

Signal that the program load has completed. For normal apps, this is called directly in the load path. For (emulated) isolated apps, the load is asynchronous, so this needs to be called when we know that the load has completed

#### Precondition

`spe->base_priv->loaded_program` is a valid SPE program

#### Parameters

<code>spectx</code>	The spe context that has been loaded.
---------------------	---------------------------------------

Register the SPE program's start address with the oprofile and gdb, by writing to the object-id file.

Definition at line 38 of file load.c.

References `_spe_context_update_event()`, `spe_context::base_private`, `DEBUG_PRINTF`, `spe_program_handle::elf_image`, `spe_context_base_priv::fd_spe_dir`, and `spe_context_base_priv::loaded_program`.

Referenced by `_base_spe_context_run()`, and `_base_spe_program_load()`.

```

{
    int objfd, len;
    char buf[20];
    spe_program_handle_t *program;

```

```

program = spectx->base_private->loaded_program;

if (!program || !program->elf_image) {
    DEBUG_PRINTF("%s called, but no program loaded\n", __func__);
    return;
}

objfd = openat(spectx->base_private->fd_spe_dir, "object-id", O_RDWR);
if (objfd < 0)
    return;

len = sprintf(buf, "%p", program->elf_image);
write(objfd, buf, len + 1);
close(objfd);

__spe_context_update_event();
}

```

Here is the call graph for this function:



#### 3.23.4.40 void\* \_base\_spe\_ps\_area\_get ( struct spe\_context \* spectx, enum ps\_area area )

\_base\_spe\_ps\_area\_get returns a pointer to the start of memory mapped problem state area

##### Parameters

<i>spectx</i>	Specifies the SPE context
<i>area</i>	specifies the area to map

#### 3.23.4.41 int \_base\_spe\_signal\_write ( spe\_context\_ptr\_t spectx, unsigned int signal\_reg, unsigned int data )

The \_base\_spe\_signal\_write function writes data to the signal notification register specified by signal\_reg for the SPE thread specified by the speid parameter.

##### Parameters

<i>spectx</i>	Specifies the SPE context whose signal register is to be written to.
<i>signal_reg</i>	Specified the signal notification register to be written. Valid signal notification registers are: SPE_SIG_NOTIFY_REG_1 SPE signal notification register 1 SPE_SIG_NOTIFY_REG_2 SPE signal notification register 2
<i>data</i>	The 32-bit data to be written to the specified signal notification register.

## Returns

On success, spe\_write\_signal returns 0. On failure, -1 is returned.

## See also

`spe_get_ps_area`, `spe_write_in_mbox`

Definition at line 307 of file mbox.c.

References `_base_spe_close_if_open()`, `_base_spe_open_if_closed()`, `spe_context::base_private`, `FD_SIG1`, `FD_SIG2`, `spe_context_base_priv::flags`, `spe_context_base_priv::signal1_mmap_base`, `spe_context_base_priv::signal2_mmap_base`, `SPE_MAP_PS`, `SPE_SIG_NOTIFY_REG_1`, `SPE_SIG_NOTIFY_REG_2`, `spe_sig_notify_1_area::SPU_Sig_Notify_1`, and `spe_sig_notify_2_area::SPU_Sig_Notify_2`.

```
{
    int rc;

    if (spectx->base_private->flags & SPE_MAP_PS) {
        if (signal_reg == SPE_SIG_NOTIFY_REG_1) {
            spe_sig_notify_1_area_t *sig = spectx->base_private->
signal1_mmap_base;

            sig->SPU_Sig_Notify_1 = data;
        } else if (signal_reg == SPE_SIG_NOTIFY_REG_2) {
            spe_sig_notify_2_area_t *sig = spectx->base_private->
signal2_mmap_base;

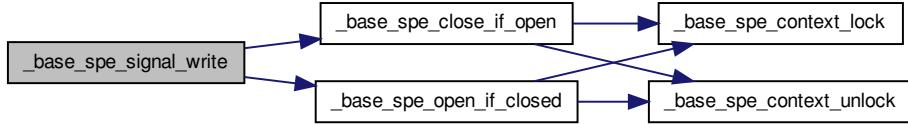
            sig->SPU_Sig_Notify_2 = data;
        } else {
            errno = EINVAL;
            return -1;
        }
        rc = 0;
    } else {
        if (signal_reg == SPE_SIG_NOTIFY_REG_1)
            rc = write(_base_spe_open_if_closed(spectx, FD_SIG1, 0), &
data, 4);
        else if (signal_reg == SPE_SIG_NOTIFY_REG_2)
            rc = write(_base_spe_open_if_closed(spectx, FD_SIG2, 0), &
data, 4);
        else {
            errno = EINVAL;
            return -1;
        }

        if (rc == 4)
            rc = 0;

        if (signal_reg == SPE_SIG_NOTIFY_REG_1)
            _base_spe_close_if_open(spectx, FD_SIG1);
        else if (signal_reg == SPE_SIG_NOTIFY_REG_2)
            _base_spe_close_if_open(spectx, FD_SIG2);
    }
}

return rc;
}
```

Here is the call graph for this function:



### 3.23.4.42 int \_base\_spe\_stop\_reason\_get( spe\_context\_ptr\_t spectx )

\_base\_spe\_stop\_reason\_get

#### Parameters

<i>spectx</i>	one thread for which to check why it was stopped
---------------	--

#### Return values

0	success - eventid and eventdata set appropriately
1	spe has not stopped after checking last, so no data was written to event
-1	an error has happened, event was not touched, errno gets set Possible values for errno: EINVAL speid is invalid Exxxx what else do we need here??

### 3.23.4.43 int \_base\_spe\_stop\_status\_get( spe\_context\_ptr\_t spectx )

\_base\_spe\_stop\_status\_get

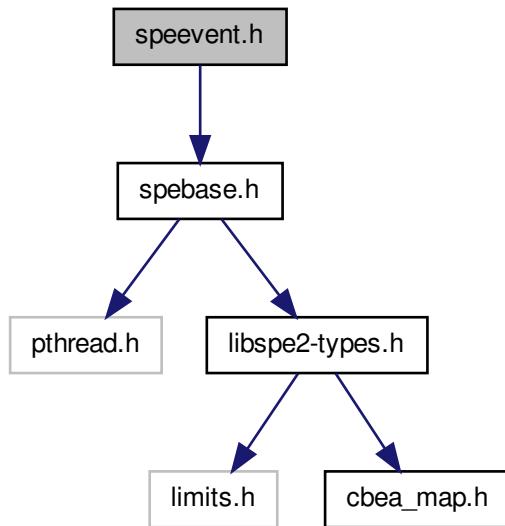
#### Parameters

<i>spectx</i>	Specifies the SPE context
---------------	---------------------------

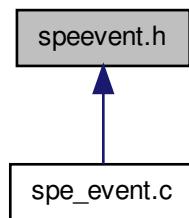
## 3.24 speevent.h File Reference

```
#include "spebase.h"
```

Include dependency graph for speevent.h:



This graph shows which files directly or indirectly include this file:



## Data Structures

- struct [spe\\_context\\_event\\_priv](#)

## Typedefs

- typedef struct [spe\\_context\\_event\\_priv](#) [spe\\_context\\_event\\_priv\\_t](#)
- typedef struct [spe\\_context\\_event\\_priv](#) \* [spe\\_context\\_event\\_priv\\_ptr\\_t](#)

## Enumerations

- enum \_\_spe\_event\_types {
   
  \_\_SPE\_EVENT\_OUT\_INTR\_MBOX, \_\_SPE\_EVENT\_IN\_MBOX, \_\_SPE\_EVENT\_TAG\_GROUP,
   
  \_\_SPE\_EVENT\_SPE\_STOPPED,
   
  \_\_NUM\_SPE\_EVENT\_TYPES }

## Functions

- int \_event\_spe\_stop\_info\_read (spe\_context\_ptr\_t spe, spe\_stop\_info\_t \*stopinfo)
- spe\_event\_handler\_ptr\_t \_event\_spe\_event\_handler\_create (void)
- int \_event\_spe\_event\_handler\_destroy (spe\_event\_handler\_ptr\_t evhandler)
- int \_event\_spe\_event\_handler\_register (spe\_event\_handler\_ptr\_t evhandler, spe\_event\_unit\_t \*event)
- int \_event\_spe\_event\_handler\_deregister (spe\_event\_handler\_ptr\_t evhandler, spe\_event\_unit\_t \*event)
- int \_event\_spe\_event\_wait (spe\_event\_handler\_ptr\_t evhandler, spe\_event\_unit\_t \*events, int max\_events, int timeout)
- int \_event\_spe\_context\_finalize (spe\_context\_ptr\_t spe)
- struct spe\_context\_event\_priv \* \_event\_spe\_context\_initialize (spe\_context\_ptr\_t spe)
- int \_event\_spe\_context\_run (spe\_context\_ptr\_t spe, unsigned int \*entry, unsigned int runflags, void \*argp, void \*envp, spe\_stop\_info\_t \*stopinfo)
- void \_event\_spe\_context\_lock (spe\_context\_ptr\_t spe)
- void \_event\_spe\_context\_unlock (spe\_context\_ptr\_t spe)

### 3.24.1 Typedef Documentation

3.24.1.1 `typedef struct spe_context_event_priv * spe_context_event_priv_ptr_t`

3.24.1.2 `typedef struct spe_context_event_priv spe_context_event_priv_t`

### 3.24.2 Enumeration Type Documentation

3.24.2.1 `enum __spe_event_types`

**Enumerator:**

```

__SPE_EVENT_OUT_INTR_MBOX
__SPE_EVENT_IN_MBOX
__SPE_EVENT_TAG_GROUP
__SPE_EVENT_SPE_STOPPED
__NUM_SPE_EVENT_TYPES
```

Definition at line 28 of file speevent.h.

```

{
__SPE_EVENT_OUT_INTR_MBOX, __SPE_EVENT_IN_MBOX,
__SPE_EVENT_TAG_GROUP, __SPE_EVENT_SPE_STOPPED,
__NUM_SPE_EVENT_TYPES
};
```

### 3.24.3 Function Documentation

#### 3.24.3.1 int \_event\_spe\_context\_finalize ( spe\_context\_ptr\_t spe )

Definition at line 416 of file spe\_event.c.

References \_\_SPE\_EVENT\_CONTEXT\_PRIV\_GET, \_\_SPE\_EVENT\_CONTEXT\_PRIV\_SET, spe\_context\_event\_priv::lock, spe\_context\_event\_priv::stop\_event\_pipe, and spe\_context\_event\_priv::stop\_event\_read\_lock.

```
{
    spe_context_event_priv_ptr_t evctx;

    if (!spe) {
        errno = ESRCH;
        return -1;
    }

    evctx = __SPE_EVENT_CONTEXT_PRIV_GET(spe);
    __SPE_EVENT_CONTEXT_PRIV_SET(spe, NULL);

    close(evctx->stop_event_pipe[0]);
    close(evctx->stop_event_pipe[1]);

    pthread_mutex_destroy(&evctx->lock);
    pthread_mutex_destroy(&evctx->stop_event_read_lock);

    free(evctx);

    return 0;
}
```

#### 3.24.3.2 struct spe\_context\_event\_priv\* \_event\_spe\_context\_initialize ( spe\_context\_ptr\_t spe ) [read]

Definition at line 439 of file spe\_event.c.

References spe\_context\_event\_priv::events, spe\_context\_event\_priv::lock, spe\_event\_unit::spe, spe\_context\_event\_priv::stop\_event\_pipe, and spe\_context\_event\_priv::stop\_event\_read\_lock.

```
{
    spe_context_event_priv_ptr_t evctx;
    int rc;
    int i;

    evctx = calloc(1, sizeof(*evctx));
    if (!evctx) {
        return NULL;
    }

    rc = pipe(evctx->stop_event_pipe);
    if (rc == -1) {
        free(evctx);
        return NULL;
    }
    rc = fcntl(evctx->stop_event_pipe[0], F_GETFL);
    if (rc != -1) {
        rc = fcntl(evctx->stop_event_pipe[0], F_SETFL, rc | O_NONBLOCK);
    }
    if (rc == -1) {
        close(evctx->stop_event_pipe[0]);
        close(evctx->stop_event_pipe[1]);
    }
}
```

```

    free(evctx);
    errno = EIO;
    return NULL;
}

for (i = 0; i < sizeof(evctx->events) / sizeof(evctx->events[0]); i++) {
    evctx->events[i].spe = spe;
}

pthread_mutex_init(&evctx->lock, NULL);
pthread_mutex_init(&evctx->stop_event_read_lock, NULL);

return evctx;
}

```

### 3.24.3.3 void \_event\_spe\_context\_lock( spe\_context\_ptr\_t spe )

Definition at line 49 of file spe\_event.c.

References \_\_SPE\_EVENT\_CONTEXT\_PRIV\_GET.

Referenced by \_event\_spe\_event\_handler\_deregister(), \_event\_spe\_event\_handler\_register(), and \_event\_spe\_event\_wait().

```

{
    pthread_mutex_lock(&__SPE_EVENT_CONTEXT_PRIV_GET(spe)->lock);
}

```

### 3.24.3.4 int \_event\_spe\_context\_run( spe\_context\_ptr\_t spe, unsigned int \* entry, unsigned int runflags, void \* argp, void \* envp, spe\_stop\_info\_t \* stopinfo )

Definition at line 477 of file spe\_event.c.

References \_\_SPE\_EVENT\_CONTEXT\_PRIV\_GET, \_base\_spe\_context\_run(), and spe\_context\_event\_priv::stop\_event\_pipe.

```

{
    spe_context_event_priv_ptr_t evctx;
    spe_stop_info_t stopinfo_buf;
    int rc;

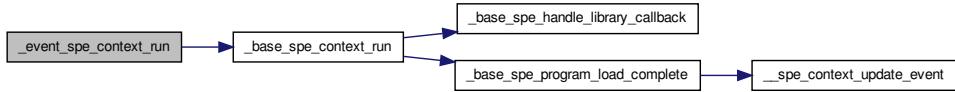
    if (!stopinfo) {
        stopinfo = &stopinfo_buf;
    }
    rc = _base_spe_context_run(spe, entry, runflags, argp, envp, stopinfo);

    evctx = __SPE_EVENT_CONTEXT_PRIV_GET(spe);
    if (write(evctx->stop_event_pipe[1], stopinfo, sizeof(*stopinfo)) != sizeof(*stopinfo)) {
        /* error check. */
    }

    return rc;
}

```

Here is the call graph for this function:



### 3.24.3.5 void \_event\_spe\_context\_unlock( spe\_context\_ptr\_t spe )

Definition at line 54 of file spe\_event.c.

References `__SPE_EVENT_CONTEXT_PRIV_GET`.

Referenced by `_event_spe_event_handler_deregister()`, `_event_spe_event_handler_register()`, and `_event_spe_event_wait()`.

```
{
    pthread_mutex_unlock(&__SPE_EVENT_CONTEXT_PRIV_GET(spe)->lock);
}
```

### 3.24.3.6 spe\_event\_handler\_ptr\_t \_event\_spe\_event\_handler\_create( void )

Definition at line 110 of file spe\_event.c.

References `__SPE_EPOLL_FD_SET`, and `__SPE_EPOLL_SIZE`.

```
{
    int epfd;
    spe_event_handler_t *evhandler;

    evhandler = calloc(1, sizeof(*evhandler));
    if (!evhandler) {
        return NULL;
    }

    epfd = epoll_create(__SPE_EPOLL_SIZE);
    if (epfd == -1) {
        free(evhandler);
        return NULL;
    }

    __SPE_EPOLL_FD_SET(evhandler, epfd);

    return evhandler;
}
```

### 3.24.3.7 int \_event\_spe\_event\_handler\_deregister( spe\_event\_handler\_ptr\_t evhandler, spe\_event\_unit\_t \* event )

Definition at line 273 of file spe\_event.c.

References \_\_base\_spe\_event\_source\_acquire(), \_\_SPE\_EPOLL\_FD\_GET, \_\_SPE\_EVENT\_ALL, \_\_SPE\_EVENT\_CONTEXT\_PRIV\_GET, \_\_SPE\_EVENT\_IN\_MBOX, \_\_SPE\_EVENT\_OUT\_INTR\_MBOX, \_\_SPE\_EVENT\_SPE\_STOPPED, \_\_SPE\_EVENT\_TAG\_GROUP, \_\_SPE\_EVENTS\_ENABLED, \_event\_spe\_context\_lock(), \_event\_spe\_context\_unlock(), spe\_context\_event\_priv::events, spe\_event\_unit::events, FD\_IBOX, FD\_MFC, FD\_WBOX, spe\_event\_unit::spe, SPE\_EVENT\_IN\_MBOX, SPE\_EVENT\_OUT\_INTR\_MBOX, SPE\_EVENT\_SPE\_STOPPED, SPE\_EVENT\_TAG\_GROUP, and spe\_context\_event\_priv::stop\_event\_pipe.

```
{
    int epfd;
    const int ep_op = EPOLL_CTL_DEL;
    spe_context_event_priv_ptr_t evctx;
    int fd;

    if (!evhandler) {
        errno = ESRCH;
        return -1;
    }
    if (!event || !event->spe) {
        errno = EINVAL;
        return -1;
    }
    if (!__SPE_EVENTS_ENABLED(event->spe)) {
        errno = ENOTSUP;
        return -1;
    }

    epfd = __SPE_EPOLL_FD_GET(evhandler);
    evctx = __SPE_EVENT_CONTEXT_PRIV_GET(event->spe);

    if (event->events & ~__SPE_EVENT_ALL) {
        errno = ENOTSUP;
        return -1;
    }

    _event_spe_context_lock(event->spe); /* for spe->event_private->events */

    if (event->events & SPE_EVENT_OUT_INTR_MBOX) {
        fd = __base_spe_event_source_acquire(event->spe, FD_IBOX);
        if (fd == -1) {
            _event_spe_context_unlock(event->spe);
            return -1;
        }
        if (epoll_ctl(epfd, ep_op, fd, NULL) == -1) {
            _event_spe_context_unlock(event->spe);
            return -1;
        }
        evctx->events[__SPE_EVENT_OUT_INTR_MBOX].events = 0;
    }

    if (event->events & SPE_EVENT_IN_MBOX) {
        fd = __base_spe_event_source_acquire(event->spe, FD_WBOX);
        if (fd == -1) {
            _event_spe_context_unlock(event->spe);
            return -1;
        }
        if (epoll_ctl(epfd, ep_op, fd, NULL) == -1) {
            _event_spe_context_unlock(event->spe);
            return -1;
        }
        evctx->events[__SPE_EVENT_IN_MBOX].events = 0;
    }

    if (event->events & SPE_EVENT_TAG_GROUP) {
        fd = __base_spe_event_source_acquire(event->spe, FD_MFC);
        if (fd == -1) {

```

```

        _event_spe_context_unlock(event->spe);
        return -1;
    }
    if (epoll_ctl(epfd, ep_op, fd, NULL) == -1) {
        _event_spe_context_unlock(event->spe);
        return -1;
    }
    evctx->events[___SPE_EVENT_TAG_GROUP].events = 0;
}

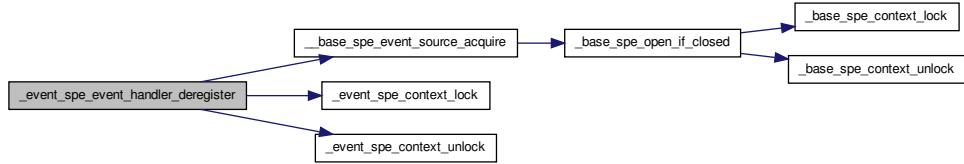
if (event->events & SPE_EVENT_SPE_STOPPED) {
    fd = evctx->stop_event_pipe[0];
    if (epoll_ctl(epfd, ep_op, fd, NULL) == -1) {
        _event_spe_context_unlock(event->spe);
        return -1;
    }
    evctx->events[___SPE_EVENT_SPE_STOPPED].events = 0;
}

_event_spe_context_unlock(event->spe);

return 0;
}

```

Here is the call graph for this function:



### 3.24.3.8 int \_event\_spe\_event\_handler\_destroy ( spe\_event\_handler\_ptr\_t evhandler )

Definition at line 135 of file spe\_event.c.

References \_\_\_SPE\_EPOLL\_FD\_GET.

```

{
    int epfd;

    if (!evhandler) {
        errno = ESRCH;
        return -1;
    }

    epfd = ___SPE_EPOLL_FD_GET(evhandler);
    close(epfd);

    free(evhandler);
    return 0;
}

```

### 3.24.3.9 int \_event\_spe\_event\_handler\_register ( spe\_event\_handler\_ptr\_t evhandler, spe\_event\_unit\_t \* event )

Definition at line 155 of file spe\_event.c.

References \_\_base\_spe\_event\_source\_acquire(), \_\_SPE\_EPOLL\_FD\_GET, \_\_SPE\_EVENT\_ALL, \_\_SPE\_EVENT\_CONTEXT\_PRIV\_GET, \_\_SPE\_EVENT\_IN\_MBOX, \_\_SPE\_EVENT\_OUT\_INTR\_MBOX, \_\_SPE\_EVENT\_SPE\_STOPPED, \_\_SPE\_EVENT\_TAG\_GROUP, \_\_SPE\_EVENTS\_ENABLED, \_event\_spe\_context\_lock(), \_event\_spe\_context\_unlock(), spe\_context::base\_private, spe\_event\_unit::data, spe\_context\_event\_priv::events, spe\_event\_unit::events, FD\_IBOX, FD\_MFC, FD\_WBOX, spe\_context\_base\_priv::flags, spe\_event\_data::ptr, spe\_event\_unit::spe, SPE\_EVENT\_IN\_MBOX, SPE\_EVENT\_OUT\_INTR\_MBOX, SPE\_EVENT\_SPE\_STOPPED, SPE\_EVENT\_TAG\_GROUP, SPE\_MAP\_PS, and spe\_context\_event\_priv::stop\_event\_pipe.

```
{
    int epfd;
    const int ep_op = EPOLL_CTL_ADD;
    spe_context_event_priv_ptr_t evctx;
    spe_event_unit_t *ev_buf;
    struct epoll_event ep_event;
    int fd;

    if (!evhandler) {
        errno = ESRCH;
        return -1;
    }
    if (!event || !event->spe) {
        errno = EINVAL;
        return -1;
    }
    if (!__SPE_EVENTS_ENABLED(event->spe)) {
        errno = ENOTSUP;
        return -1;
    }

    epfd = __SPE_EPOLL_FD_GET(evhandler);
    evctx = __SPE_EVENT_CONTEXT_PRIV_GET(event->spe);

    if (event->events & ~__SPE_EVENT_ALL) {
        errno = ENOTSUP;
        return -1;
    }

    _event_spe_context_lock(event->spe); /* for spe->event_private->events */

    if (event->events & SPE_EVENT_OUT_INTR_MBOX) {
        fd = __base_spe_event_source_acquire(event->spe, FD_IBOX);
        if (fd == -1) {
            _event_spe_context_unlock(event->spe);
            return -1;
        }

        ev_buf = &evctx->events[__SPE_EVENT_OUT_INTR_MBOX];
        ev_buf->events = SPE_EVENT_OUT_INTR_MBOX;
        ev_buf->data = event->data;

        ep_event.events = EPOLLIN;
        ep_event.data.ptr = ev_buf;
        if (epoll_ctl(epfd, ep_op, fd, &ep_event) == -1) {
            _event_spe_context_unlock(event->spe);
            return -1;
        }
    }

    if (event->events & SPE_EVENT_IN_MBOX) {
```

```
fd = __base_spe_event_source_acquire(event->spe, FD_WBOX);
if (fd == -1) {
    _event_spe_context_unlock(event->spe);
    return -1;
}

ev_buf = &evctx->events[___SPE_EVENT_IN_MBOX];
ev_buf->events = SPE_EVENT_IN_MBOX;
ev_buf->data = event->data;

ep_event.events = EPOLLOUT;
ep_event.data.ptr = ev_buf;
if (epoll_ctl(epfd, ep_op, fd, &ep_event) == -1) {
    _event_spe_context_unlock(event->spe);
    return -1;
}
}

if (event->events & SPE_EVENT_TAG_GROUP) {
    fd = __base_spe_event_source_acquire(event->spe, FD_MFC);
    if (fd == -1) {
        _event_spe_context_unlock(event->spe);
        return -1;
    }

    if (event->spe->base_private->flags & SPE_MAP_PS) {
        _event_spe_context_unlock(event->spe);
        errno = ENOTSUP;
        return -1;
    }

    ev_buf = &evctx->events[___SPE_EVENT_TAG_GROUP];
    ev_buf->events = SPE_EVENT_TAG_GROUP;
    ev_buf->data = event->data;

    ep_event.events = EPOLLIN;
    ep_event.data.ptr = ev_buf;
    if (epoll_ctl(epfd, ep_op, fd, &ep_event) == -1) {
        _event_spe_context_unlock(event->spe);
        return -1;
    }
}

if (event->events & SPE_EVENT_SPE_STOPPED) {
    fd = evctx->stop_event_pipe[0];

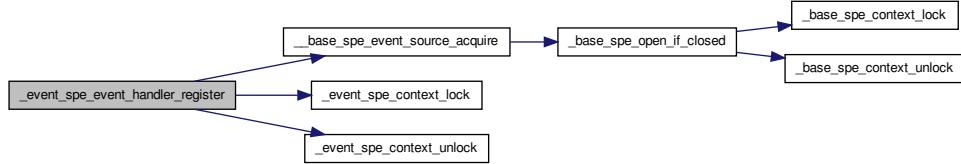
    ev_buf = &evctx->events[___SPE_EVENT_SPE_STOPPED];
    ev_buf->events = SPE_EVENT_SPE_STOPPED;
    ev_buf->data = event->data;

    ep_event.events = EPOLLIN;
    ep_event.data.ptr = ev_buf;
    if (epoll_ctl(epfd, ep_op, fd, &ep_event) == -1) {
        _event_spe_context_unlock(event->spe);
        return -1;
    }
}

_event_spe_context_unlock(event->spe);

return 0;
}
```

Here is the call graph for this function:



### 3.24.3.10 int \_event\_spe\_event\_wait( spe\_event\_handler\_ptr\_t evhandler, spe\_event\_unit\_t \* events, int max\_events, int timeout )

Definition at line 360 of file spe\_event.c.

References `__SPE_EPOLL_FD_GET`, `_event_spe_context_lock()`, `_event_spe_context_unlock()`, and `spe_event_unit::spe`.

```

{
    int epfd;
    struct epoll_event *ep_events;
    int rc;

    if (!evhandler) {
        errno = ESRCH;
        return -1;
    }
    if (!events || max_events <= 0) {
        errno = EINVAL;
        return -1;
    }

    epfd = __SPE_EPOLL_FD_GET(evhandler);

    ep_events = malloc(sizeof(*ep_events) * max_events);
    if (!ep_events) {
        return -1;
    }

    for ( ; ; ) {
        rc = epoll_wait(epfd, ep_events, max_events, timeout);
        if (rc == -1) { /* error */
            if (errno == EINTR) {
                if (timeout >= 0) { /* behave as timeout */
                    rc = 0;
                    break;
                }
                /* else retry */
            }
            else {
                break;
            }
        }
        else if (rc > 0) {
            int i;
            for (i = 0; i < rc; i++) {
                spe_event_unit_t *ev = (spe_event_unit_t *) (ep_events[i].data.ptr);
                _event_spe_context_lock(ev->spe); /* lock ev itself */
            }
        }
    }
}

```

```

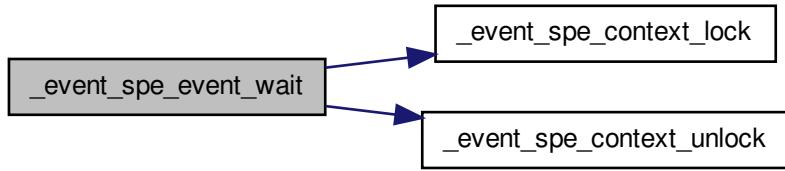
        events[i] = *ev;
        _event_spe_context_unlock(ev->spe);
    }
    break;
}
else /* timeout */
{
    break;
}
}

free(ep_events);

return rc;
}

```

Here is the call graph for this function:



### 3.24.3.11 int \_event\_spe\_stop\_info\_read ( spe\_context\_ptr\_t spe, spe\_stop\_info\_t \* stopinfo )

Definition at line 59 of file spe\_event.c.

References `__SPE_EVENT_CONTEXT_PRIV_GET`, `spe_context_event_priv::stop_event_pipe`, and `spe_context_event_priv::stop_event_read_lock`.

```

{
    spe_context_event_priv_ptr_t evctx;
    int rc;
    int fd;
    size_t total;

    evctx = __SPE_EVENT_CONTEXT_PRIV_GET(spe);
    fd = evctx->stop_event_pipe[0];

    pthread_mutex_lock(&evctx->stop_event_read_lock); /* for atomic read */

    rc = read(fd, stopinfo, sizeof(*stopinfo));
    if (rc == -1) {
        pthread_mutex_unlock(&evctx->stop_event_read_lock);
        return -1;
    }

    total = rc;
    while (total < sizeof(*stopinfo)) { /* this loop will be executed in few cases
        */
        struct pollfd fds;

```

```
fds.fd = fd;
fds.events = POLLIN;
rc = poll(&fds, 1, -1);
if (rc == -1) {
    if (errno != EINTR) {
        break;
    }
}
else if (rc == 1) {
    rc = read(fd, (char *)stopinfo + total, sizeof(*stopinfo) - total);
    if (rc == -1) {
        if (errno != EAGAIN) {
            break;
        }
    }
    else {
        total += rc;
    }
}
pthread_mutex_unlock(&evctx->stop_event_read_lock);

return rc == -1 ? -1 : 0;
}
```

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